NOTE: UNLESS OTHERWISE SPECIFIED

1. RESISTANCE VALUES ARE IN OHMS, 1/4W ±5%.
2. CAPACITOR VALUES ARE IN MICROFARADS.

REV.	ZONE	ECO #	REVISION	DATE	APPD
B	—	B084	INITIAL RELEASE		—
C	2D	B111	U1 WAS LS37, ADDED C1-6,8&9		
D	2D 42	B138	R14 WAS 220±5%, ADDED 1/4W ±5% TO NOTE 1		
E		B168	MADE ALL GND SYMBOLS IDENTICAL. LABELED L10 & L11, J9 PIN 83 WAS BAT, ADDED PINS 9 & 12 TO RP2 RP2 WERE PINS 3 & 18 ON RP1, J2 PINS R & P WERE AGND, C12 WAS 100μF		
F	4A	B500	ADDED R16-29.		

J1 FLOPPY DISK/KYBD — COMP SIDE / CIRCUIT SIDE

J3 MEM 2

J4 MEM 1

J5 SLOT 1 — J6 SLOT 2 — J7 SLOT 3

J2 VIDEO/POWER — COMP SIDE / CIRCUIT SIDE

J8 CPU

J9 I/O

R16-29, 330Ω

C1-6 } .001
C8-9 } CAPS

J13 HARD DISK

J14 MOUSE

J15 RS232A

J16 RS232B

METRIC	apple computer inc.		
DRFT R BOWERS 3/30/82	DRFT CK J. FITCH 5/7/82	TITLE	SCHEMATIC, MOTHERBOARD LISA 1
ENG APPL J. FITCH 5/7/82	MFG APPL — / —		
RELEASE — / —	ORIG DIV P.O.S.		
DESIGNER J. FITCH	SCALE —	SIZE D	DRAWING NUMBER 050-4013-F SHT 1/1

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