

NOTES:
1. DATE: 10-1-81
2. FILE: 3-1-81
3. DO: NC-1-81

STEP 1: Y-PLATE
STEP 2: X-PLATE
STEP 3: THERM
STEP 4: THERM

EUJAL0002-0-0

CHANGE NO	REV	DATE	BY
1	1	10-1-81	...
2	1	10-1-81	...
3	1	10-1-81	...
4	1	10-1-81	...

DATE	BY
10-1-81	...

DATE	BY
10-1-81	...

DATE	BY
10-1-81	...

DATE	BY
10-1-81	...

digital

D. P. M.

EUJAL0002-0-0

REVISION HISTORY
NO. DATE BY DESCRIPTION

ECO #2 REWORK INSTRUCTIONS
W/E ADDS SIDE 1
2-2 E92-4 TO E116-4

J
H
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E
D
C
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J
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A

0-0-20007

REVISION HISTORY
NO. DATE BY DESCRIPTION

D.P.M.

DOCUMENT NUMBER
EUA L0002-0-0 E

SCALE 2/1 SHEET 2 OF 2

8 7 6 5 4 3 2 1

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION	REFERENCE DESIGNATOR
				00	
1	1	E-MD-5013555-0-0	5013555-00	ETCHED CIRCUIT BOARD	1
2	2	SEE NOTES	1012084-01	8 MFD 25V +75-10% AL EL	6
3	3		1012784-00	.047 MFD 50V +80-20% CER	73
4	4		1210711-02	/REPLACED BY 12-16988-02	1
5	5		1215924-00	SOCKET IC W/METAL CONT	22
					CONT XE59-XE60, XE69-XE70, XE83-XE85, XE90-XE92, XE97, XE98, XE107, XE108, XE117, XE118, XE127, XE128, XE133-XE136
6	6		1300005-04	R NETWORK 15-470 5.0 % 16PIN	2
7	7		1300365-00	1.0 K .25 W 5.0 % CC	1
8	8		1301890-00	560.0 .25 W 5.0 % CC	2
9	9		1811660-21	*** THIS ITEM IS NOT USED ***	-
10	10		1910532-00	74S00 NAND GATE-QUAD 2IN	5
11	11		1910533-00	74S03 NAND GATE-QUAD 2IN,0	1
12	12		1910534-00	74S04 INVERTER GATE-HEX 11	4
13	13		1910535-00	74S05 INVERTER GATE-HEX 1	1
14	14		1910536-00	74S10 NAND GATE-TRIPLE 3IN	4
15	15		1910539-00	74S20 NAND GATE-DUAL 4INPU	1
16	16		1910540-00	74S22 NAND GATE-DUAL 4INPU	1
17	17		1910544-00	74S74 FF-D DUAL,EDGE TRIGG	1
18	18		1910545-00	74S112 FF-JK DUAL,EDGE TRIG	1
19	19		1910547-00	74S153 MUX 1 OF 4 (DUAL)	2
20	20		1910548-00	74S157 MUX 1 OF 2 (QUAD)	1
21	21		1910552-00	74S194 SHIFT REG.,4BIT RIGH	1
22	22		1910956-00	74S151 MUX 1 OF 8	9
					CONT E17,E24,E31,E52,E35 E42 E4,E5,E55,E57 E68 E5,E49,E63,E75 E62 E22 E3 E56 E34,E64 E6 E30 E18,E32,E33,E46,E54,E66,E67,E76, E77 E12,E16,E28,E47,E74,E82,E87,E88 E48 E43 E36-E39
23	23		1911573-00	74S280 PARITY GEN/CHKR,9BIT	8
24	24		1911675-00	74S138 DECODER/DEMUX 3-8 LIN	1
25	25		1912389-00	74S08 AND GATE-QUAD 2IN,PO	1
26	26		1912586-00	DM 85S68N REGISTER,64BIT EDGE	4

REVISION HISTORY		BASIC PART NO: L0002		DRN:	D. SIREEN	DATE:	19-OCT-79	D I G I T A L				
ENG	ECO NUMBER	REV	SECTION A OF A	CHK'D:	E.T. GERRY	DATE:	19-OCT-79	TITLE PARTS LIST				
								D.P.M.				
								DOCUMENT NUMBER				
								SIZE	CODE	NUMBER	REV	
								K	PL	L0002-0-DBF	C	
								ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:	FILE NAME:	EDIT #
								E-UA-L0002-0-0		B-DD-L0002-0-0	Z1257.PLS	14

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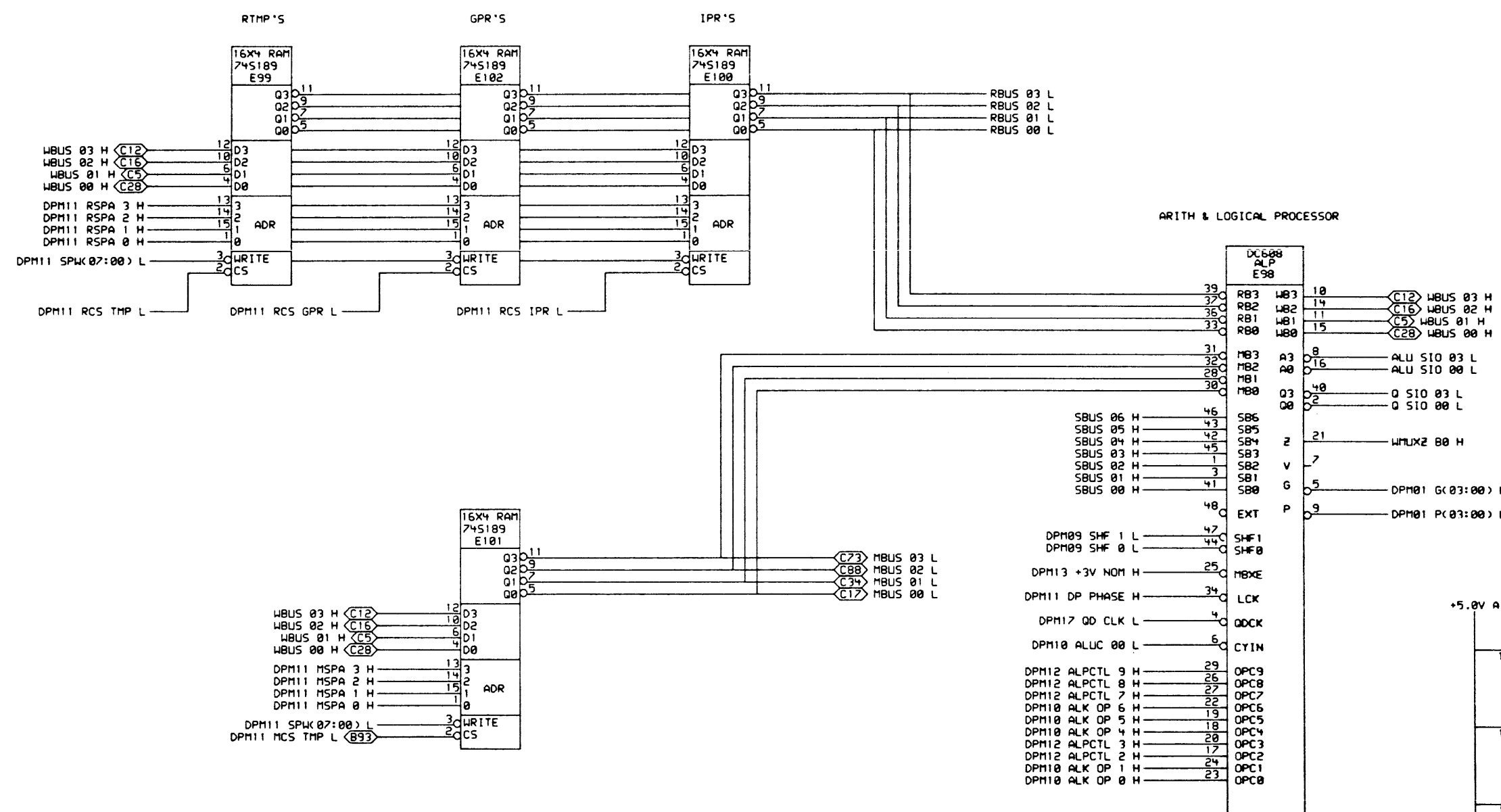
TWJ

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION 00	REFERENCE DESIGNATOR
27	27		1912661-00	74S189 MEMORY READ/WRITE	32	E93-E96,E99-E106,E109-E116, CONT E119-E126,E129-E132
28	28		1912746-00	DEC 74S37 NAND GATE-QUAD 2IN	2	E53,E65
29	29		1913462-00	74S240 OCTAL BUFFER,INVERTI	1	E40
30	30		1913493-00	74S241 OCTAL BUFFER,TRI-STA	1	E41
31	31		1913671-00	74S374 FF-D OCTAL TRISTATE	8	E14,E26,E29,E44,E79-E81,E86
32	32		1913839-00	74LS165 SHIFT REG.,8BIT	2	E50,E58
33	33		1914085-00	74S260 NOR GATE-DUAL,POS	1	E23
34	34		1914214-00	LS374 FF-D OCTAL EDGE TRIG	5	E13,E71-E73,E78
35	35		1914694-00	DC 620A BIPOLAR,LS,400-GATE	1	E92
36	36		1914682-00	DC 608B BIPOLAR,LS,400-GATE	8	E97,E98,E107,E108,E117,E118, CONT E127,E128
37	37		1914684-00	DC 610B BIPOLAR,LS,400-GATE	1	E70
38	38		1914686-00	DC 612B BIPOLAR,LS,400-GATE	1	E91
39	39		1914687-00	DC 613B BIPOLAR,LS,400-GATE	4	E133-E136
40	40		1914688-00	DC 614C BIPOLAR,LS,400-GATE	1	E84
41	41		1914689-00	DC 615B BIPOLAR,LS,400-GATE	1	E90
42	42		1914690-00	DC 616C BIPOLAR,LS,400-GATE	1	E85
43	43		1914691-00	DC 617C BIPOLAR,LS,400-GATE	1	E69
44	44		1914695-00	DC 621C BIPOLAR,LS,400-GATE	1	E59
45	45		1914696-00	DC 622B BIPOLAR,LS,400-GATE	1	E83
46	46		1914703-00	DC 629C BIPOLAR,LS,400-GATE	1	E60
47	47		23553A2-00	A2-05	1	E51
48	48		23904A9-00	A9-01	1	E15
49	49		23618F1-00	F1-01	1	E20
50	50		23619F1-00	F1-02	1	E21
51	51		23021F2-00	F2-01	1	E7
52	52		23022F2-00	F2-01	1	E25
53	53		23023F2-00	F2-01	1	E27
54	54		23024F2-00	F2-02	1	E8
55	55		23025F2-00	F2-02	1	E9
56	56		23026F2-00	F2-02	1	E10
57	57		23027F2-00	F2-02	1	E11
58	58		9000024-01	EYELET, ROLLED FLANGE, .121 OD X	12	
59	59		1302379-00	75.0 .25 W 5.0 % CC	6	R5-R9,R11
60	60		1503121-00	2N 2369 NPN 350MW SI N	2	Q2,Q3
61	61		1912388-00	74S02 NOR GATE-QUAD 2IN,PO	1	E2
62	62		1215006-03	SOCKET 18PIN IC LOW PROFILE	9	XE7-XE11,XE20,XE21,XE25,XE27
63	63		1215935-00	GASKET, THERMAL .50"X.80"	22	
64	64		1215936-00	HEAT SINK, FORCED CONVECTION	22	
65	65		1305125-00	383.0 .25 W 1.0 % RN55D-F10	1	R12

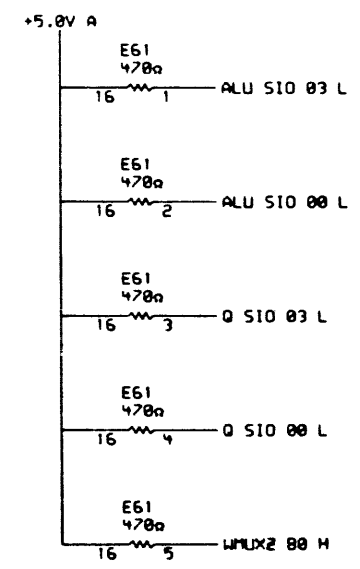
66 NOTE: SOME MODULES WILL HAVE 10-05306 INSTEAD OF 10-12084-01

! D ! I ! G ! I ! T ! A ! L !	! TITLE	! SECTION A OF A	! SIZE ! CODE !	! DOCUMENT NUMBER	! REV !
! ! ! ! ! ! ! !	D.P.M.		K ! PL	L0002-0-DBF	C

TW



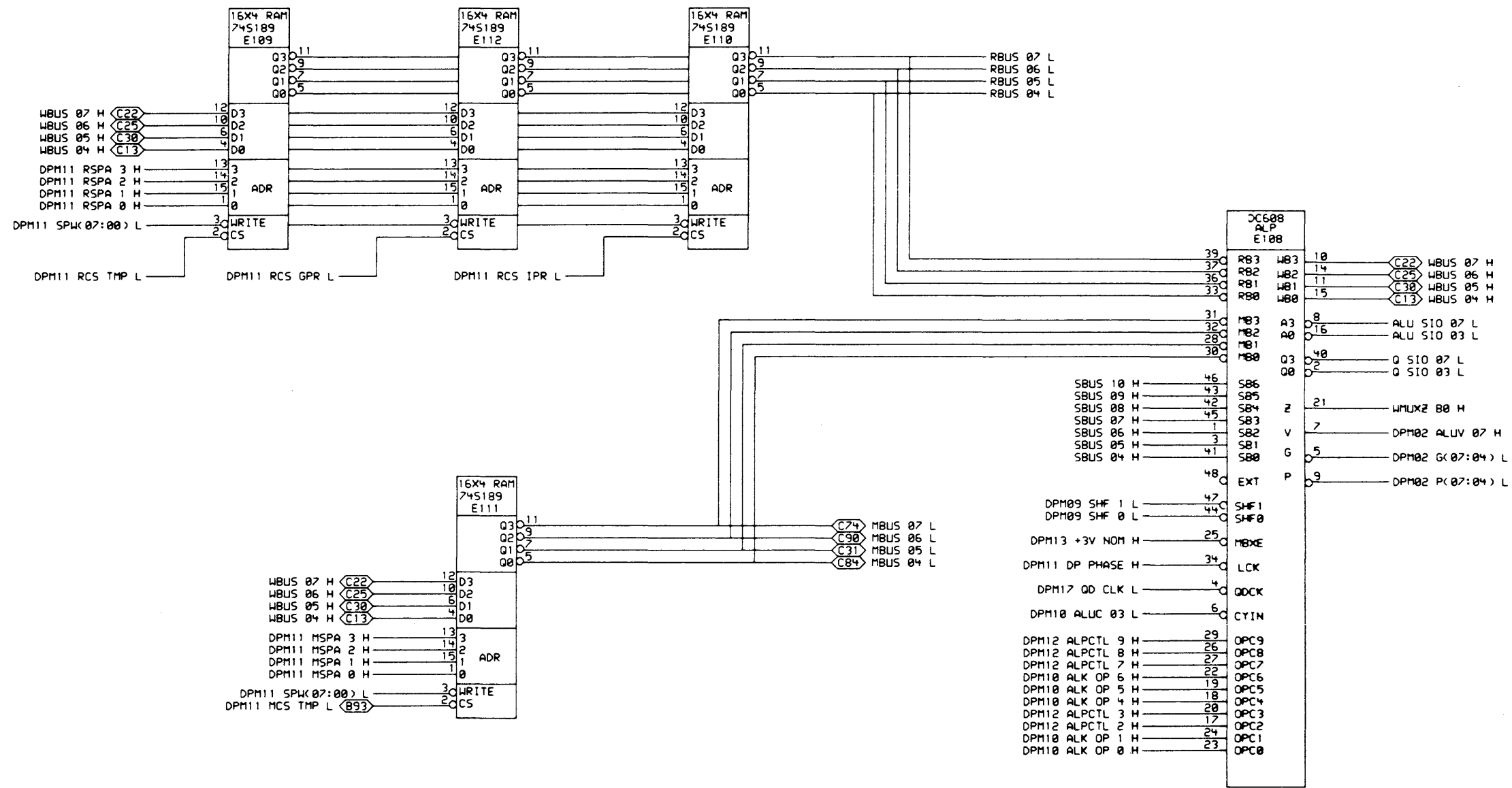
- NOTES:
- SPLIT VCC IS SUPPLIED TO I.C.'S AS FOLLOWS:
 - +5.0V A (A20, A38, A58, A76, B20): E1-E52, E55-E62, E65-E69, E83
 - +5.0V B (B38, B58, B76): E53, E54, E63, E64, E70-E82, E84-E92
 - +5.0V C (C20, C38, C58, C76): E93-E136
 - ALL RESISTORS 1/4W 5% UNLESS OTHERWISE SPECIFIED.
 - POWER, GND DECOUPLING CAPS ARE LOCATED ON SHEET DPM21.



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REVISIONS		
CHK	CHANGE NO.	REV

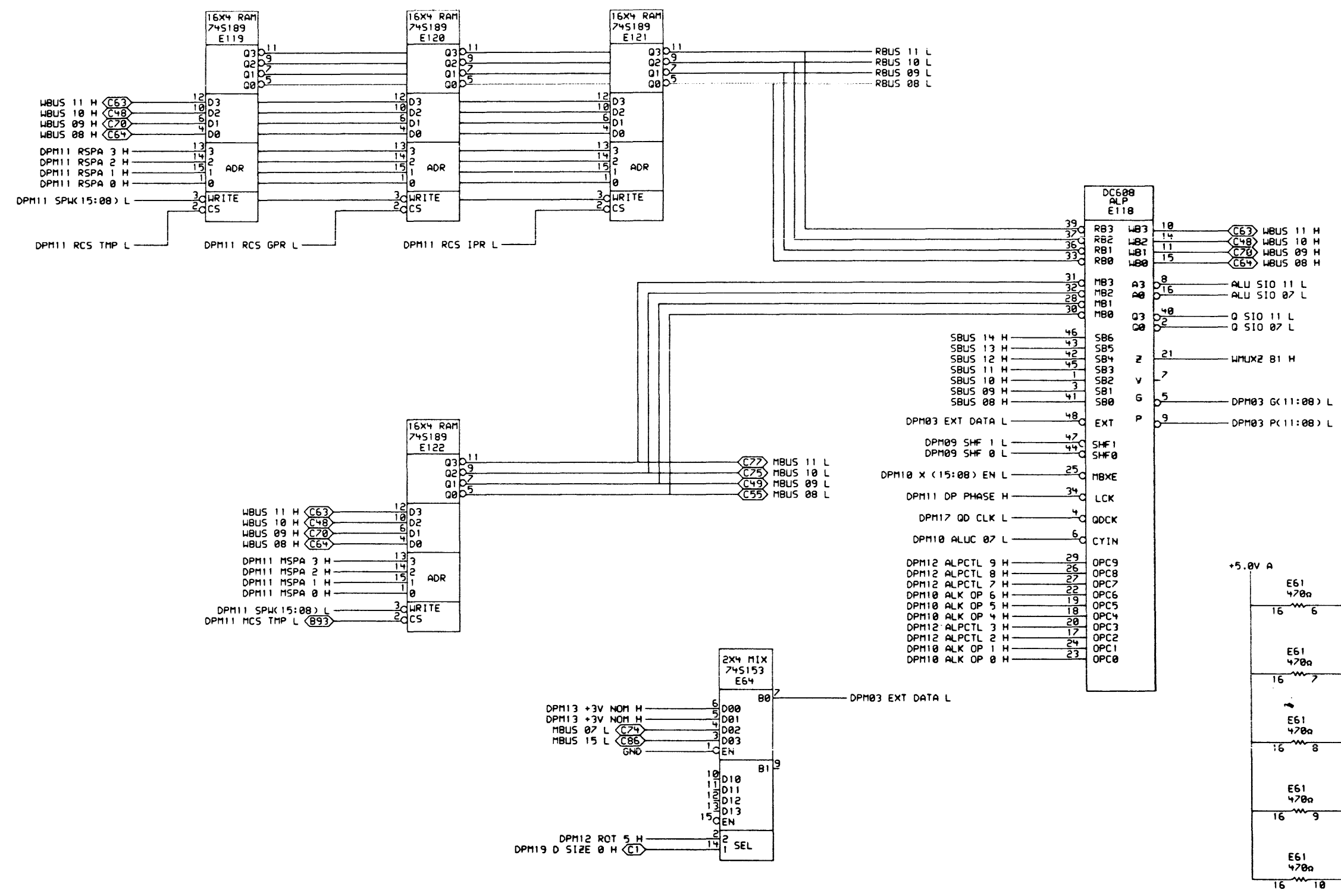
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	CHK'D.	DATE	BOARD LOCATION: AC2	SHEET	DATA PATH (03:00)
[160,1271]DPM01.DRW		24-MAR-80 09:10	NEXT HIGHER ASSEMBLY:	SIZE CODE	NUMBER
FIRST USED ON OPTION/MODEL: 11/250		B-DD-L0002-0-0	D-C	L0002-0-1	REV. B



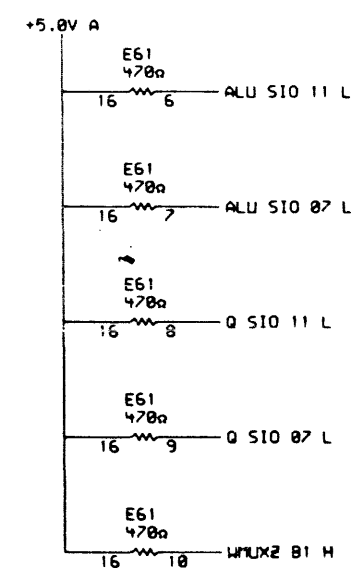
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REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN. <i>A. P. ...</i>	DATE 26-MAR-88	ENG.	DATE	TITLE: DPM02 DATA PATH (07:04)
	CHK'D.	DATE 17-FEB-88 14:32	BOARD LOCATION: AC2	SHEET 1 OF 1	SIZE CODE NUMBER REV. D CS L0002-0-2 B
FIRST USED ON OPTION/MODEL: 11/750		NEXT HIGHER ASSEMBLY: B-DD-L0002-0-0			



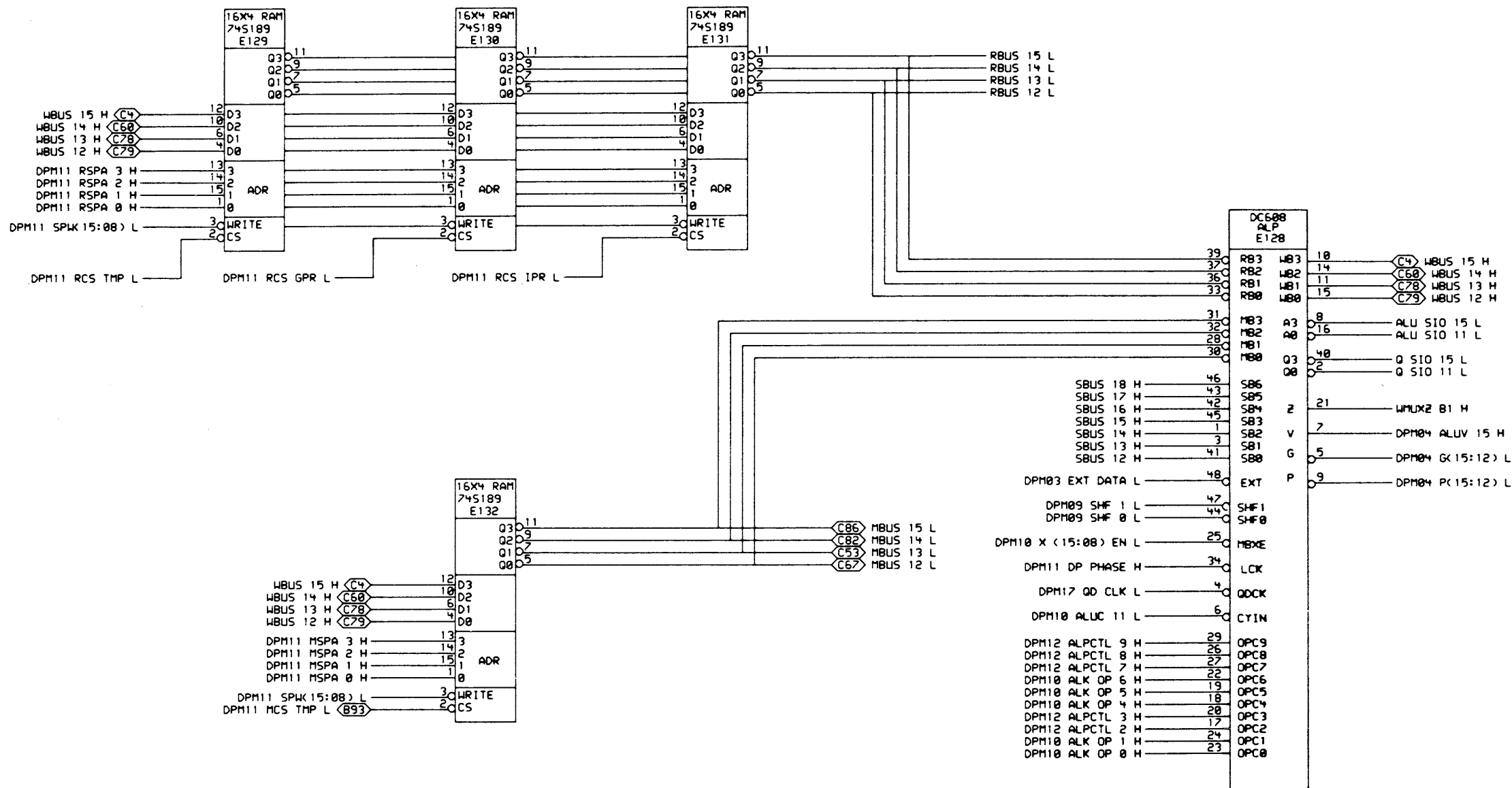
- SBUS 14 H 46
- SBUS 13 H 43
- SBUS 12 H 42
- SBUS 11 H 45
- SBUS 10 H 1
- SBUS 09 H 3
- SBUS 08 H 41
- S00
- S01
- S02
- S03
- S04
- S05
- S06
- EXT
- SHF1
- SHF0
- MBXE
- LCK
- QDCK
- CYIN
- OPC9
- OPC8
- OPC7
- OPC6
- OPC5
- OPC4
- OPC3
- OPC2
- OPC1
- OPC0



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REVISIONS	
CHK	CHANGE NO. REV

digital	DRN. A. Perrin	DATE 26-MAR-80	ENG.	DATE	TITLE: DPM03 DATA PATH (11:08)
	CHK'D.	DATE 17-FEB-80 14:35	BOARD LOCATION: AC2	SHEET 1 OF 1	
FIRST USED ON OPTION MODEL: 117750		NEXT HIGHER ASSEMBLY: B-DD-L0002-0-0		SIZE CODE D CS	NUMBER L0002-0-3



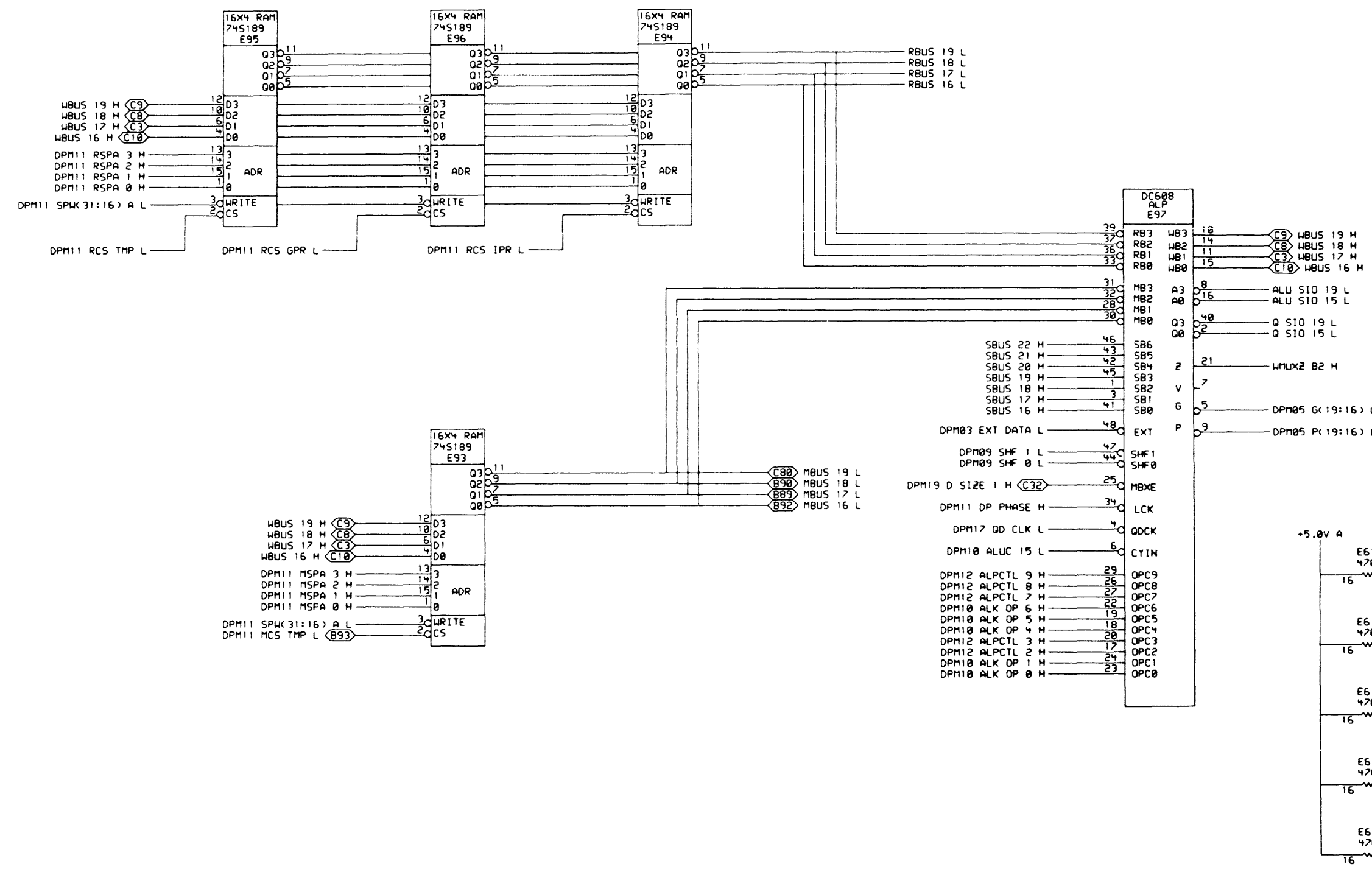
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REVISIONS		
CHK	CHANGE NO.	REV

digital
 DRN. A. Perrin
 CHK'D.
 DATE 26-FEB-80
 DATE 17-FEB-80 14:38
 FIRST USED ON OPTION/MODEL: 11/750

ENG. DATE
 BOARD LOCATION: AC2
 SHEET 1 OF 1
 NEXT HIGHER ASSEMBLY: B-DD-L0002-0-0

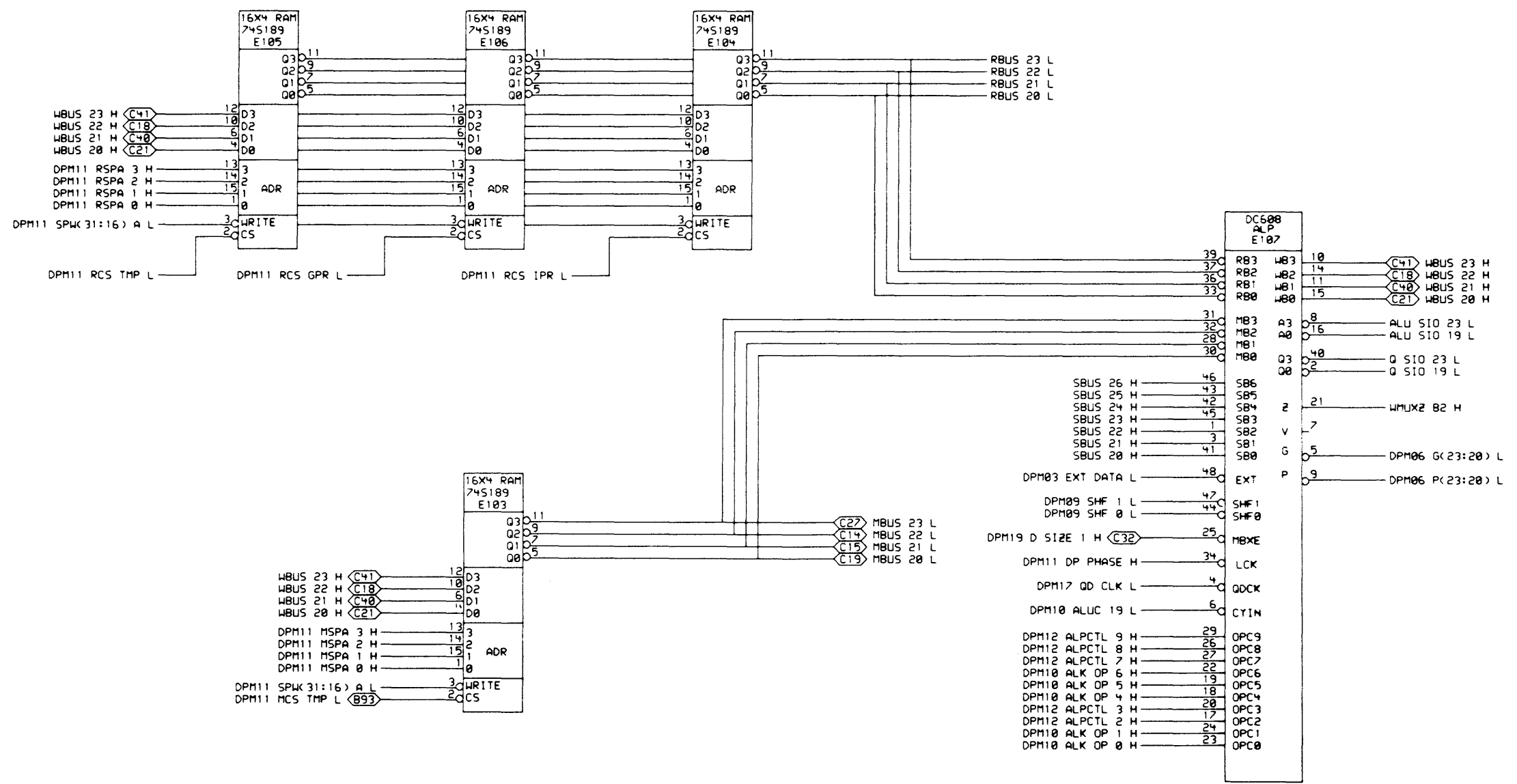
TITLE: DPM04
 DATA PATH (15:12)
 SIZE CODE NUMBER REV.
 D CS L0002-0-4 B



digital	DRN. A. Perrin	DATE 26-MAR-88	ENG.	DATE	TITLE: DPM05
	CHK'D.	DATE 17-FEB-88 14:41	BOARD LOCATION: AC2	SHEET 1 OF 1	DATA PATH (19:16)
FIRST USED ON OPTION/MODEL: 117750		NEXT HIGHER ASSEMBLY: B-DD-L0002-0-0		SIZE CODE D CS	NUMBER L0002-0-5
				REV. B	

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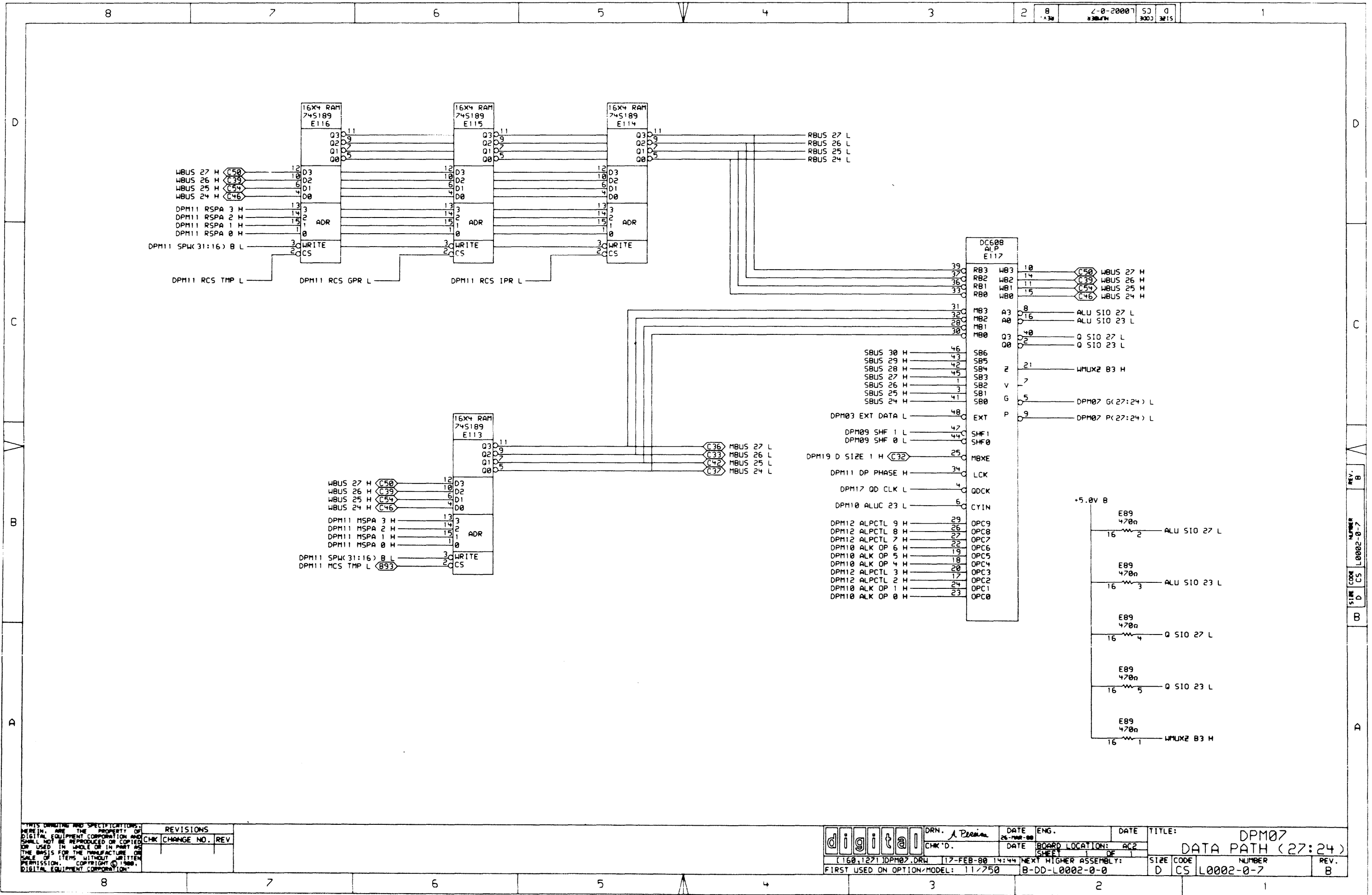
REVISIONS		
CHK	CHANGE NO.	REV



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REVISIONS		
CHK	CHANGE NO.	REV

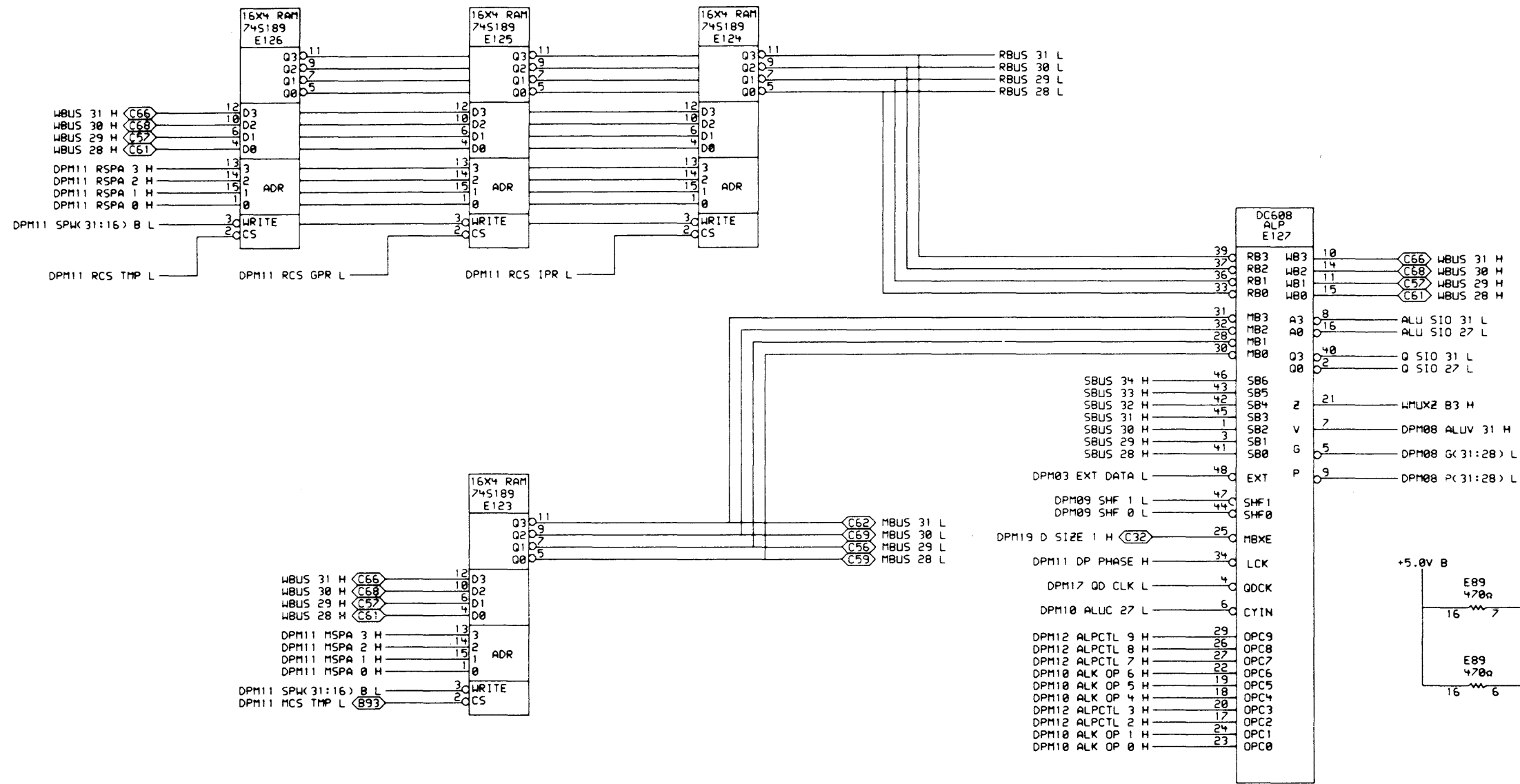
digital	DRN. <i>A. Pagan</i>	DATE ENG. 26-1002-88	DATE	TITLE: DPM06
	CHK'D.	DATE BOARD LOCATION: AC2		DATA PATH (23:20)
[160,1271]DPM06.DRW 17-FEB-88 14:43		NEXT HIGHER ASSEMBLY:	SIZE CODE	NUMBER
FIRST USED ON OPTION/MODEL: 11/750		B-DD-L0002-0-0	D CS	L0002-0-6
			REV. B	



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REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN. A. Plesman	DATE 26-FEB-80	ENG.	DATE	TITLE: DPM07
	CHK'D.	DATE	BOARD LOCATION: AC2	SHEET 1 OF 1	DATA PATH (27:24)
[160,127] DPM07.DRW [17-FEB-80 14:44]		NEXT HIGHER ASSEMBLY:		SIZE CODE	NUMBER
FIRST USED ON OPTION/MODEL: 11/750		B-DD-L0002-0-0		D CS	L0002-0-7
				REV. B	



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REVISIONS		
CHK	CHANGE NO.	REV.

DATE	ENG.	DATE	TITLE:
17-FEB-80	A. P. P.	26-MAR-80	DPM08 DATA PATH (31:28)
17-FEB-80			

FILE	CODE	NUMBER	REV.
D	CS	L0002-0-8	B

ROTATOR CHIP SLICES

D

D

C

C

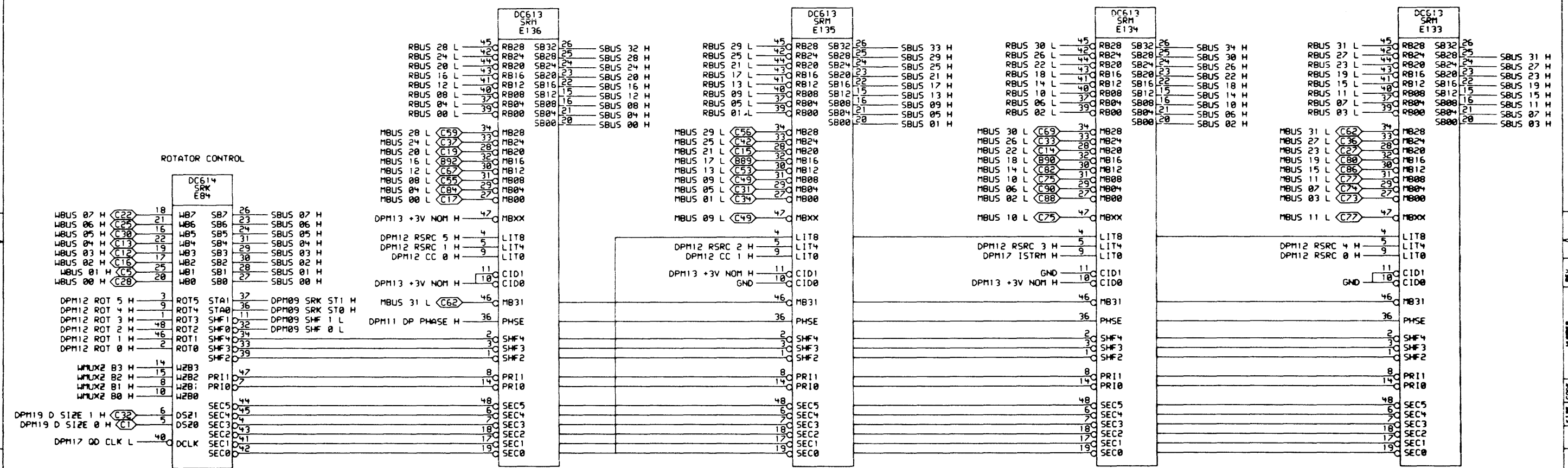
B

REV. B

NUMBER

CD

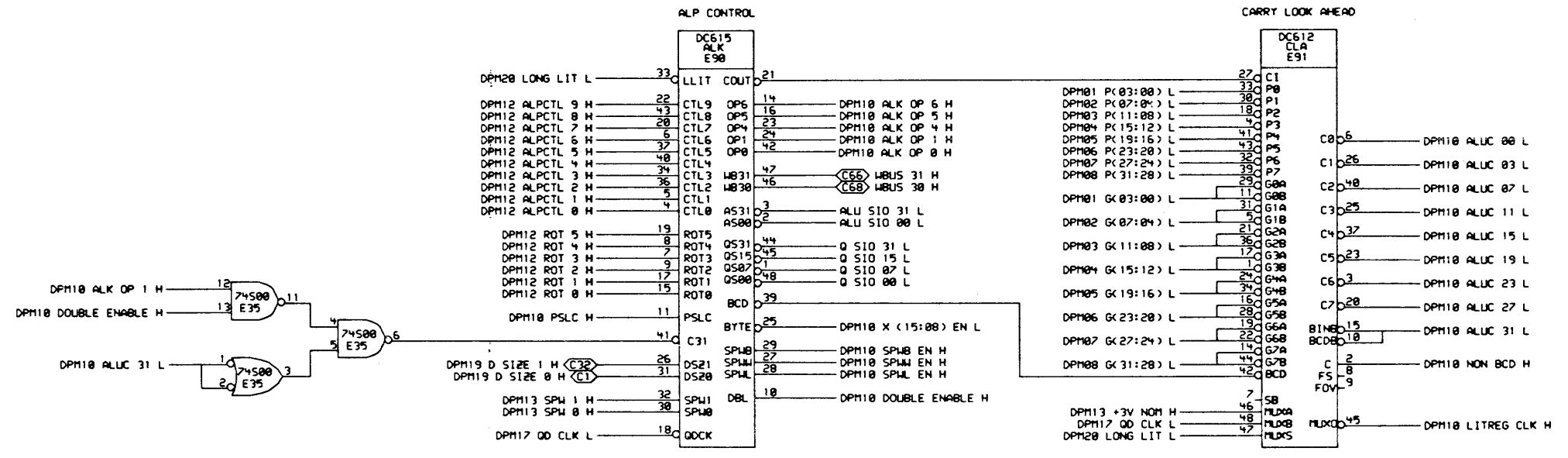
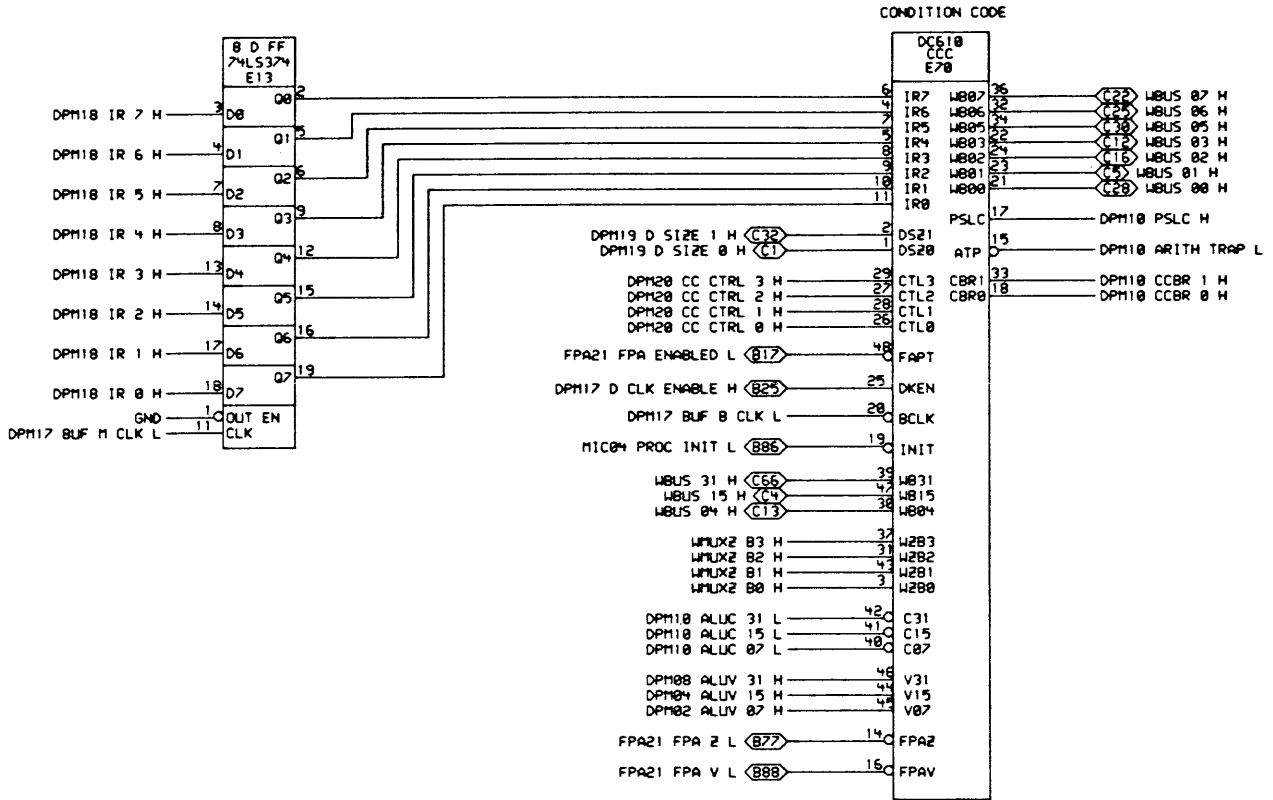
A



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REVISIONS	
CHK	CHANGE NO. REV.

digital	DRN. <i>A. Plesian</i>	DATE 26-MAR-80	ENG.	DATE	TITLE: DPM09 ROTATOR LOGIC
	CHK'D.	DATE	BOARD LOCATION: AC2		
FIRST USED ON OPTION/MODEL: 11/750		NEXT HIGHER ASSEMBLY: B-DD-L0002-0-0		SIZE CODE: D CS	NUMBER: L0002-0-9
				REV. B	



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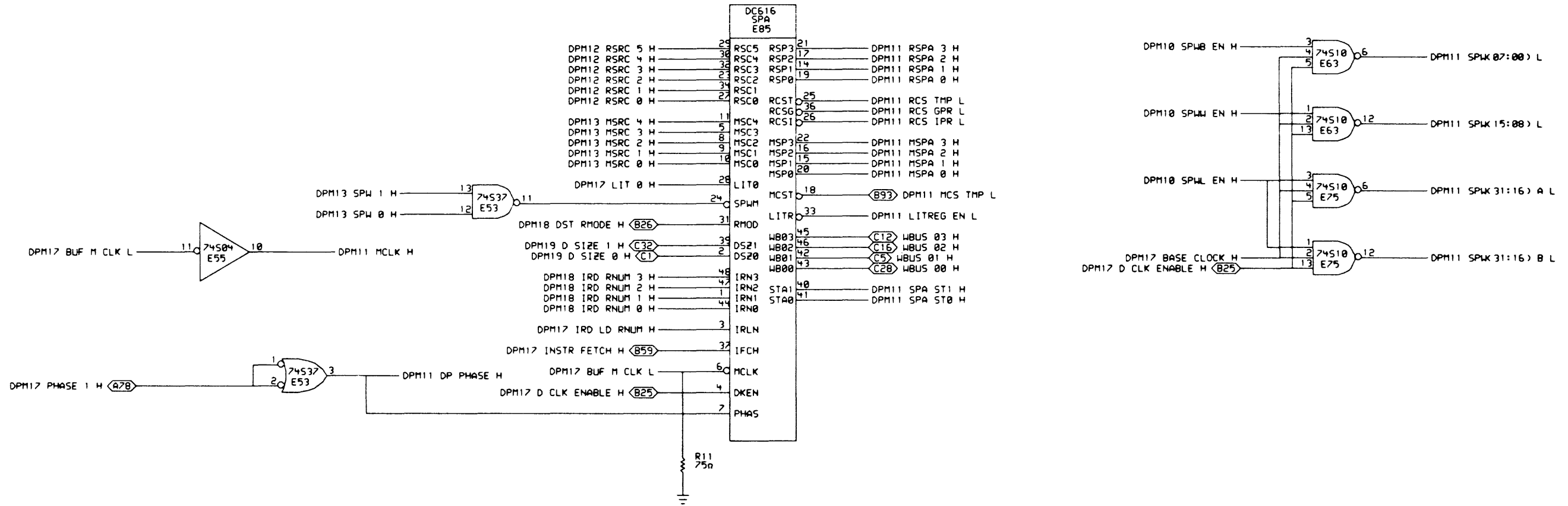
REV.	CHANGE NO.	REV.

digital DRN. A *Person* DATE 09-SEP-88 ENG. DATE TITLE: DPM10 ALK, CLA & CCC

DATE BOARD LOCATION: AC2
SHEET 1 OF 1

(160,1271) DPM10.DRW 25-AUG-88 11:01 NEXT HIGHER ASSEMBLY: SIZE CODE NUMBER REV.
FIRST USED ON OPTION/MODEL: 11/750 B-DD-L0002-0-0 D CS L0002-0-10 C

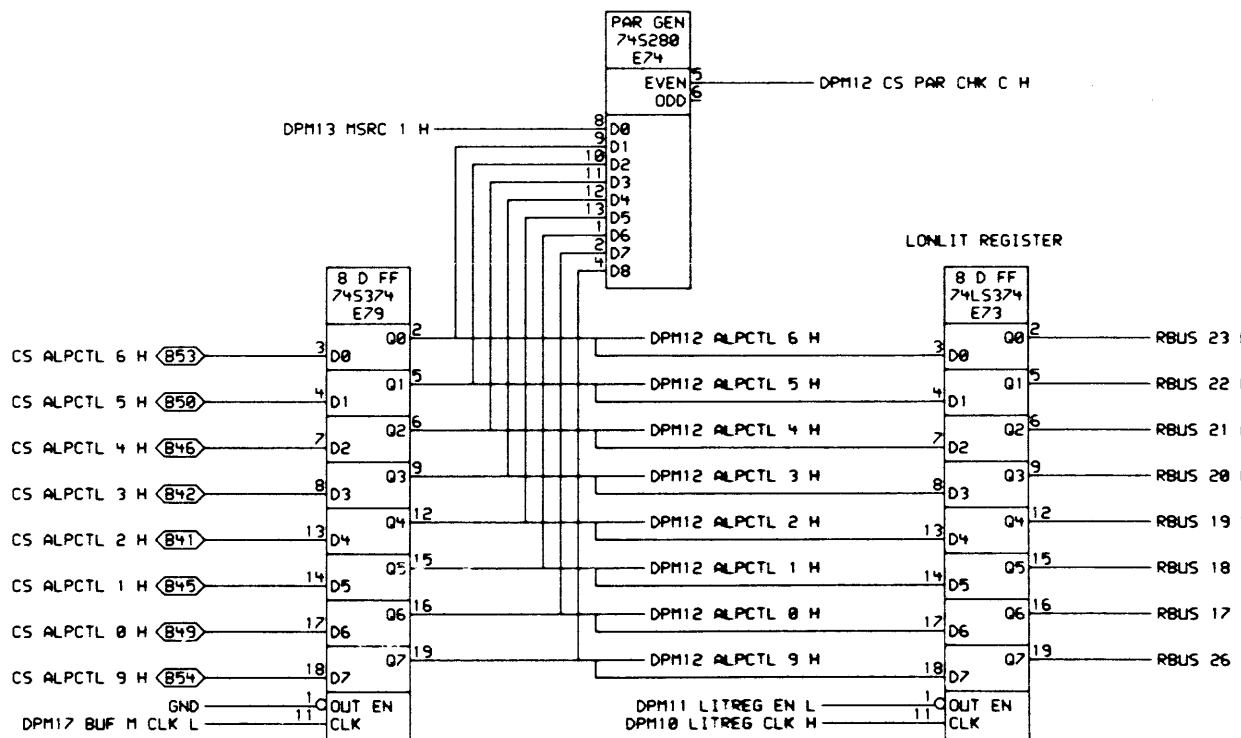
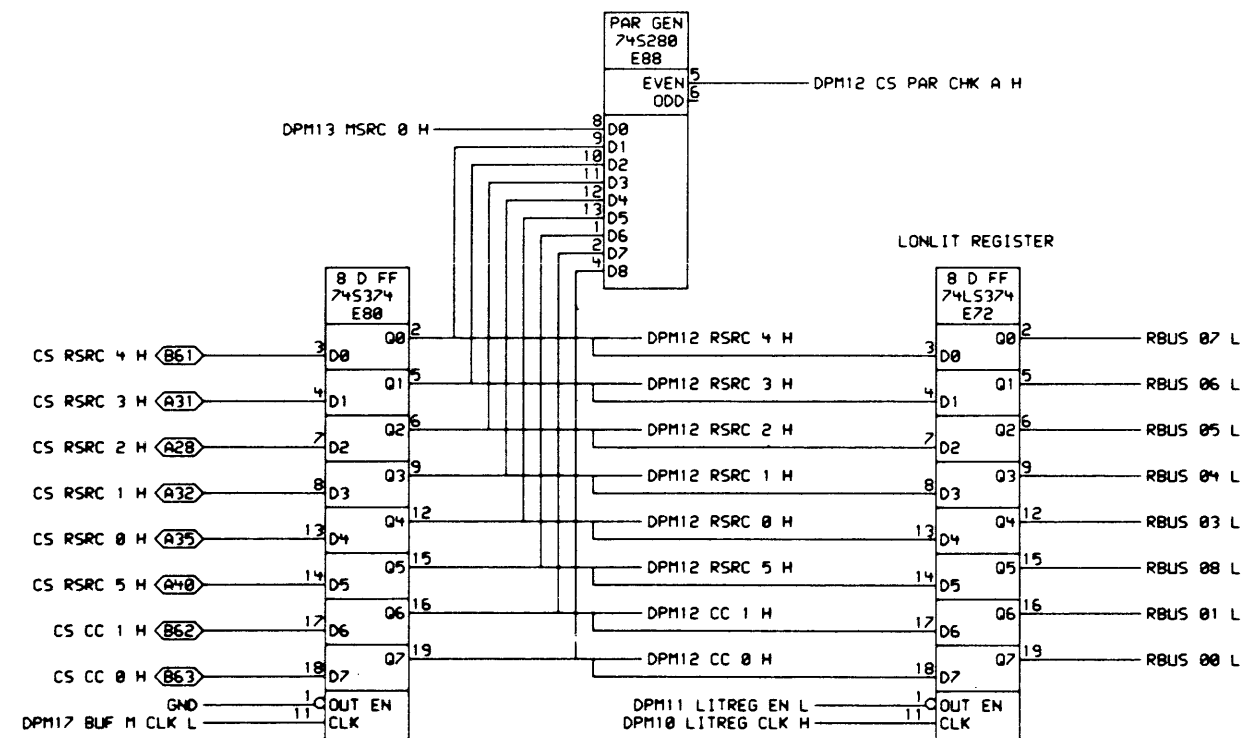
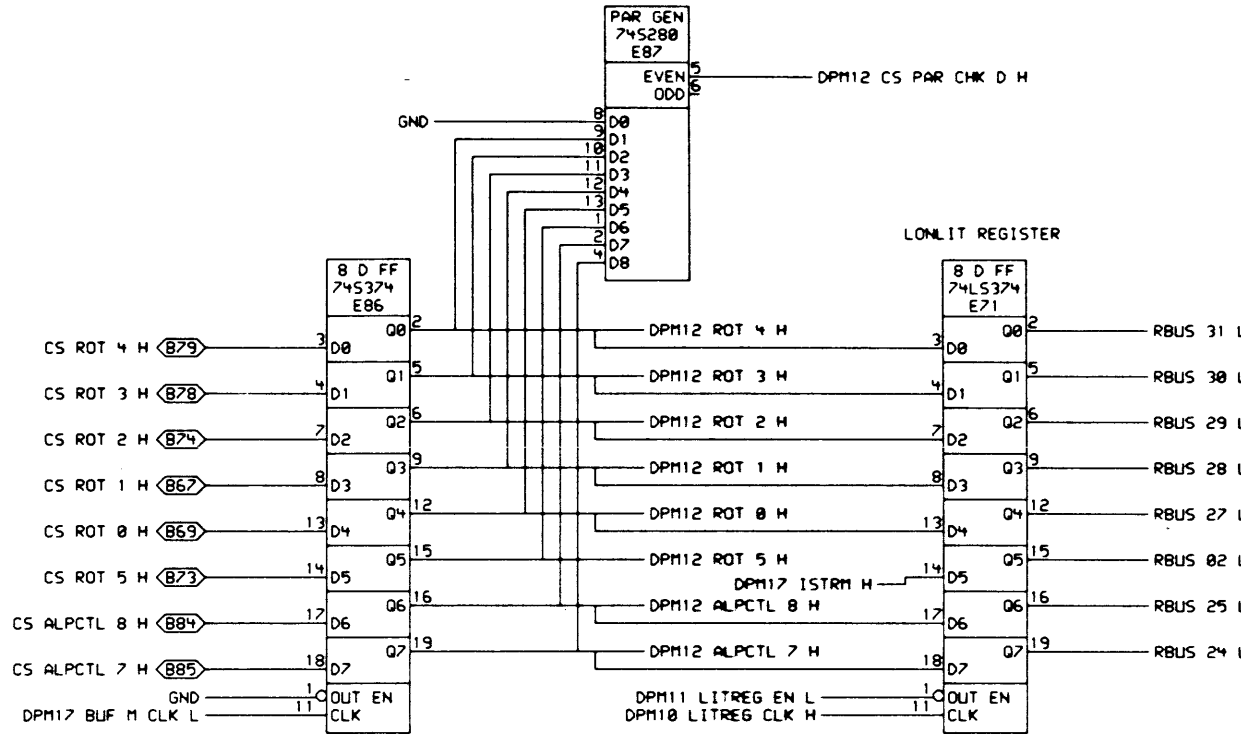
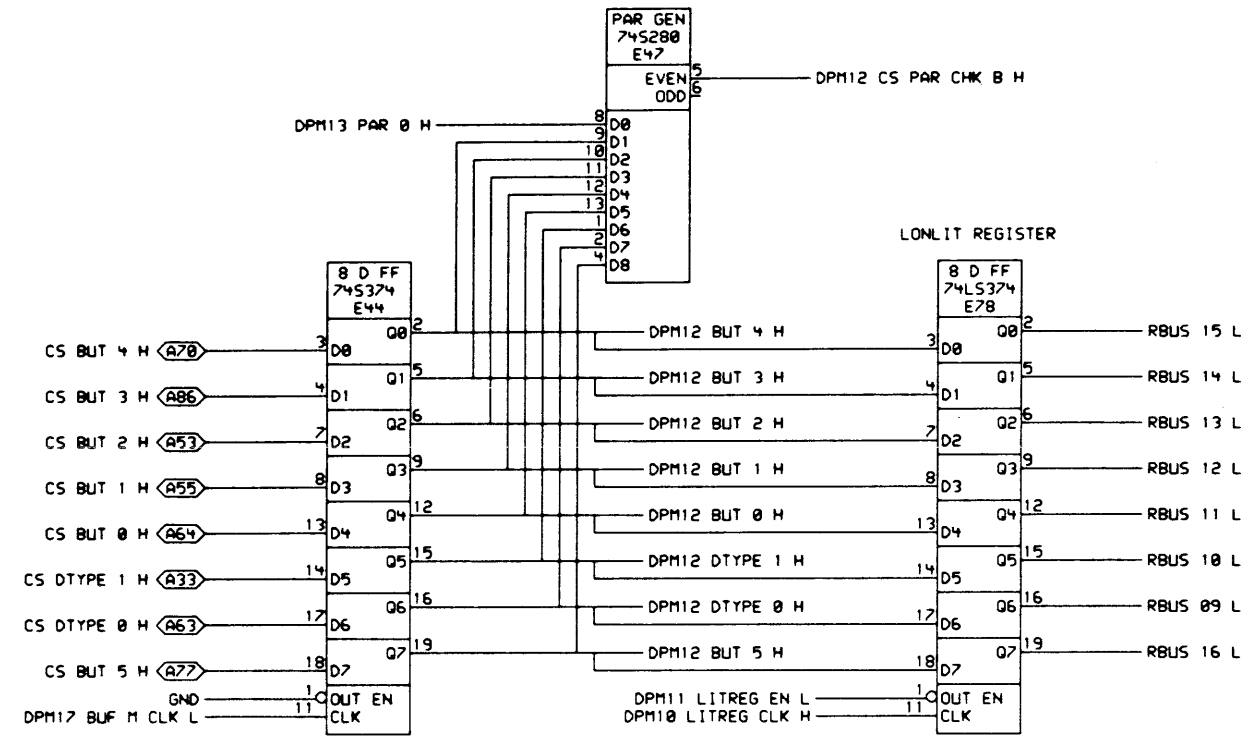
SCRATCH PAD ADDR CONTROL



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REVISIONS		
CHK	CHANGE NO.	REV

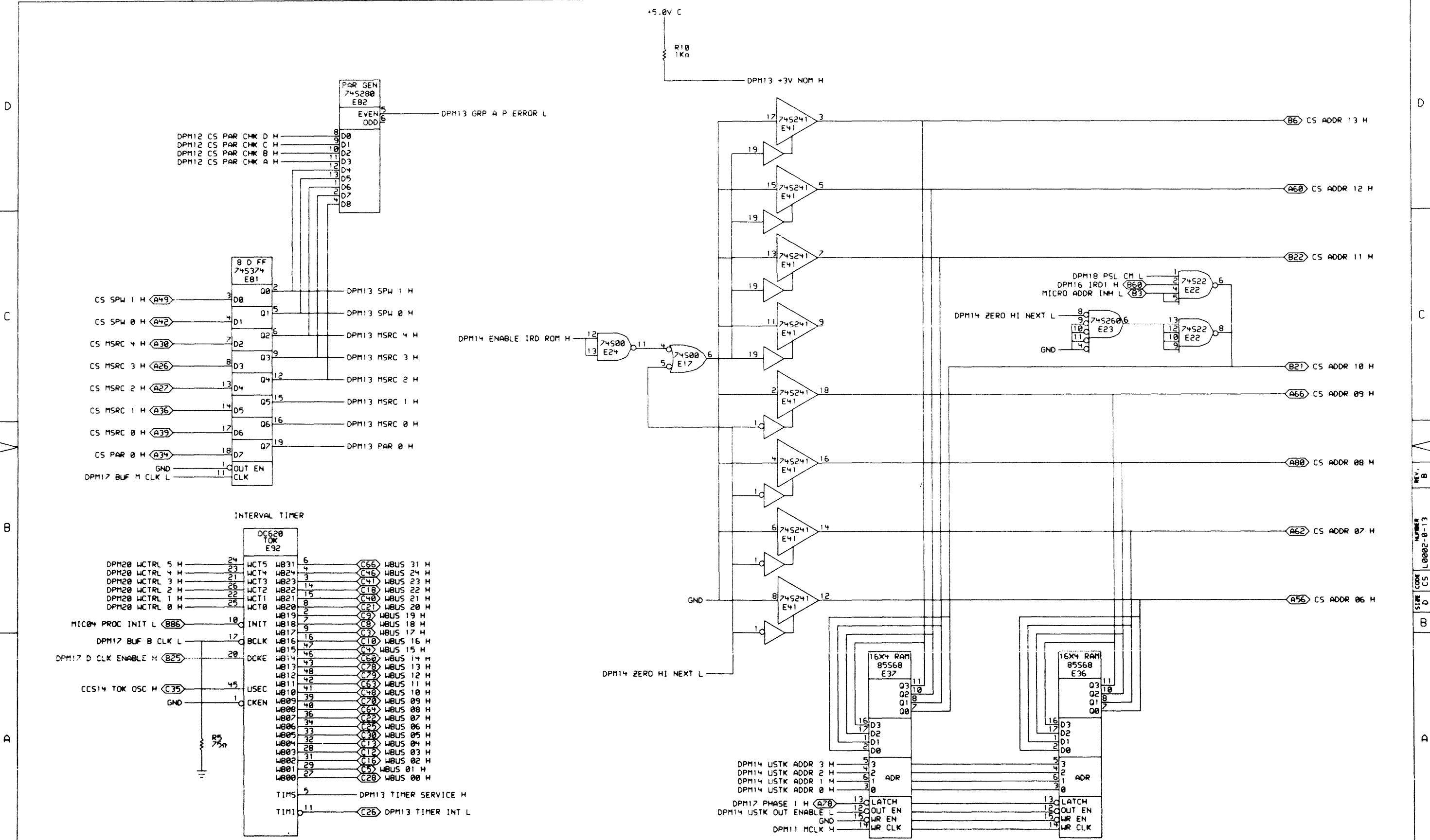
digital	DRN. A. Plescia	DATE 26-MAR-88	ENG.	DATE	TITLE: DPM11 SCRATCH PAD CONTROL
	CHK'D.	DATE 17-FEB-88 14:50	BOARD LOCATION: AC2	SHEET 1 OF 1	SIZE CODE NUMBER REV. D CS L0002-0-11 B
FIRST USED ON OPTION/MODEL: 11/750		NEXT HIGHER ASSEMBLY: B-DD-L0002-0-0			



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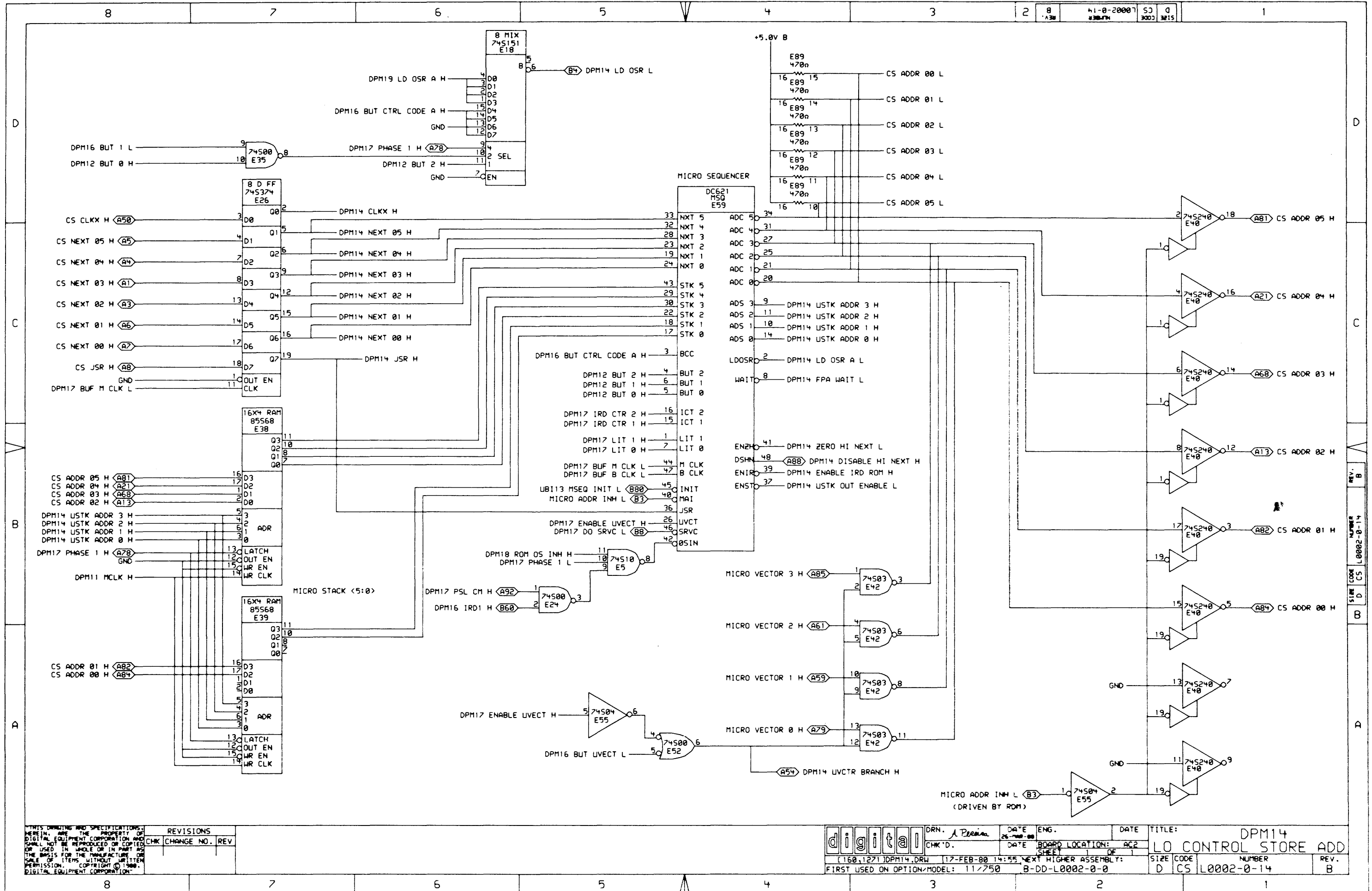
REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN. A. P. P.	DATE ENG. 25-MAR-88	DATE	TITLE: DPM12 CS LATCH, LITREG
	CHK'D.	DATE BOARD LOCATION: AC2	SHEET 1 OF 1	SIZE CODE NUMBER REV. D CS L0002-0-12 B
FIRST USED ON OPTION/MODEL: 11/750		NEXT HIGHER ASSEMBLY: B-00-L0002-0-0		



REVISIONS		TITLE: DPM13 HI CONTROL STORE ADD	
CHK	CHANGE NO.	REV	

DRN. <i>A. P. ...</i>	DATE 26-MAR-88	ENG.	DATE	TITLE: DPM13 HI CONTROL STORE ADD
CHK'D.	DATE	BOARD LOCATION: AC2	SHEET 1 OF	SIZE CODE NUMBER REV. D CS L0002-0-13 B
FIRST USED ON OPTION/MODEL: 11/750		NEXT HIGHER ASSEMBLY: B-DD-L0002-0-0		



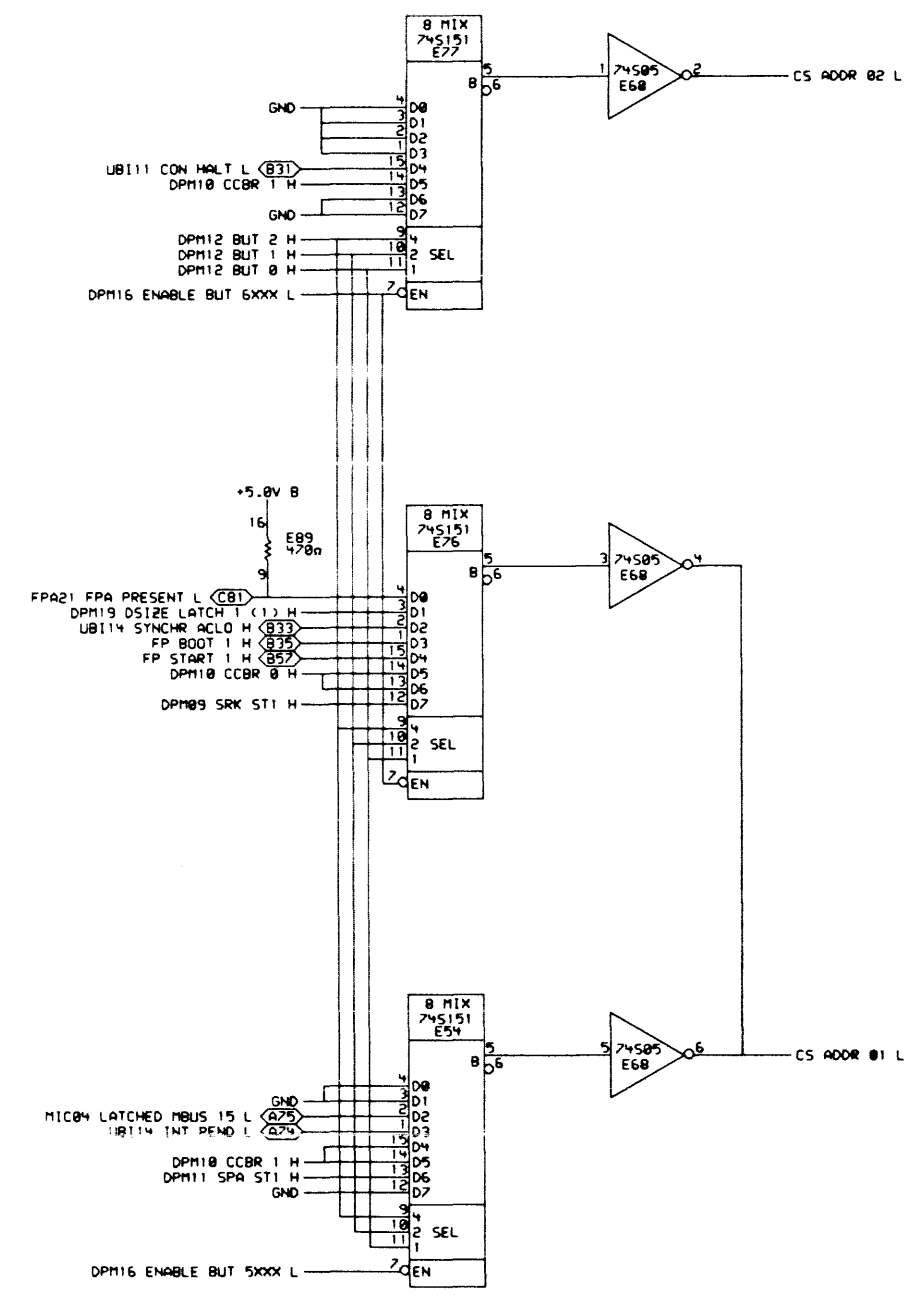
REVISIONS		
CHK	CHANGE NO.	REV

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	DRN. <i>A. P. ...</i> DATE 26- 1980 -80 ENG. <i>...</i> DATE <i>...</i>	TITLE: DPM14 LO CONTROL STORE ADD
[160,1271]DPM14.DRW FIRST USED ON OPTION/MODEL: 11/750	12-FEB-80 14:55 NEXT HIGHER ASSEMBLY: B-DD-L0002-0-0	SIZE CODE D CS NUMBER L0002-0-14 REV. B

D
C
B
D

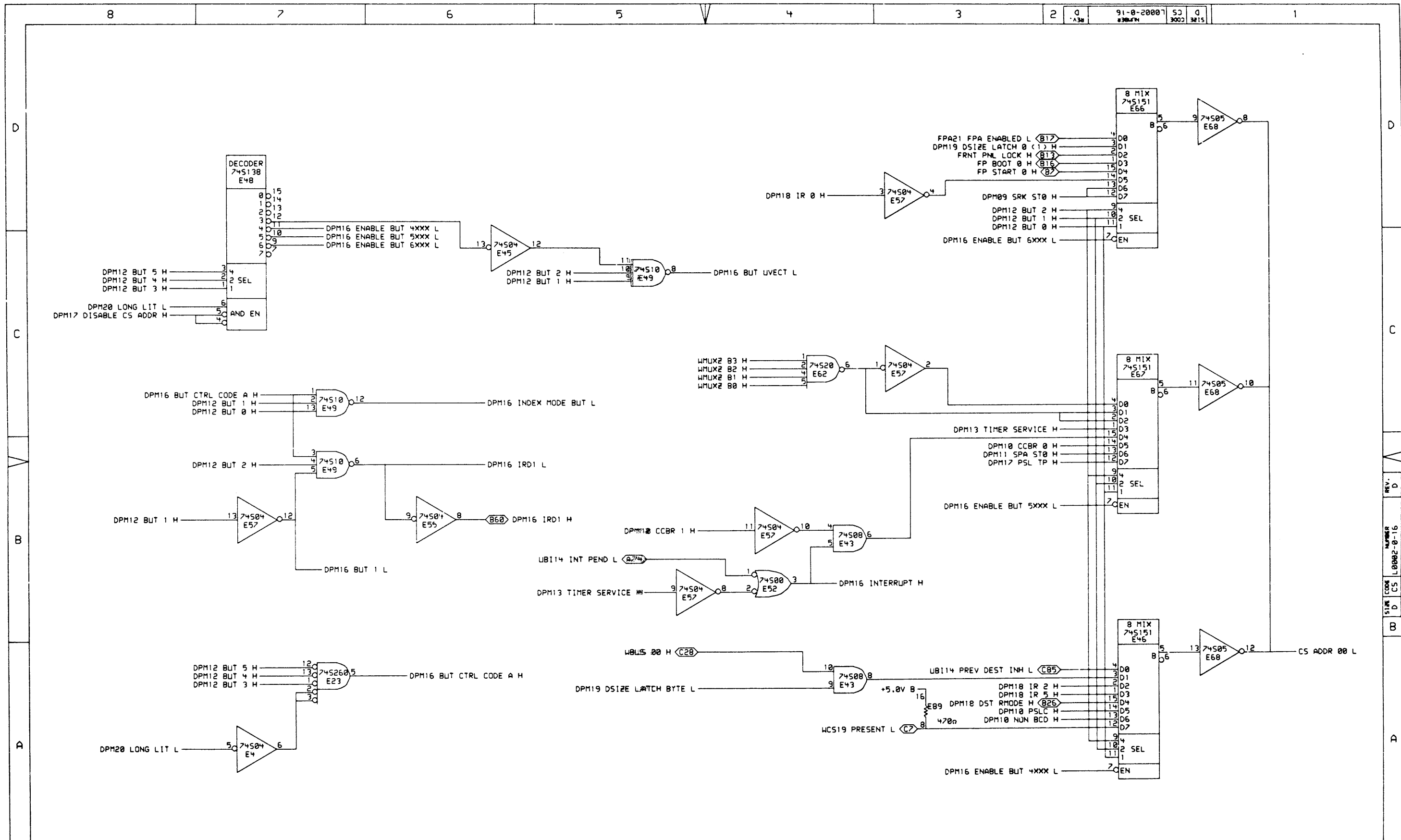
D
C
B
A



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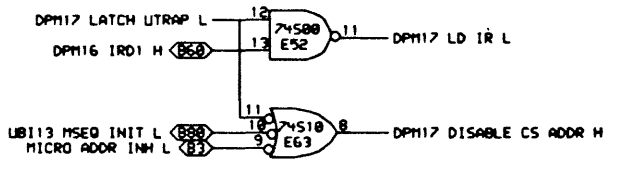
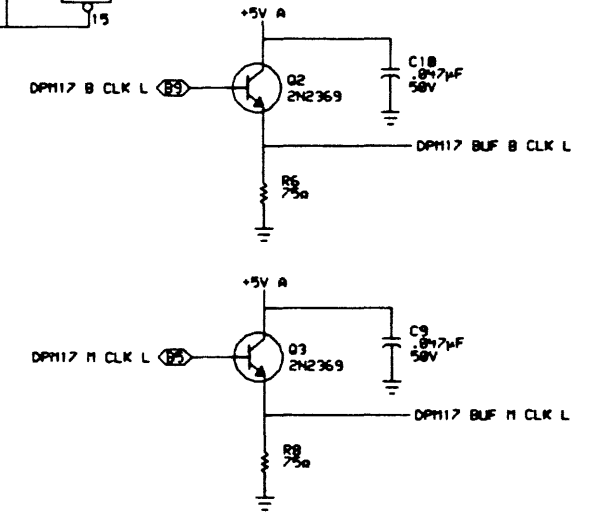
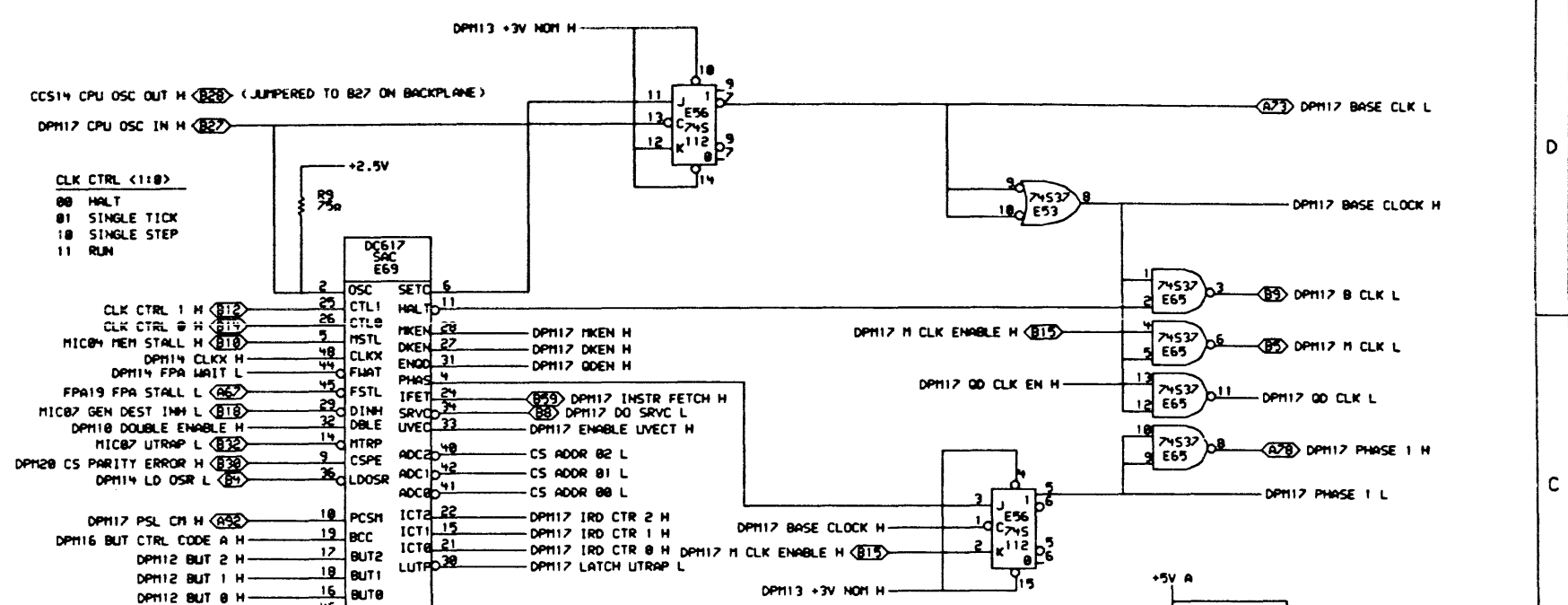
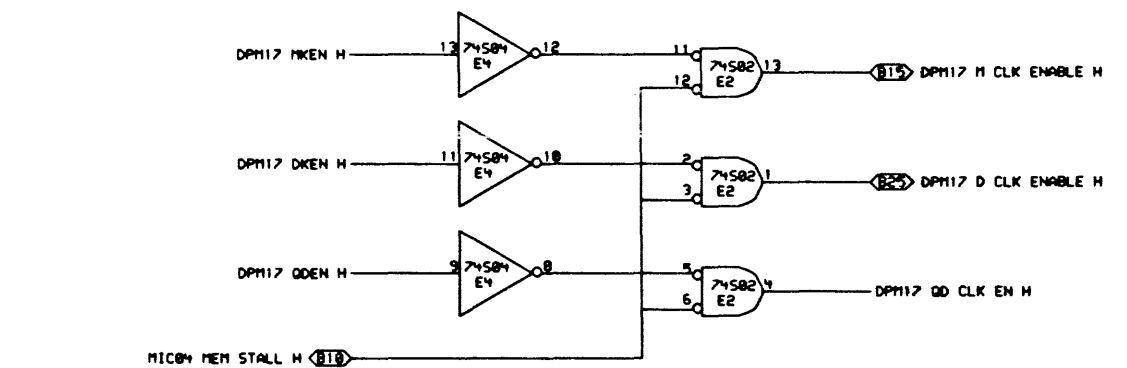
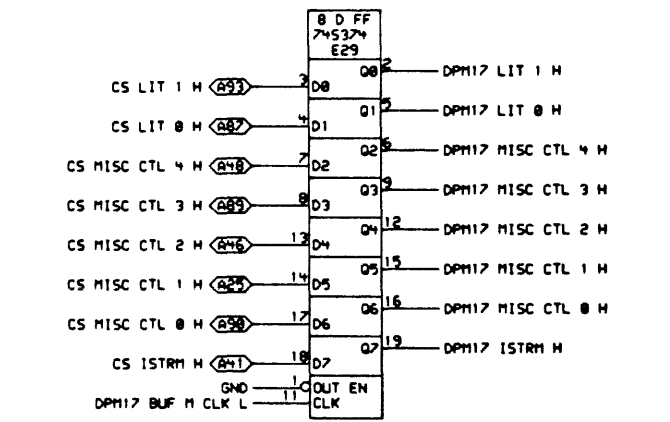
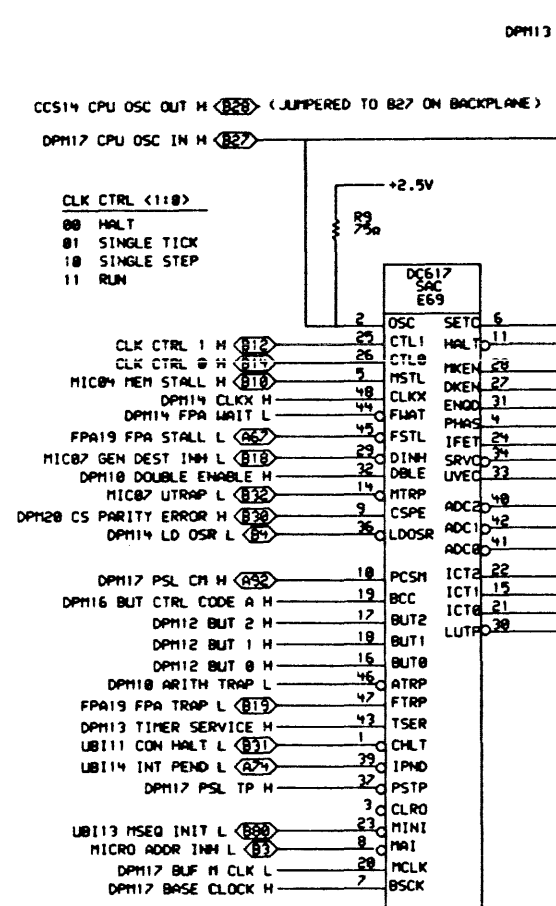
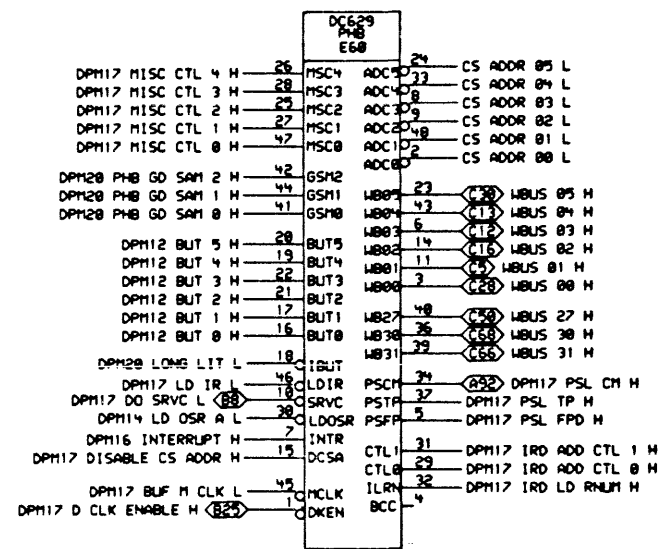
digital	DRN. A. P. ...	DATE 09-SEP-80	ENG.	DATE	TITLE: DPM15 LOW BRANCH BITS
	CHK'D.	DATE 25-AUG-80 11:13	BOARD LOCATION: AC2	SHEET 1 OF 1	NUMBER L0002-0-15
FIRST USED ON OPTION/MODEL: 11/750		NEXT HIGHER ASSEMBLY: B-DD-L0002-0-0		SIZE D	CODE CS



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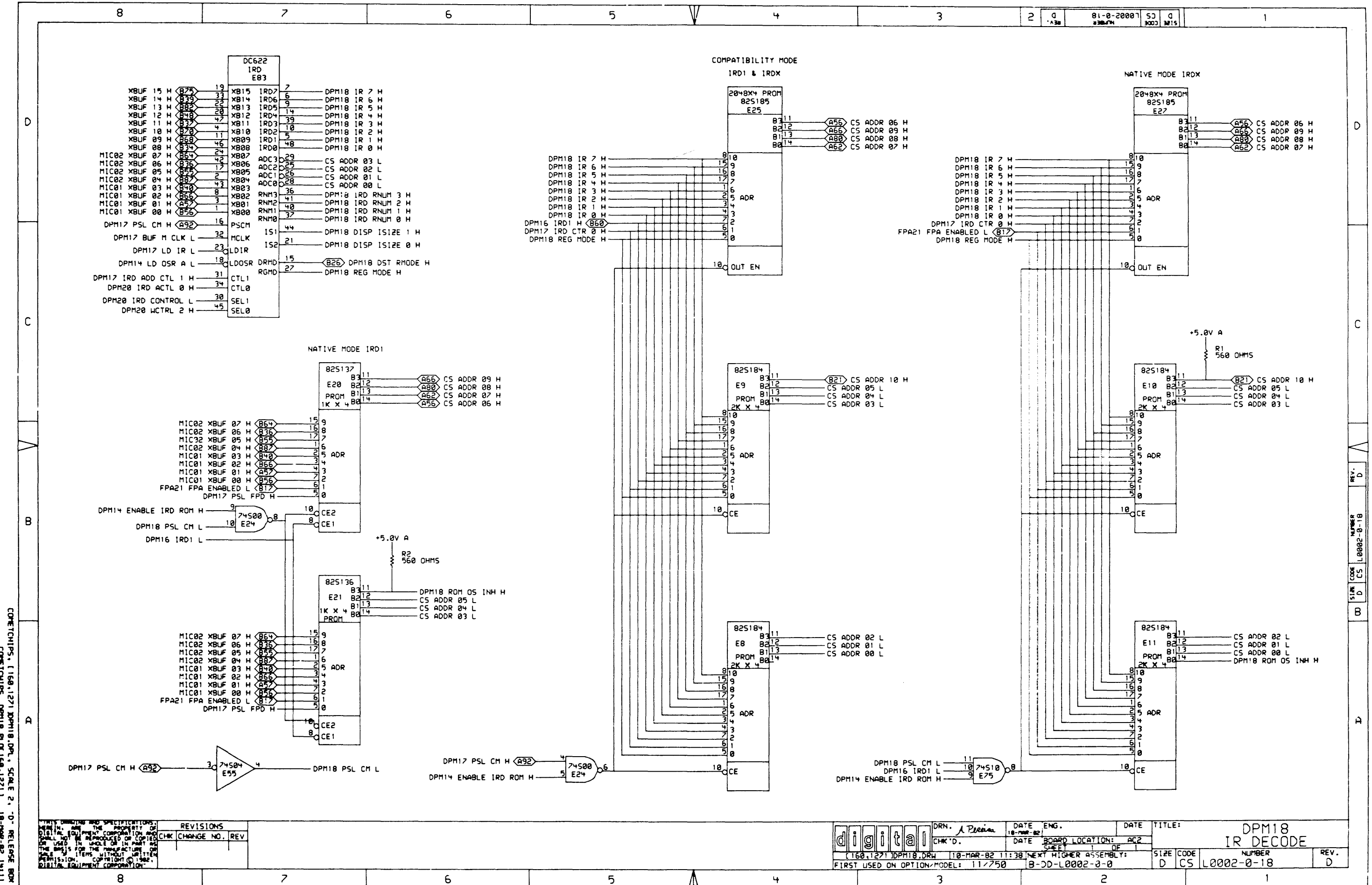
	DRN. <i>A. Plescia</i>	DATE 19-FEB-82	ENG.	DATE	TITLE: DPM16 BRANCH BIT00
	CHK'D.	DATE	BOARD LOCATION: AC2	SHEET	SIZE CODE NUMBER REV. D ICS L0002-0-16 D
FIRST USED ON OPTION MODEL: 11/750		NEXT HIGHER ASSEMBLY: B-DD-L0002-0-0			



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REVISIONS	CHK	CHANGE NO.	REV

digital	DRN. A. P. ...	DATE 09-SEP-88	ENG.	DATE	TITLE: DPM17 SYS CLOCK
	CHK'D.	DATE 25-AUG-88 11:29	BOARD LOCATION: AC2	OF	SIZE CODE NUMBER REV. D CS L0002-0-17 C
FIRST USED ON OPTION/MODEL: 11/750		NEXT HIGHER ASSEMBLY: 18-DD-L0002-0-0			

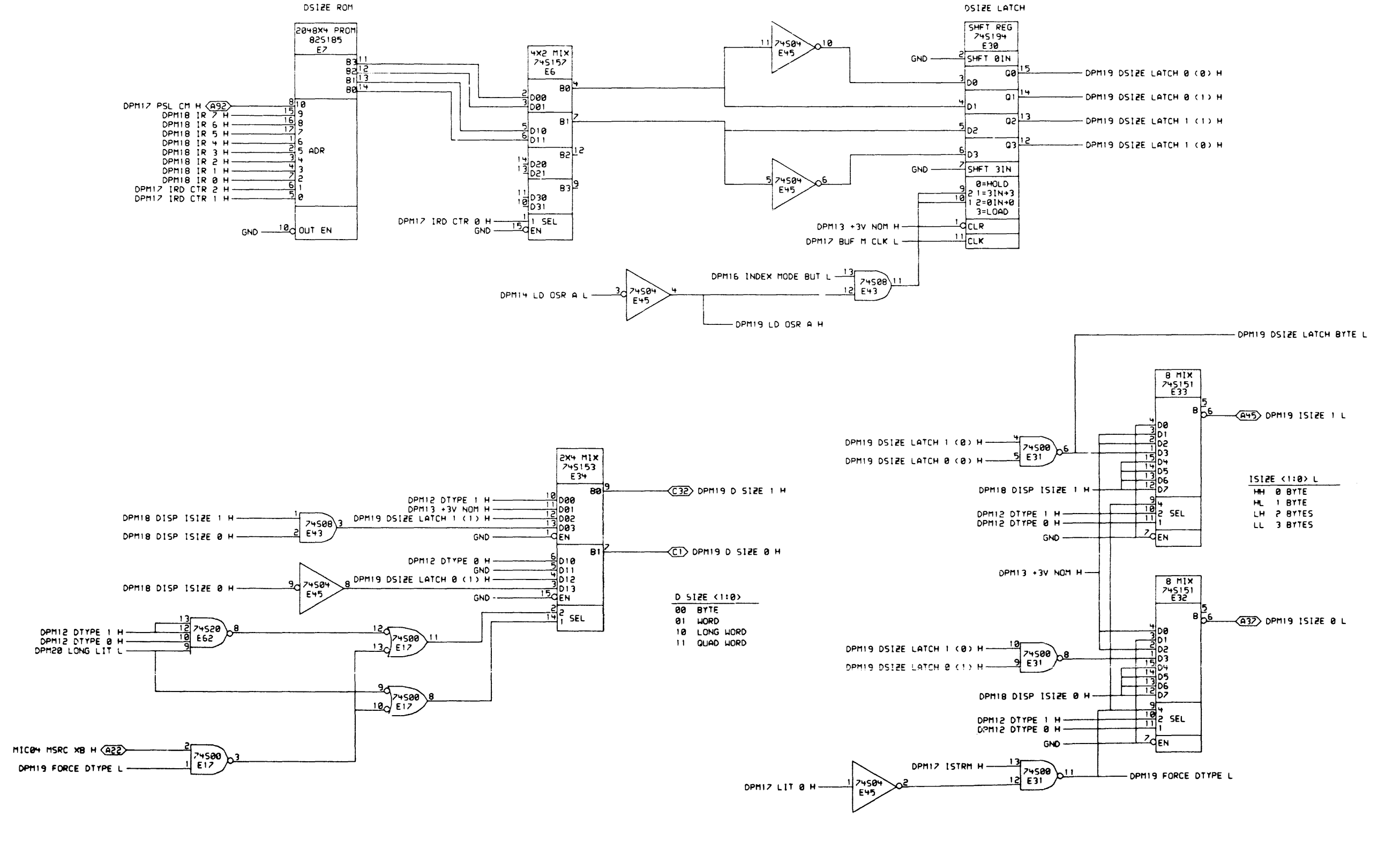


COMETCHIPS, (158,127) DPM18.DRW, SCALE 2, "D" RELEASE BOX
 COMETCHIPS, DPM18.PLOK(158,127) 1, 18-MAR-82 14:11

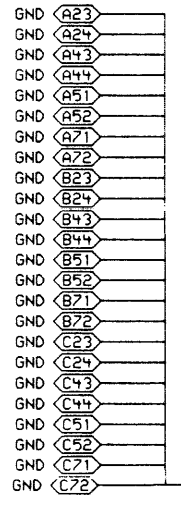
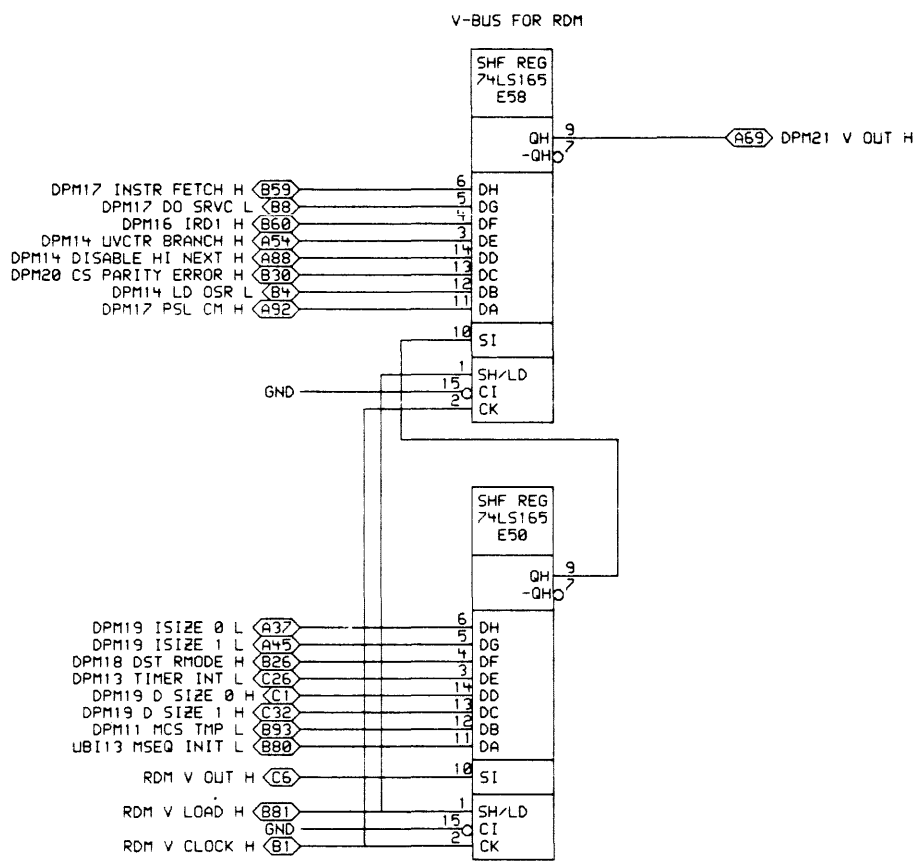
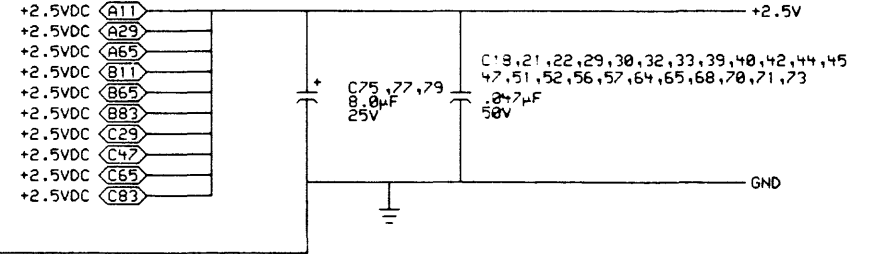
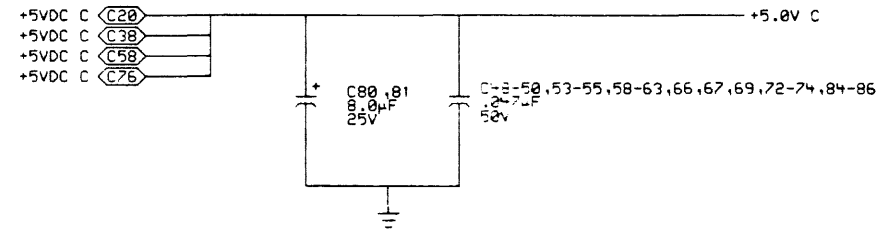
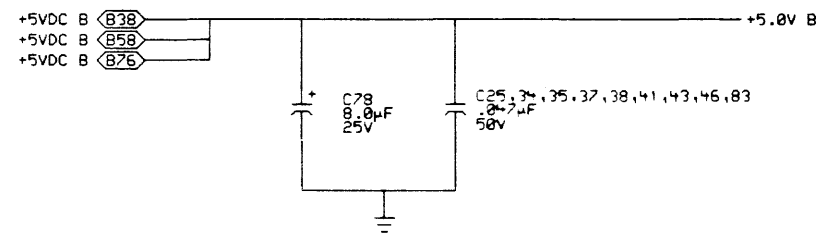
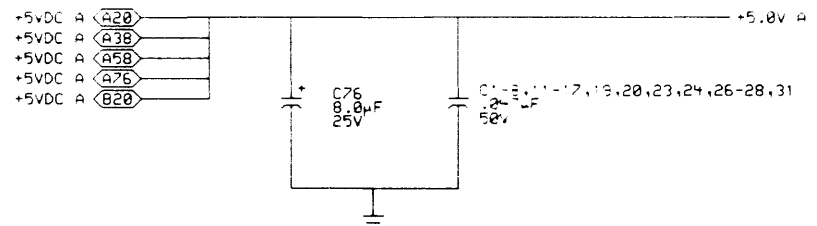
REVISIONS	
CHK	CHANGE NO. REV

	DRN. A. Plescia DATE 10-MAR-82 CHK'D.	DATE 10-MAR-82 BOARD LOCATION: AC2 SHEET 1 OF 1	DATE NEXT HIGHER ASSEMBLY:	TITLE: DPM18 IR DECODE
(158,127) DPM18.DRW FIRST USED ON OPTION/MODEL: 11/750	10-MAR-82 11:38 B-DD-L0002-0-0	SIZE D CODE CS	NUMBER L0002-0-18	REV. D

REV. D
 SIZE CODE NUMBER
 D CS L0002-0-18



REVISIONS		DRN.	DATE	ENG.	DATE	TITLE:
CHK	CHANGE NO.	REV.	19-FEB-82	AC2	19-FEB-82	DPM19 DSIZE & ISIZE
[160,127] DPM19.DRW 19-FEB-82 16:35 NEXT HIGHER ASSEMBLY: B-DD-L0002-0-0						SIZE CODE
FIRST USED ON OPTION/MODEL: 11/250						D CS
						NUMBER
						L0002-0-19
						REV.
						C



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REVISIONS	
CHK	CHANGE NO. REV

digital	DRN. <i>M. Hammond</i>	DATE 17-MAY-83	ENG.	DATE	TITLE: DPM21 VISIBILITY BUS
	CHK'D.	DATE	BOARD LOCATION: AC2	SHEET 1 OF 1	NUMBER L0002-0-21
DSKD: (203,2001) DPM21.DRW (17-MAY-83) 11:05 NEXT HIGHER ASSEMBLY: B-DD-L0002-0-0					REV. C
FIRST USED ON OPTION/MODEL: 11/750					SIZE CODE K CS

SIGNAL NAME	PAGE NUMBER(S)
ALU S10 00 L	01,10
ALU S10 03 L	01,02
ALU S10 07 L	03,02
ALU S10 11 L	03,04
ALU S10 15 L	05,04
ALU S10 19 L	05,06
ALU S10 23 L	07,06
ALU S10 27 L	07,08
ALU S10 31 L	08,10
CCS14 CPU OSC OUT H	17
CCS14 TOK OSC H	13
CLK CTRL 0 H	17
CLK CTRL 1 H	17
CS ADDR 00 H	14
CS ADDR 00 L	18,14,17,16
CS ADDR 01 H	14
CS ADDR 01 L	18,14,17,15
CS ADDR 02 H	14
CS ADDR 02 L	18,14,17,15
CS ADDR 03 H	14
CS ADDR 03 L	18,14,17
CS ADDR 04 H	14
CS ADDR 04 L	18,14,17
CS ADDR 05 H	14
CS ADDR 05 L	18,14,17
CS ADDR 06 H	18,13
CS ADDR 07 H	18,13
CS ADDR 08 H	18,13
CS ADDR 09 H	18,13
CS ADDR 10 H	18,13
CS ADDR 11 H	13
CS ADDR 12 H	13
CS ADDR 13 H	13
CS ALPCTL 0 H	12
CS ALPCTL 1 H	12
CS ALPCTL 2 H	12
CS ALPCTL 3 H	12
CS ALPCTL 4 H	12
CS ALPCTL 5 H	12
CS ALPCTL 6 H	12
CS ALPCTL 7 H	12
CS ALPCTL 8 H	12
CS ALPCTL 9 H	12
CS BUT 0 H	12
CS BUT 1 H	12
CS BUT 2 H	12
CS BUT 3 H	12
CS BUT 4 H	12
CS BUT 5 H	12
CS CC 0 H	12

SIGNAL NAME	PAGE NUMBER(S)
CS CC 1 H	12
CS CLKX H	14
CS DTYPE 0 H	12
CS DTYPE 1 H	12
CS HNEXT PAR H	20
CS ISTRM H	17
CS JSR H	14
CS LIT 0 H	17,20
CS LIT 1 H	17,20
CS MISC CTL 0 H	17
CS MISC CTL 1 H	17
CS MISC CTL 2 H	17
CS MISC CTL 3 H	17
CS MISC CTL 4 H	17
CS MSRC 0 H	13
CS MSRC 1 H	13
CS MSRC 2 H	13
CS MSRC 3 H	13
CS MSRC 4 H	13
CS NEXT 00 H	14
CS NEXT 01 H	14
CS NEXT 02 H	14
CS NEXT 03 H	14
CS NEXT 04 H	14
CS NEXT 05 H	14
CS PAR 0 H	13
CS PAR 1 H	20
CS ROT 0 H	12
CS ROT 1 H	12
CS ROT 2 H	12
CS ROT 3 H	12
CS ROT 4 H	12
CS ROT 5 H	12
CS RSRC 0 H	12
CS RSRC 1 H	12
CS RSRC 2 H	12
CS RSRC 3 H	12
CS RSRC 4 H	12
CS RSRC 5 H	12
CS SPW 0 H	13
CS SPW 1 H	13
CS WCTRL 0 H	20
CS WCTRL 1 H	20
CS WCTRL 2 H	20
CS WCTRL 3 H	20
CS WCTRL 4 H	20
CS WCTRL 5 H	20
DPM01 G(03:00) L	10,01
DPM01 P(03:00) L	10,01
DPM02 ALUV 07 H	10,02

SIGNAL NAME	PAGE NUMBER(S)
DPM02 G(07:04) L	10,02
DPM02 P(07:04) L	10,02
DPM03 EXT DATA L	03,05,06,07,08,04
DPM03 G(11:08) L	10,03
DPM03 P(11:08) L	10,03
DPM04 ALUV 15 H	10,04
DPM04 G(15:12) L	10,04
DPM04 P(15:12) L	10,04
DPM05 G(19:16) L	10,05
DPM05 P(19:16) L	10,05
DPM06 G(23:20) L	10,06
DPM06 P(23:20) L	10,06
DPM07 G(27:24) L	10,07
DPM07 P(27:24) L	10,07
DPM08 ALUV 31 H	10,08
DPM08 G(31:28) L	10,08
DPM08 P(31:28) L	10,08
DPM09 SHF 0 L	09,05,01,06,02,07,03,08,04
DPM09 SHF 1 L	09,05,01,06,02,07,03,08,04
DPM09 SRK ST0 H	16,09
DPM09 SRK ST1 H	15,09
DPM10 ALK OP 0 H	10,05,01,06,02,07,03,08,04
DPM10 ALK OP 1 H	10,05,01,06,02,07,03,08,04
DPM10 ALK OP 4 H	10,05,01,06,02,07,03,08,04
DPM10 ALK OP 5 H	10,05,01,06,02,07,03,08,04
DPM10 ALK OP 6 H	10,05,01,06,02,07,03,08,04
DPM10 ALUC 00 L	10,01
DPM10 ALUC 03 L	10,02
DPM10 ALUC 07 L	10,03
DPM10 ALUC 11 L	10,04
DPM10 ALUC 15 L	10,05
DPM10 ALUC 19 L	10,06
DPM10 ALUC 23 L	10,07
DPM10 ALUC 27 L	10,08
DPM10 ALUC 31 L	10
DPM10 ARITH TRAP L	17,10
DPM10 CCBR 0 H	16,10,15
DPM10 CCBR 1 H	15,16,10
DPM10 DOUBLE ENABLE H	10,17
DPM10 LITREG CLK H	12,10
DPM10 NON BCD H	16,10
DPM10 PSLC H	16,10
DPM10 SPWB EN H	11,10
DPM10 SPWL EN H	11,10
DPM10 SPWH EN H	11,10
DPM10 X(15:08) EN L	10,03,04
DPM11 DP PHASE H	11,05,01,06,02,07,03,08,04,09
DPM11 LITREG EN L	12,11
DPM11 MCLK H	13,14,11
DPM11 MCS TMP L	21,11,05,01,06,02,07,03,08,04

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REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN. <i>DeMora</i>	DATE 21-MAR-88	ENG.	DATE	TITLE: DPM22
	CHK'D.	DATE	BOARD LOCATION: AC2	SHEET OF 1	FORWARD REFERENCE
FIRST USED ON OPTION/MODEL: 11/750		NEXT HIGHER ASSEMBLY: B-DD-L0002-0-0		SIZE CODE D CS	NUMBER L0002-0-22
				REV. B	

SIGNAL NAME	PAGE NUMBER(S)
DPM11 MSPA 0 H	11,05,01,06,02,07,03,08,04
DPM11 MSPA 1 H	11,05,01,06,02,07,03,08,04
DPM11 MSPA 2 H	11,05,01,06,02,07,03,08,04
DPM11 MSPA 3 H	11,05,01,06,02,07,03,08,04
DPM11 RCS GPR L	11,05,01,06,02,07,03,08,04
DPM11 RCS IPR L	11,05,01,06,02,07,03,08,04
DPM11 RCS TMP L	11,05,01,06,02,07,03,08,04
DPM11 RSPA 0 H	11,05,01,06,02,07,03,08,04
DPM11 RSPA 1 H	11,05,01,06,02,07,03,08,04
DPM11 RSPA 2 H	11,05,01,06,02,07,03,08,04
DPM11 RSPA 3 H	11,05,01,06,02,07,03,08,04
DPM11 SPA ST0 H	16,11
DPM11 SPA ST1 H	15,11
DPM11 SPWK 07:00 L	11,01,02
DPM11 SPWK 15:00 L	11,03,04
DPM11 SPWK 31:16 A L	11,05,06
DPM11 SPWK 31:16 B L	11,07,08
DPM12 ALPCTL 0 H	12,10
DPM12 ALPCTL 1 H	12,10
DPM12 ALPCTL 2 H	12,10,05,01,06,02,07,03,08,04
DPM12 ALPCTL 3 H	12,10,05,01,06,02,07,03,08,04
DPM12 ALPCTL 4 H	12,10
DPM12 ALPCTL 5 H	12,10
DPM12 ALPCTL 6 H	12,10
DPM12 ALPCTL 7 H	12,10,05,01,06,02,07,03,08,04
DPM12 ALPCTL 8 H	12,10,05,01,06,02,07,03,08,04
DPM12 ALPCTL 9 H	12,10,05,01,06,02,07,03,08,04
DPM12 BUT 0 H	14,12,16,15,17
DPM12 BUT 1 H	12,16,15,14,17
DPM12 BUT 2 H	14,12,16,15,17
DPM12 BUT 3 H	16,12,17
DPM12 BUT 4 H	16,12,17
DPM12 BUT 5 H	16,12,17
DPM12 CC 0 H	20,12,09
DPM12 CC 1 H	20,12,09
DPM12 CS PAR CHK A H	13,12
DPM12 CS PAR CHK B H	12,13
DPM12 CS PAR CHK C H	12,13
DPM12 CS PAR CHK D H	13,12
DPM12 DTYPE 0 H	19,12
DPM12 DTYPE 1 H	19,12
DPM12 ROT 0 H	12,09,10
DPM12 ROT 1 H	12,09,10
DPM12 ROT 2 H	12,09,10
DPM12 ROT 3 H	12,09,10
DPM12 ROT 4 H	12,09,10
DPM12 ROT 5 H	03,09,12,10
DPM12 RSRC 0 H	12,11,09
DPM12 RSRC 1 H	12,11,09
DPM12 RSRC 2 H	12,11,09

SIGNAL NAME	PAGE NUMBER(S)
DPM12 RSRC 3 H	12,11,09
DPM12 RSRC 4 H	12,11,09
DPM12 RSRC 5 H	12,11,09
DPM13 +3V NOM H	20,19,17,03,10,01,02,09,13
DPM13 GRP A P ERROR L	20,13
DPM13 MSRC 0 H	13,11,12
DPM13 MSRC 1 H	12,13,11
DPM13 MSRC 2 H	13,11
DPM13 MSRC 3 H	13,11
DPM13 MSRC 4 H	13,11
DPM13 PAR 0 H	12,13
DPM13 SPW 0 H	11,13,10
DPM13 SPW 1 H	11,13,10
DPM13 TIMER INT L	21,13
DPM13 TIMER SERVICE H	16,17,13
DPM14 CLKX H	20,14,17
DPM14 DISABLE HI NEXT H	21,14
DPM14 ENABLE IRD ROM H	20,18,13,14
DPM14 FPA WAIT L	14,17
DPM14 JSR H	20,14
DPM14 LD OSR A L	19,14,17,18
DPM14 LD OSR L	14,21,17
DPM14 NEXT 00 H	20,14
DPM14 NEXT 01 H	20,14
DPM14 NEXT 02 H	14,20
DPM14 NEXT 03 H	14,20
DPM14 NEXT 04 H	14,20
DPM14 NEXT 05 H	14,20
DPM14 USTK ADDR 0 H	13,14
DPM14 USTK ADDR 2 H	13,14
DPM14 USTK ADDR 3 H	13,14
DPM14 USTK OUT ENABLE L	13,14
DPM14 UVCTR BRANCH H	14,21
DPM14 ZERO HI NEXT L	13,14
DPM16 BUT 1 L	14,16
DPM16 BUT CTRL CODE A H	14,16,17
DPM16 BUT LIVECT L	16,14
DPM16 ENABLE BUT 4XXX L	16
DPM16 ENABLE BUT 5XXX L	16,15
DPM16 ENABLE BUT 6XXX L	16,15
DPM16 INDEX MODE BUT L	19,16
DPM16 INTERRUPT H	16,17
DPM16 IRD1 H	18,13,14,17,16,21
DPM16 IRD1 L	18,16
DPM17 B CLK L	17
DPM17 BASE CLK L	17
DPM17 BASE CLOCK H	20,17,11
DPM17 BUF B CLK L	20,14,10,13,17
DPM17 BUF M CLK L	10,20,14,17,19,12,11,13,18

SIGNAL NAME	PAGE NUMBER(S)
DPM17 CPU OSC IN H	17
DPM17 D CLK ENABLE H	17,11,10,13
DPM17 DISABLE CS ADDR H	16,17
DPM17 DKEN H	17
DPM17 DO SRVC L	21,14,17
DPM17 ENABLE LIVECT H	14,17
DPM17 INSTR FETCH H	21,12,11
DPM17 IRD ADD CTL 0 H	20,17
DPM17 IRD ADD CTL 1 H	20,17,18
DPM17 IRD CTR 0 H	19,18,17
DPM17 IRD CTR 1 H	19,14,17
DPM17 IRD CTR 2 H	19,14,17
DPM17 IRD LD RNUM H	17,11
DPM17 ISTRM H	20,17,19,12,09
DPM17 LATCH UTRAP L	17
DPM17 LD IR L	17,18
DPM17 LIT 0 H	20,17,19,14,11
DPM17 LIT 1 H	20,17,14
DPM17 M CLK ENABLE H	17,20
DPM17 M CLK L	17
DPM17 MISC CTL 0 H	20,17
DPM17 MISC CTL 1 H	20,17
DPM17 MISC CTL 2 H	20,17
DPM17 MISC CTL 3 H	20,17
DPM17 MISC CTL 4 H	20,17
DPM17 MKEN H	17
DPM17 PHASE 1 H	14,13,11,17
DPM17 PHASE 1 L	17,14
DPM17 PSL CM H	19,14,18,21,17
DPM17 PSL FPD H	18,17
DPM17 PSL TP H	17,16
DPM17 QD CLK EN H	17
DPM17 QD CLK L	17,09,10,05,01,06,02,07,03,08,04
DPM17 QDEN H	17
DPM18 DISP ISIZE 0 H	19,18
DPM18 DISP ISIZE 1 H	19,18
DPM18 DST RMODE H	16,21,18,11
DPM18 IR 0 H	19,18,10,16
DPM18 IR 1 H	19,18,10
DPM18 IR 2 H	19,18,10,16
DPM18 IR 3 H	19,18,10,16
DPM18 IR 4 H	19,18,10
DPM18 IR 5 H	19,18,10,16
DPM18 IR 6 H	19,18,10
DPM18 IR 7 H	19,18,10
DPM18 IRD RNUM 0 H	18,11
DPM18 IRD RNUM 1 H	18,11
DPM18 IRD RNUM 2 H	18,11
DPM18 IRD RNUM 3 H	18,11
DPM18 PSL CM L	13,18

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REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN. <i>DJ</i>	DATE 21-MAR-80	ENG.	DATE	TITLE: DPM23 FORWARD REFERENCE
	CHK'D.	DATE	BOARD LOCATION: AC2	SHEET 1	NUMBER L0002-0-23
FIRST USED ON OPTION/MODEL: 11/750		NEXT HIGHER ASSEMBLY: B-DD-L0002-0-0		SIZE CODE D CS	REV. B

SIGNAL NAME	PAGE NUMBER(S)
DPM18 REG MODE H	18
DPM19 ROM OS INH H	14,18
DPM19 D SIZE 0 H	19,21,03,10,09,11
DPM19 D SIZE 1 H	19,21,10,09,11,05,06,07,08
DPM19 DSIZE LATCH BYTE L	19,16
DPM19 DSIZE LATCH 0 <0> H	19
DPM19 DSIZE LATCH 0 <1> H	19,16
DPM19 DSIZE LATCH 1 <0> H	19
DPM19 DSIZE LATCH 1 <1> H	19,15
DPM19 FORCE DTYPE L	19
DPM19 ISIZE 0 L	19,21
DPM19 ISIZE 1 L	19,21
DPM19 LD OSR A H	14,19
DPM20 CC CTRL 0 H	20,10
DPM20 CC CTRL 1 H	20,10
DPM20 CC CTRL 2 H	20,10
DPM20 CC CTRL 3 H	20,10
DPM20 CS PARITY ERROR H	20,21,17
DPM20 IRD ACTL 0 H	20,18
DPM20 IRD CONTROL L	20,18
DPM20 LONG LIT L	16,20,19,17,10
DPM20 PAR 1 H	20
DPM20 PHB GD SAM 0 H	20,17
DPM20 PHB GD SAM 1 H	20,17
DPM20 PHB GD SAM 2 H	20,17
DPM20 WCTRL 0 H	20,13
DPM20 WCTRL 1 H	20,13
DPM20 WCTRL 2 H	20,18,13
DPM20 WCTRL 3 H	20,13
DPM20 WCTRL 4 H	20,13
DPM20 WCTRL 5 H	20,13
DPM21 V OUT H	21
FP BOOT 0 H	16
FP BOOT 1 H	15
FP START 0 H	16
FP START 1 H	15
FPA19 FPA TRAP L	17
FPA21 FPA ENABLED L	18,16,10
FPA21 FPA PRESENT L	15
FPA19 FPA STALL L	17
FPA21 FPA V L	10
FPA21 FPA Z L	10
FRNT PNL LOCK H	16
MBUS 00 L	01,09
MBUS 01 L	01,09
MBUS 02 L	01,09
MBUS 03 L	01,09
MBUS 04 L	02,09
MBUS 05 L	02,09
MBUS 06 L	02,09
MBUS 07 L	03,02,09
MBUS 08 L	03,09

SIGNAL NAME	PAGE NUMBER(S)
MBUS 09 L	03,09
MBUS 10 L	03,09
MBUS 11 L	03,09
MBUS 12 L	04,09
MBUS 13 L	04,09
MBUS 14 L	04,09
MBUS 15 L	03,04,09
MBUS 16 L	05,09
MBUS 17 L	05,09
MBUS 18 L	05,09
MBUS 19 L	05,09
MBUS 20 L	06,09
MBUS 21 L	06,09
MBUS 22 L	06,09
MBUS 23 L	06,09
MBUS 24 L	07,09
MBUS 25 L	07,09
MBUS 26 L	07,09
MBUS 27 L	07,09
MBUS 28 L	08,09
MBUS 29 L	08,09
MBUS 30 L	08,09
MBUS 31 L	08,09
MIC01 XBUF 00 H	18
MIC01 XBUF 01 H	18
MIC01 XBUF 02 H	18
MIC01 XBUF 03 H	18
MIC02 XBUF 04 H	18
MIC02 XBUF 05 H	18
MIC02 XBUF 06 H	18
MIC02 XBUF 07 H	18
MIC04 LATCHED MBUS 15 L	15
MIC04 MEM STALL H	17,20
MIC04 MSRC XB H	19
MIC04 PROC INIT L	10,13
MIC07 GEN DEST INH L	17
MIC07 UTRAP L	17
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MICRO VECTOR 1 H	14
MICRO VECTOR 2 H	14
MICRO VECTOR 3 H	14
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Q SIO 03 L	01,02
Q SIO 07 L	03,10,02
Q SIO 11 L	03,04
Q SIO 15 L	05,10,04
Q SIO 19 L	05,06
Q SIO 23 L	07,06
Q SIO 27 L	07,08

SIGNAL NAME	PAGE NUMBER(S)
Q SIO 31 L	08,10
RBUS 00 L	12,01,09
RBUS 01 L	12,01,09
RBUS 02 L	12,01,09
RBUS 03 L	12,01,09
RBUS 04 L	12,02,09
RBUS 05 L	12,02,09
RBUS 06 L	12,02,09
RBUS 07 L	12,02,09
RBUS 08 L	12,03,09
RBUS 09 L	12,03,09
RBUS 10 L	12,03,09
RBUS 11 L	12,03,09
RBUS 12 L	12,04,09
RBUS 13 L	12,04,09
RBUS 14 L	12,04,09
RBUS 15 L	12,04,09
RBUS 16 L	12,05,09
RBUS 17 L	12,05,09
RBUS 18 L	12,05,09
RBUS 19 L	12,05,09
RBUS 20 L	12,06,09
RBUS 21 L	12,06,09
RBUS 22 L	12,06,09
RBUS 23 L	12,06,09
RBUS 24 L	12,07,09
RBUS 25 L	12,07,09
RBUS 26 L	12,07,09
RBUS 27 L	12,07,09
RBUS 28 L	12,08,09
RBUS 29 L	12,08,09
RBUS 30 L	12,08,09
RBUS 31 L	12,08,09
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RDM V LOAD H	21
RDM V OUT H	21
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SBUS 01 H	09,01
SBUS 02 H	09,01
SBUS 03 H	09,01
SBUS 04 H	09,01,02
SBUS 05 H	09,01,02
SBUS 06 H	09,01,02
SBUS 07 H	09,02
SBUS 08 H	02,03,09
SBUS 09 H	02,03,09
SBUS 10 H	02,03,09
SBUS 11 H	03,09
SBUS 12 H	03,04,09
SBUS 13 H	03,04,09

NOTES:
1. THIS PAGE LISTS THE SCHEMATIC PAGE NUMBER(S) WHERE A SIGNAL NAME IS REFERENCED.

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CHK	CHANGE NO.	REV

REVISIONS		DATE		TITLE:	
		19-FEB-82	ENG.	DPM24	
			CHK'D.	FORWARD REFERENCE	
			DATE	BOARD LOCATION:	AC2
			SHEET	OF	1
				SIZE	CODE
				D	CS
				NUMBER	L0002-0-24
				REV.	D

digital
FIRST USED ON OPTION/MODEL: 11/750

DATE 19-FEB-82
NEXT HIGHER ASSEMBLY: B-DD-L0002-0-0

DATE 19-FEB-82
TITLE: DPM24 FORWARD REFERENCE
SIZE CODE D CS
NUMBER L0002-0-24
REV. D

SIGNAL NAME	PAGE NUMBER(S)
SBUS 14 H	03,04,09
SBUS 15 H	04,09
SBUS 16 H	05,04,09
SBUS 17 H	05,04,09
SBUS 18 H	05,04,09
SBUS 19 H	05,09
SBUS 20 H	05,06,09
SBUS 21 H	05,06,09
SBUS 22 H	05,06,09
SBUS 23 H	06,09
SBUS 24 H	06,07,09
SBUS 25 H	06,07,09
SBUS 26 H	06,07,09
SBUS 27 H	07,09
SBUS 28 H	07,08,09
SBUS 29 H	07,08,09
SBUS 30 H	07,08,09
SBUS 31 H	08,09
SBUS 32 H	08,09
SBUS 33 H	08,09
SBUS 34 H	08,09
UBI03 BUSF PAR H	20
UBI11 CON HALT L	17,15
UBI13 MSEQ INIT L	21,14,17
UBI14 INT PEND L	16,15,17
UBI14 PREV DEST INH L	16
UBI14 SYNCHR ACLO H	15
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WBUS 04 H	17,10,09,13,02
WBUS 05 H	17,10,09,13,02
WBUS 06 H	10,09,13,02
WBUS 07 H	10,09,13,02
WBUS 08 H	13,03
WBUS 09 H	13,03
WBUS 10 H	13,03
WBUS 11 H	13,03
WBUS 12 H	13,04
WBUS 13 H	13,04
WBUS 14 H	13,04
WBUS 15 H	10,13,04
WBUS 16 H	13,05
WBUS 17 H	13,05
WBUS 18 H	13,05
WBUS 19 H	13,05
WBUS 20 H	13,06
WBUS 21 H	13,06
WBUS 22 H	13,06

SIGNAL NAME	PAGE NUMBER(S)
WBUS 23 H	13,06
WBUS 24 H	13,07
WBUS 25 H	07
WBUS 26 H	07
WBUS 27 H	17,07
WBUS 28 H	08
WBUS 29 H	08
WBUS 30 H	17,10,08
WBUS 31 H	17,10,13,08
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WMUX2 00 H	01,16,10,09,02
WMUX2 01 H	03,16,10,09,04
WMUX2 02 H	05,16,10,09,06
WMUX2 03 H	16,10,09,07,08
XBUF 08 H	18
XBUF 09 H	18
XBUF 10 H	18
XBUF 11 H	18
XBUF 12 H	18
XBUF 13 H	18
XBUF 14 H	18
XBUF 15 H	18

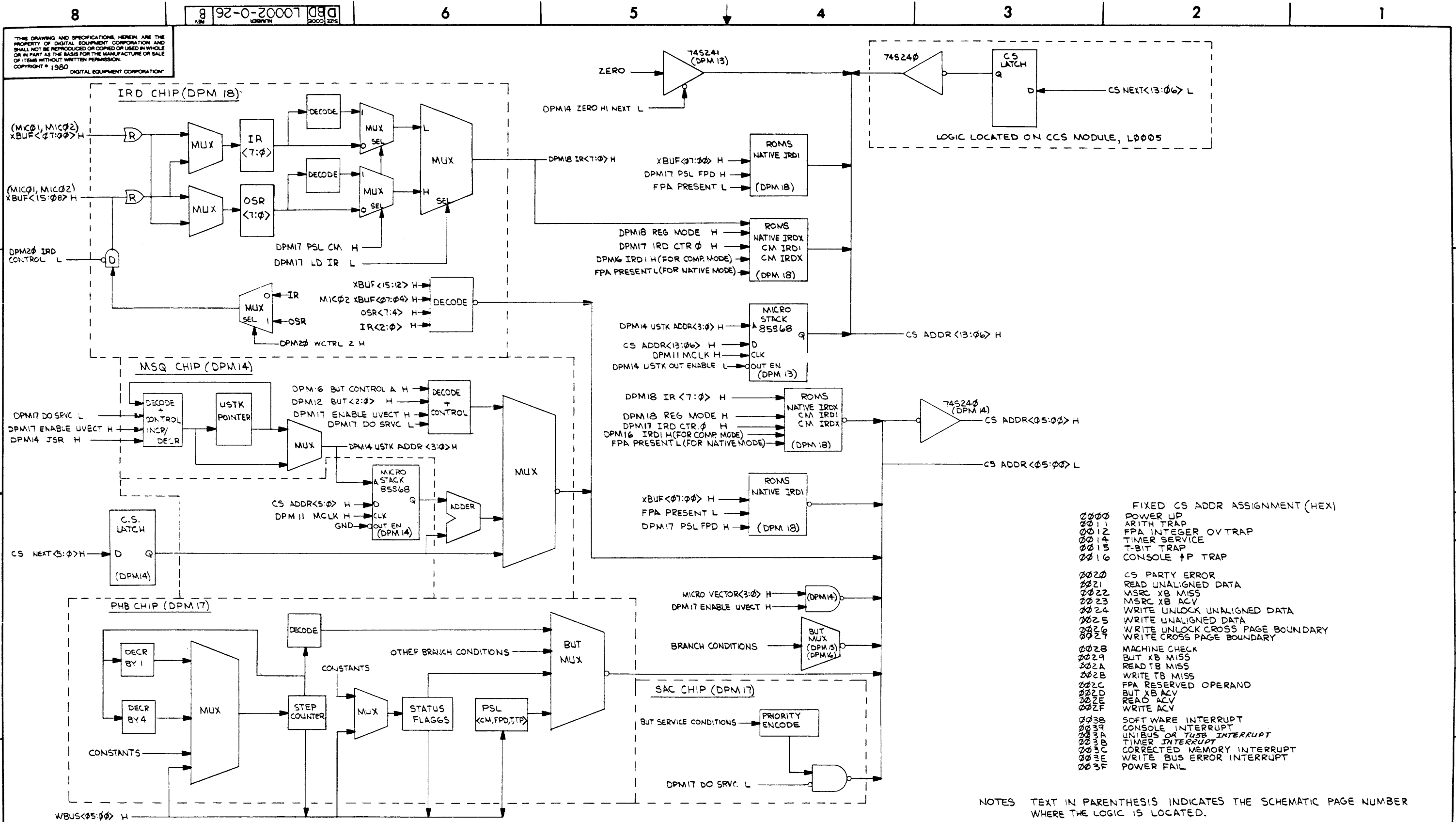
SIGNAL NAME PAGE NUMBER(S)

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REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN. <i>D. Gross</i>	DATE 21-MAR-88	ENG.	DATE	TITLE: DPM25 FORWARD REFERENCE
	CHK'D.	DATE	BOARD LOCATION: AC2	SHEET 1 OF 1	NUMBER L0002-0-25
FIRST USED ON OPTION/MODEL: 11/750		19-MAR-88 12:58	NEXT HIGHER ASSEMBLY: B-DD-L0002-0-0	SIZE CODE D CS	REV. B



- FIXED CS ADDR ASSIGNMENT (HEX)
- 0000 POWER UP
 - 0001 ARITH TRAP
 - 0002 FPA INTEGER OV TRAP
 - 0003 TIMER SERVICE
 - 0004 T-BIT TRAP
 - 0005 CONSOLE I/P TRAP
 - 0020 CS PARTY ERROR
 - 0021 READ UNALIGNED DATA
 - 0022 MSRC XB MISS
 - 0023 MSRC XB ACV
 - 0024 WRITE UNLOCK UNALIGNED DATA
 - 0025 WRITE UNALIGNED DATA
 - 0026 WRITE UNLOCK CROSS PAGE BOUNDARY
 - 0027 WRITE CROSS PAGE BOUNDARY
 - 0028 MACHINE CHECK
 - 0029 BUT XB MISS
 - 002A READ TB MISS
 - 002B WRITE TB MISS
 - 002C FPA RESERVED OPERAND
 - 002D BUT XB ACV
 - 002E READ ACV
 - 002F WRITE ACV
 - 0030 SOFTWARE INTERRUPT
 - 0031 CONSOLE INTERRUPT
 - 0032 UNIBUS OR TUSB INTERRUPT
 - 0033 TIMER INTERRUPT
 - 0034 CORRECTED MEMORY INTERRUPT
 - 0035 WRITE BUS ERROR INTERRUPT
 - 0036 POWER FAIL

NOTES TEXT IN PARENTESIS INDICATES THE SCHEMATIC PAGE NUMBER WHERE THE LOGIC IS LOCATED.

MICRO SEQ. PORTION

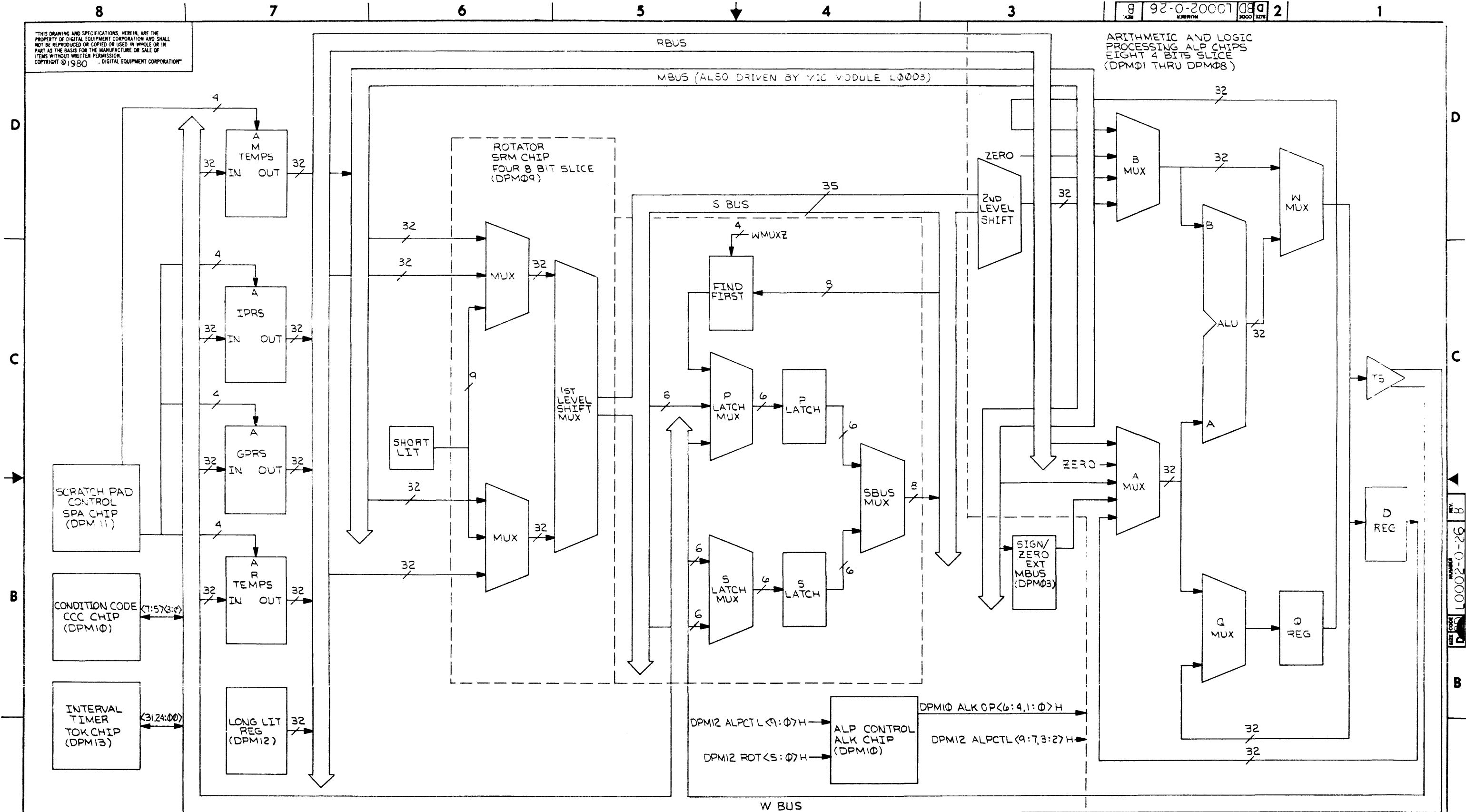
OWN <i>Brian House</i>	DATE <i>16 Jan 80</i>	TITLE digital
CHKD <i>J.F. Sullivan</i>	DATE <i>JAN 24 80</i>	DPM BLOCK DIAGRAM
DESIGN <i>Dominic di</i>	DATE <i>MAR 21, 80</i>	DOCUMENT NUMBER
RESP <i>Dominic di</i>	DATE <i>MAR 21, 80</i>	SIZE D CODE BD NUMBER L0002-0-26 REV B
WFO <i>APB</i>	DATE <i>3/20/80</i>	SCALE <i>1/4"</i> SHEET 1 OF 2
NEXT HIGHER DOC B-DD-L0002-0		

REV.	ECO NUMBER	DATE

REV B NUMBER L0002-0-26 SIZE CODE D BD

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ARITHMETIC AND LOGIC PROCESSING ALP CHIPS EIGHT 4 BITS SLICE (DPM01 THRU DPM08)



REV	
CHG	
CHK	
REVISIONS	
CHANGE NO.	

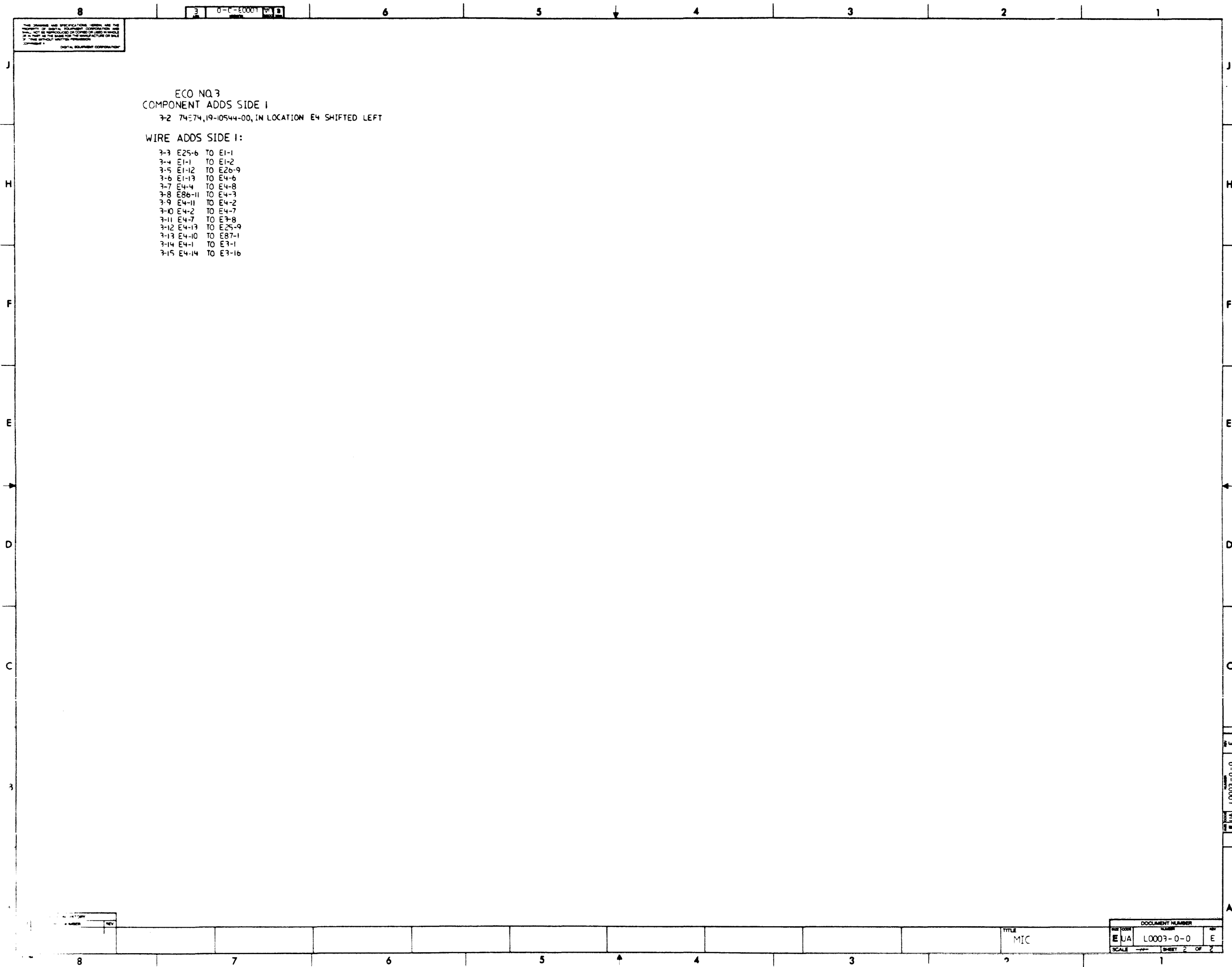
DATA PATH PORTION

DRN: R. Counter	16 Jan 79	FIRST USED ON	
CHK'D: J.F.S.	24 Jan 79	TITLE	DPM BLOCK DIAGRAM
ENG: D. Zi	3/21/80	SIZE	D BD
PROJ. ENG: D. Cane	3/21/80	NUMBER	00002-0-26
PROD: J. J. K.	3/20/80	SCALE	2 OF 2
NEXT HIGHER ASSY.		DIST.	
B-DD-0002-0		REV.	B

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ECO NO.3
COMPONENT ADDS SIDE 1
7-2 74:74,19-10544-00, IN LOCATION E4 SHIFTED LEFT

- WIRE ADDS SIDE 1:
- 7-3 E25-6 TO E1-1
 - 7-4 E1-1 TO E1-2
 - 7-5 E1-12 TO E26-9
 - 7-6 E1-13 TO E4-6
 - 7-7 E4-4 TO E4-8
 - 7-8 E86-11 TO E4-3
 - 7-9 E4-11 TO E4-2
 - 7-10 E4-2 TO E4-7
 - 7-11 E4-7 TO E3-8
 - 7-12 E4-13 TO E25-9
 - 7-13 E4-10 TO E87-1
 - 7-14 E4-1 TO E3-1
 - 7-15 E4-14 TO E3-16



DATE: _____
BY: _____
CHECKED: _____

DOCUMENT NUMBER
E J A L0003-0-0 E
SCALE: _____ SHEET 2 OF 2

TITLE
MIC

AUTOMATED BY PRTLST.3L(32)

PARTS LIST

SHEET A1 OF A2

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY	PER VARIATION	REFERENCE DESIGNATOR
1	1	E-MD-5013693-0-0	5013693-00	DRILL & ETCH DRAWING	1		
2	2		1012784-00	.047 MFD 50V +80-20% CER	69		C9-C23, C25, C26, C29-C41, C43-C81
3	3	SEE NOTES	1012084-01	8 MFD 25V +75-10% AL EL	6		C1-C6
4	4		1313580-00	360.0 .25 W 5.0 % CC	18		R1-R18
5	5		1910532-00	74500 NAND GATE-QUAD 2IN	1		E65
6	6		1910534-00	74504 INVERTER GATE-HEX 1I	2		E85, E87
7	7		1910536-00	74510 NAND GATE-TRIPLE 3IN	1		E91
8	8		1910542-00	74564 A-0-1 GATE 4-2-3-2	3		E24, E47, E89
9	9		1910547-00	745153 MUX 1 OF 4 (DUAL)	3		E100-E102
10	10		1910549-00	745158 MUX 1 OF 2 (QUAD)	2		E5, E79
11	11		1910541-00	74540 NAND GATE-DUAL 4IN, B	1		E38
12	12		1910957-00	745175 FF-D QUAD COMMON CLO	3		E3, E23, E25
13	13		1911573-00	745280 PARITY GEN/CHKR, 9BIT	17		E6, E27, E48, E59, E68, E98, CONT E105-E108, E111, E144, E145, E150, CONT E157, E159, E166, E18, E39, E60, E80
14	14		1911641-00	SN 745257 MUX, QUAD 2 TO 1	4		E26, E92
15	15		1911712-00	74551 AND-OR GATE-INVERT D	2		E112, E143
16	16		1916310-00	RAM, 256X1, TRI STATE	2		
17	17		1912096-00	DEC 74586 XOR GATE, QUAD 2IN	1		E96
18	18		1912097-00	SN 745182 LOOK AHD CARRY GEN	1		E164
19	19		1912388-00	74502 NOR GATE-QUAD 2IN, PO	1		E88
20	20		1912389-00	74508 AND GATE-QUAD 2IN, PO	1		E90
21	21		1912746-00	DEC 74537 NAND GATE-QUAD 2IN	2		E94, E95
22	22		1913340-00	74532 OR GATE-QUAD 2IN	2		E22, E93
23	23		1913493-00	745241 OCTAL BUFFER, TRI-STA	5		E7, E28, E49, E69, E97
24	24		1913662-00	RAM 1KX1 16 PIN TT	50		E8-E16, E29-E37, E50-E58, E70-E78, CONT E117-E123, E129-E135
25	25		1913671-00	745374 FF-D OCTAL TRISTATE	5		E2, E139, E146, E153, E160
26	26		1913839-00	74LS165 SHIFT REG. 8BIT	2		E44, E84
27	27		1913888-00	DC 102A EQUALS CHECKER 8BIT	6		E109, E110, E151, E152, E158, E165

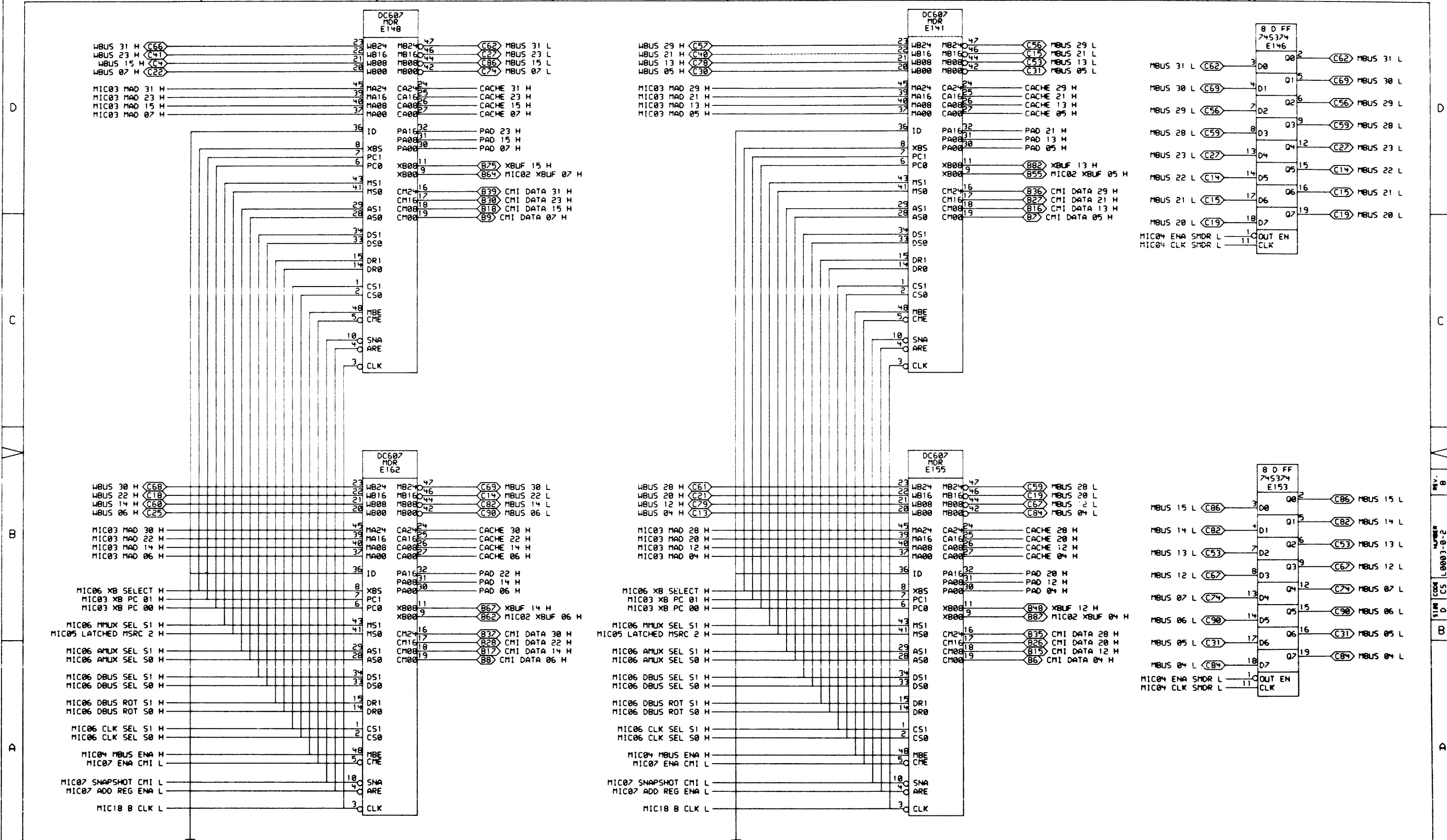
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ENG	ECO NUMBER	REV	SECTION A OF A	D.SIREEN	31-MAY-79	PARTS LIST	
---	INITIAL	B	SECTION VARIATION INDEX	CHK'D: F. GAROFALO	DATE: 31-MAY-79	MIC	
D.L.	L0003-TW001	C	[A] 00	DES. ENG: P. BINDER	DATE: 31-MAY-79	DOCUMENT NUMBER	
			[B]	RESP. ENG.: P. BINDER	DATE: 31-MAY-79	SIZE	CODE
			[C]	MFG. ENG.: VANCE PARKER	DATE: 8-FEB-80	PL	NUMBER
			[D]	ASSEMBLY NUMBER:	TOP DOCUMENT NUMBER:	K	REV
			[E]	E-UA-L0003-0-0	B-JD-L0003-0-0	PL	L0003-0-DBP
			[F]			FILE NAME:	EDIT #
			[G]			Z1258.PLS	11
			[H]				
			[I]				
			[J]				
			[K]				
			[L]				
			[M]				
			[N]				

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LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION 00	REFERENCE DESIGNATOR
28	28	1914085-00	74S260 NOR GATE-DUAL, POS	3	E45, E67, E86
29	29	1914086-00	74S30 NAND GATE-POS 8IN	1	E64
30	30	1914681-00	DC 6078 BIPOLAR, LS, 400-GATE	8	E140, E141, E147, E148, E154, E155, E161, E162
31	31	1914683-00	DC 609E BIPOLAR, LS, 400-GATE	4	E142, E149, E156, E163
32	32	1914697-00	DC 623C BIPOLAR, LS, 400-GATE	1	E103
33	33	1914698-00	DC 624E BIPOLAR, LS, 400-GATE	1	E128
34	34	1914699-00	DC 625B BIPOLAR, LS, 400-GATE	1	E127
35	35	1914700-00	DC 626B BIPOLAR, LS, 400-GATE	1	E116
36	36	1914701-00	DC 627B BIPOLAR, LS, 400-GATE	1	E104
37	37	1914702-00	DC 628B BIPOLAR, LS, 400-GATE	1	E115
38	38	1915193-00	LS244 DRIVER, LINE, OCTAL, T	1	E99
39	39	1915697-00	RAM 256X4 TRI-STATE	20	E19-E21, E40-E42, E61-E63, E81-E83, E113, E114, E124-E126, E136-E138 E66
40	40	1910537-00	74S11 AND GATE-TRIPLE 3INP	1	
41	41	1210711-02	/REPLACED BY 12-16988-02	1	
42	42	9000024-01	EYELET, ROLLED FLANGE .121 OD X	12	
43	43	1503121-00	2N 2369 NPN 350MW SI N	1	Q1
44	44	1302379-00	75.0 .25 W 5.0 % CC	4	R19-R21, R23
45	45	9009185-00	JUMPER, WIRE, INSULATED, BLACK B	1	R22
46	46	1910544-00	74S74 FF-D DUAL EDGE TRIGG	1	E46
47	47	1910878-00	7427 NOR GATE-TRIPLE 3IN	2	E43, E1
48	48	1215924-00	SOCKET IC W/METAL CONT	18	XE103, XE104, XE115, XE116, XE127, XE128, XE140-XE142, XE147-XE149, XE154-XE156, XE161-XE163
49	49	1215935-00	GASKET, THERMAL .50"X.80"	18	
50	50	1215936-00	HEAT SINK, FORCED CONVECTION	18	

51 NOTE: SPARE I.C. LOCATIONS ARE E4, E17
 52 NOTE: SOME MODULES WILL HAVE 10-05305 INSTEAD OF 10-12084-01

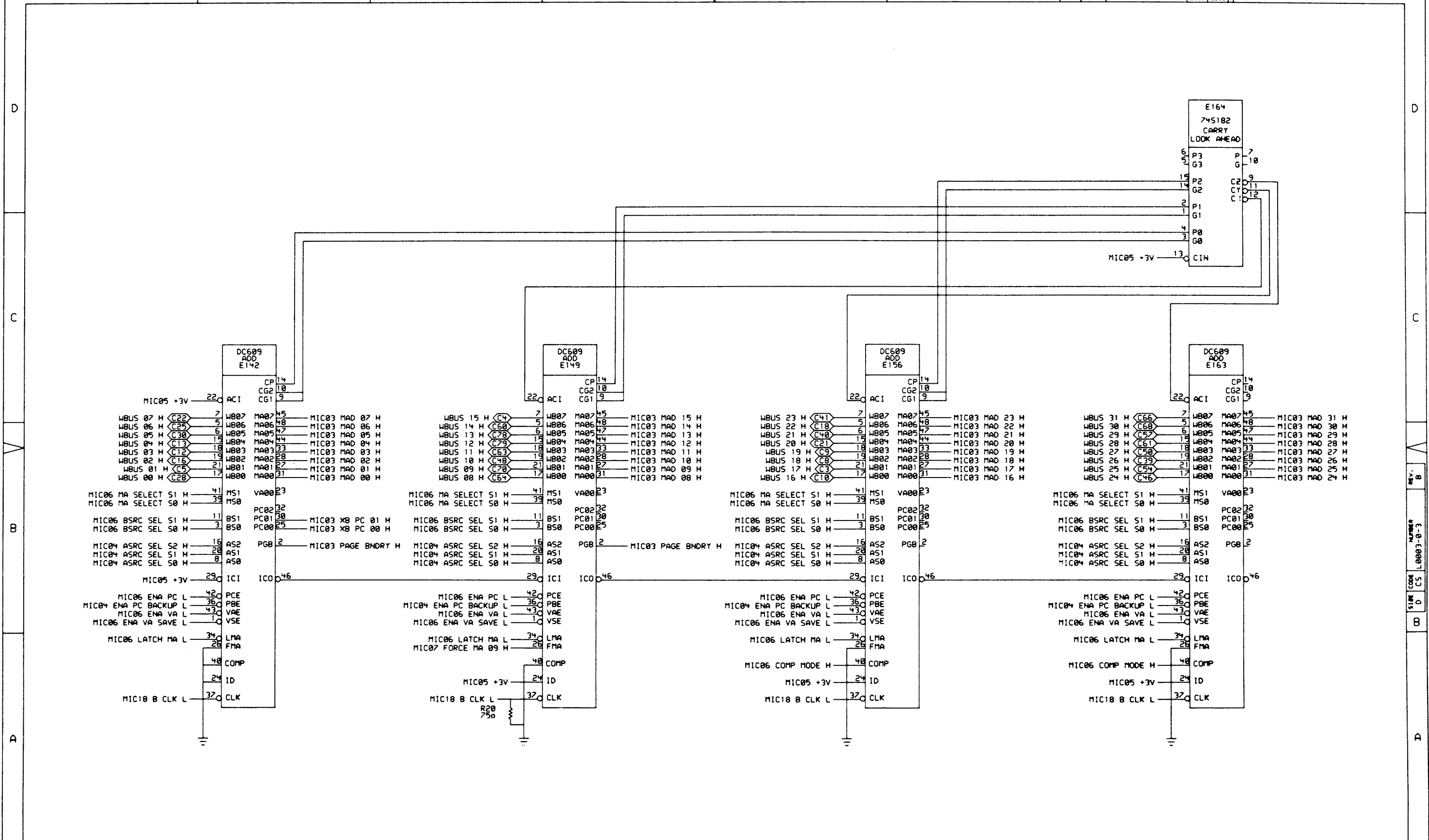
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							MIC			K	PL	L0003-0-DBP	C



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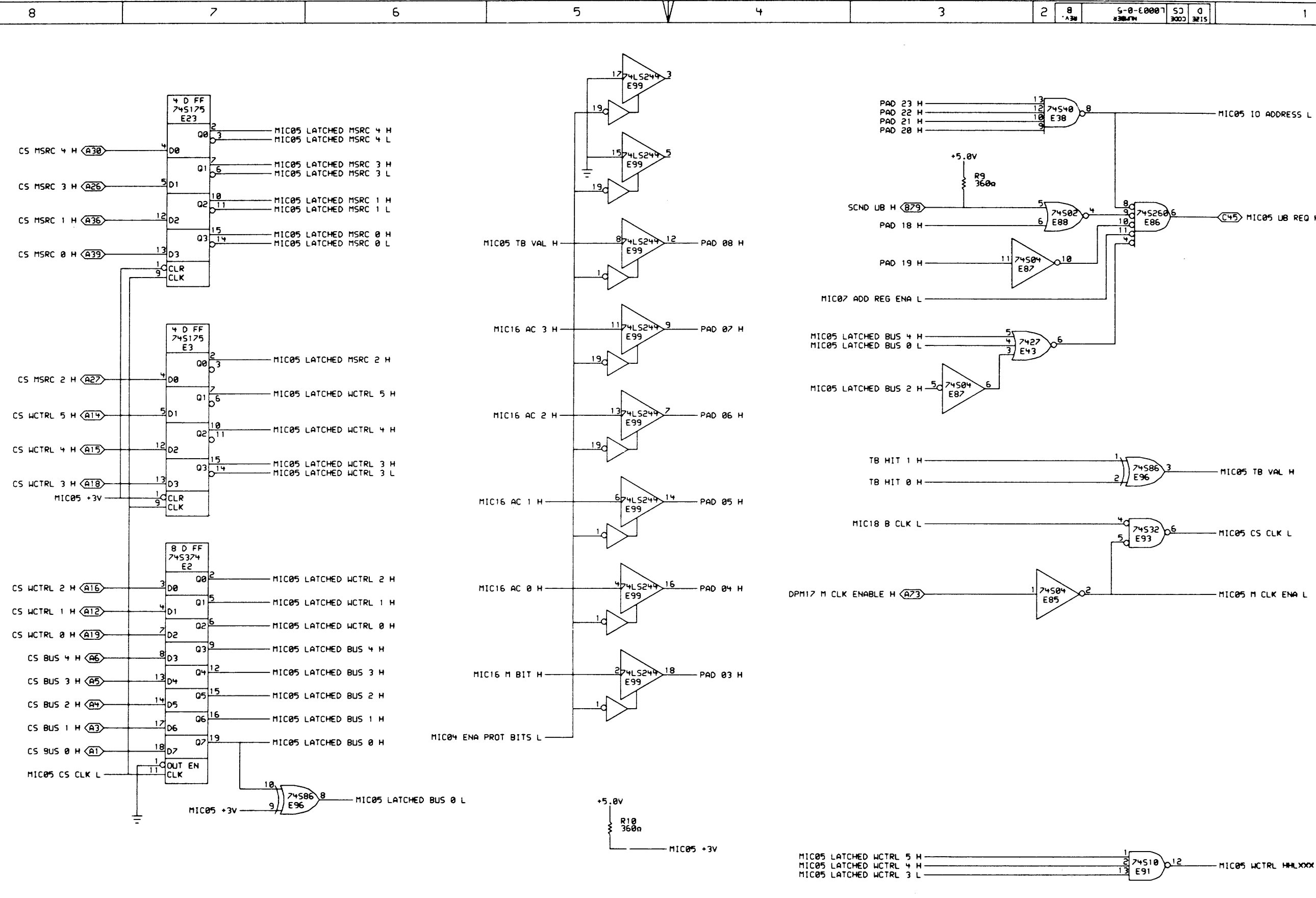
REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN. <i>GJM</i>	DATE	ENG.	DATE	TITLE: DATA ROUTING & ALIGNMENT SHT2
	CHK'D.	DATE	BOARD LOCATION: AC3	SHEET 1 OF 1	SIZE CODE NUMBER REV. D CS L0003-0-2 B
FIRST USED ON OPTION/MODEL: 11/750			NEXT HIGHER ASSEMBLY: B-DD-L0003-0-0		



REVISIONS		
CHK	CHANGE NO.	REV.

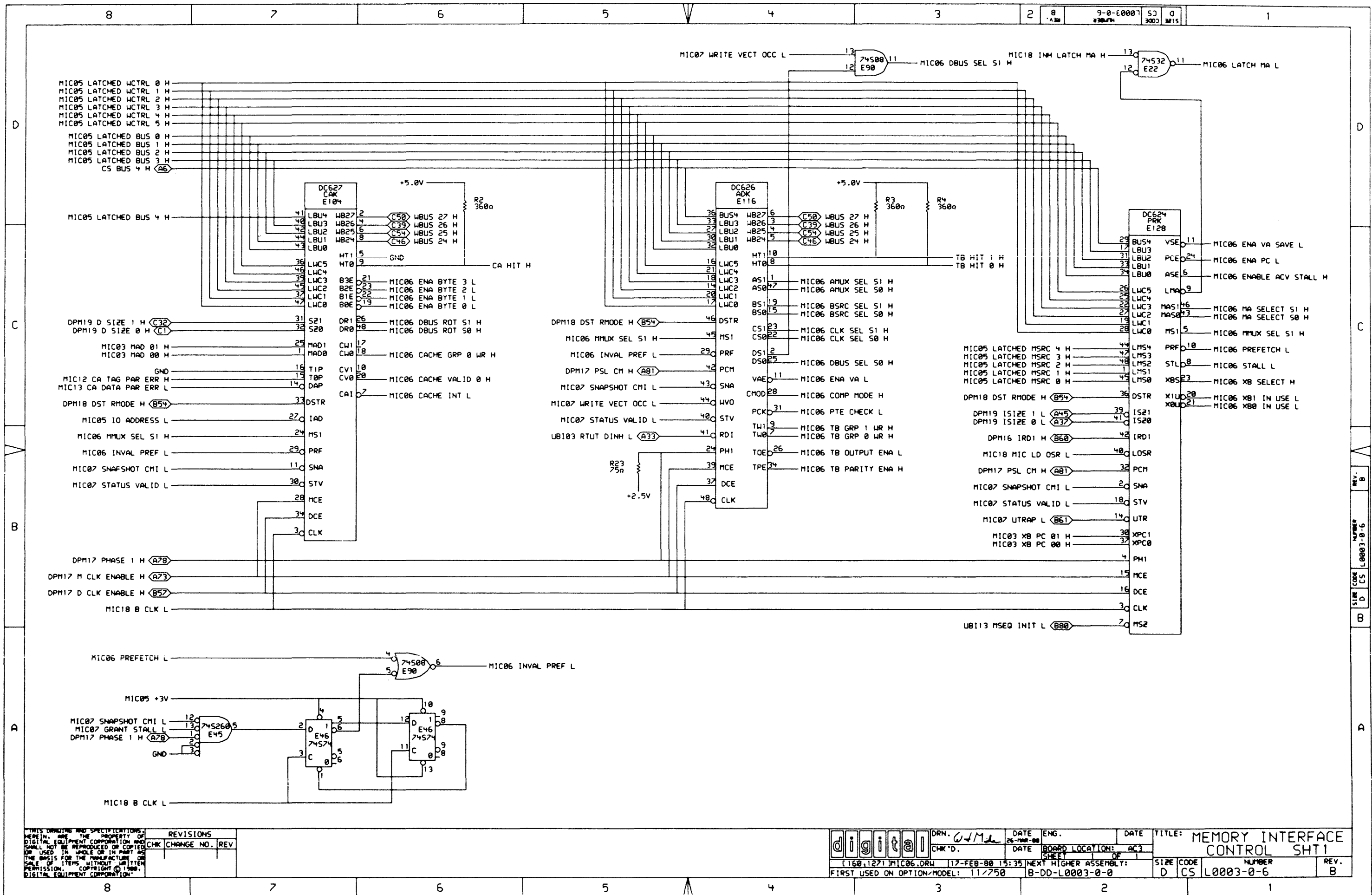
digital	DRN. <i>WJM</i>	DATE 26-FEB-80	ENG.	DATE	TITLE: MEMORY ADDRESS
	CHK'D.	DATE 17-FEB-80 15:30	BOARD LOCATION: AC3	SHEET 1 OF 1	
FIRST USED ON OPTION/MODEL: 11/750			NEXT HIGHER ASSEMBLY: B-DD-L0003-0-0	SIZE CODE D CS	NUMBER L0003-0-3
				REV. B	



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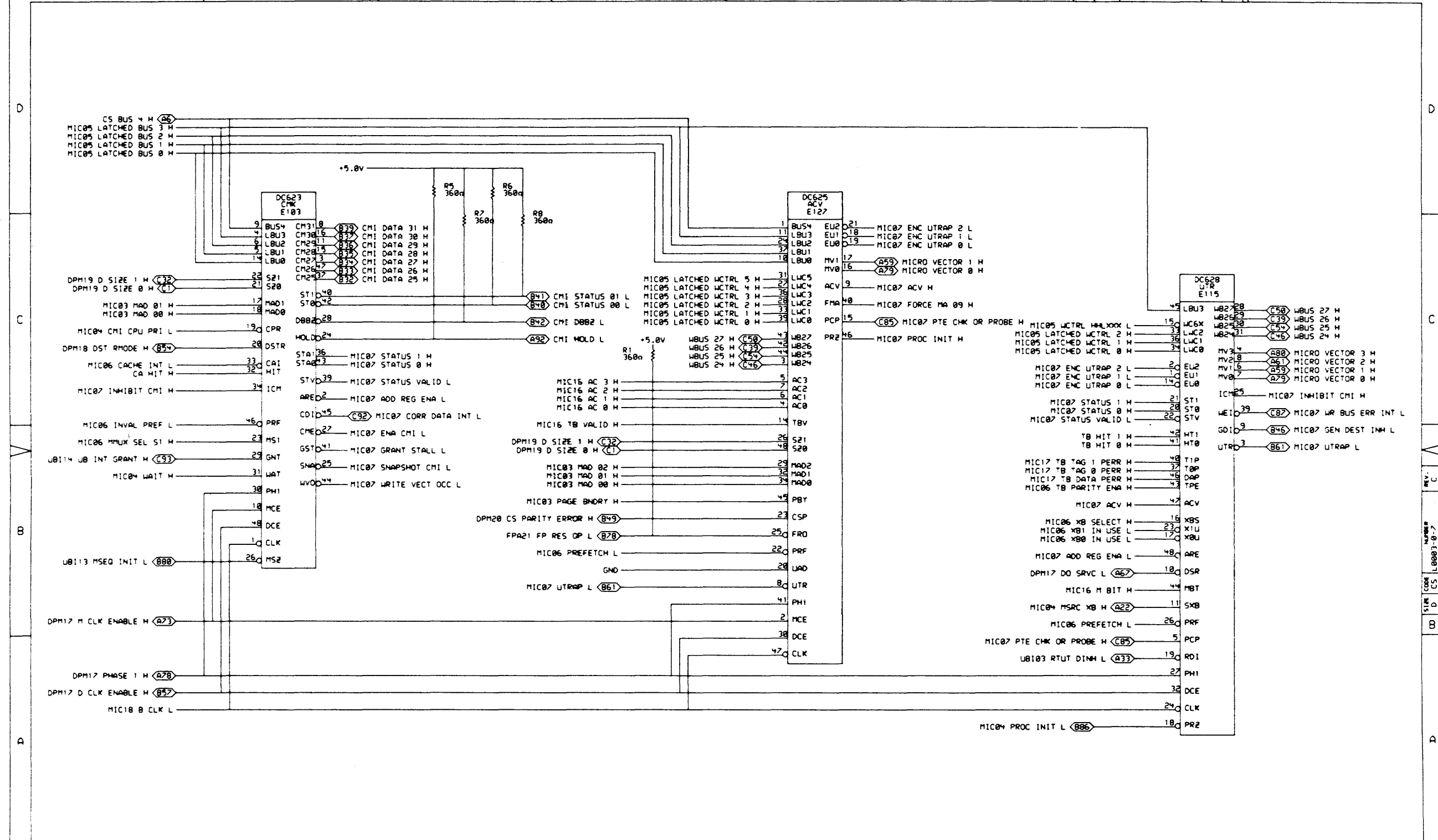
REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN. <i>WJM</i>	DATE 25-MAR-80	ENG.	DATE	TITLE: MEMORY INTERFACE CS LATCHES
	CHK'D.	DATE	BOARD LOCATION: AC3	SHEET 1 OF 1	
[160,1271]MIC05.DRW		17-FEB-80 15:34	NEXT HIGHER ASSEMBLY:	SIZE CODE D CS	NUMBER L0003-0-5
FIRST USED ON OPTION/MODEL: 11/750		B-DD-L0003-0-0		REV. B	



REVISIONS	
CHK	CHANGE NO. REV

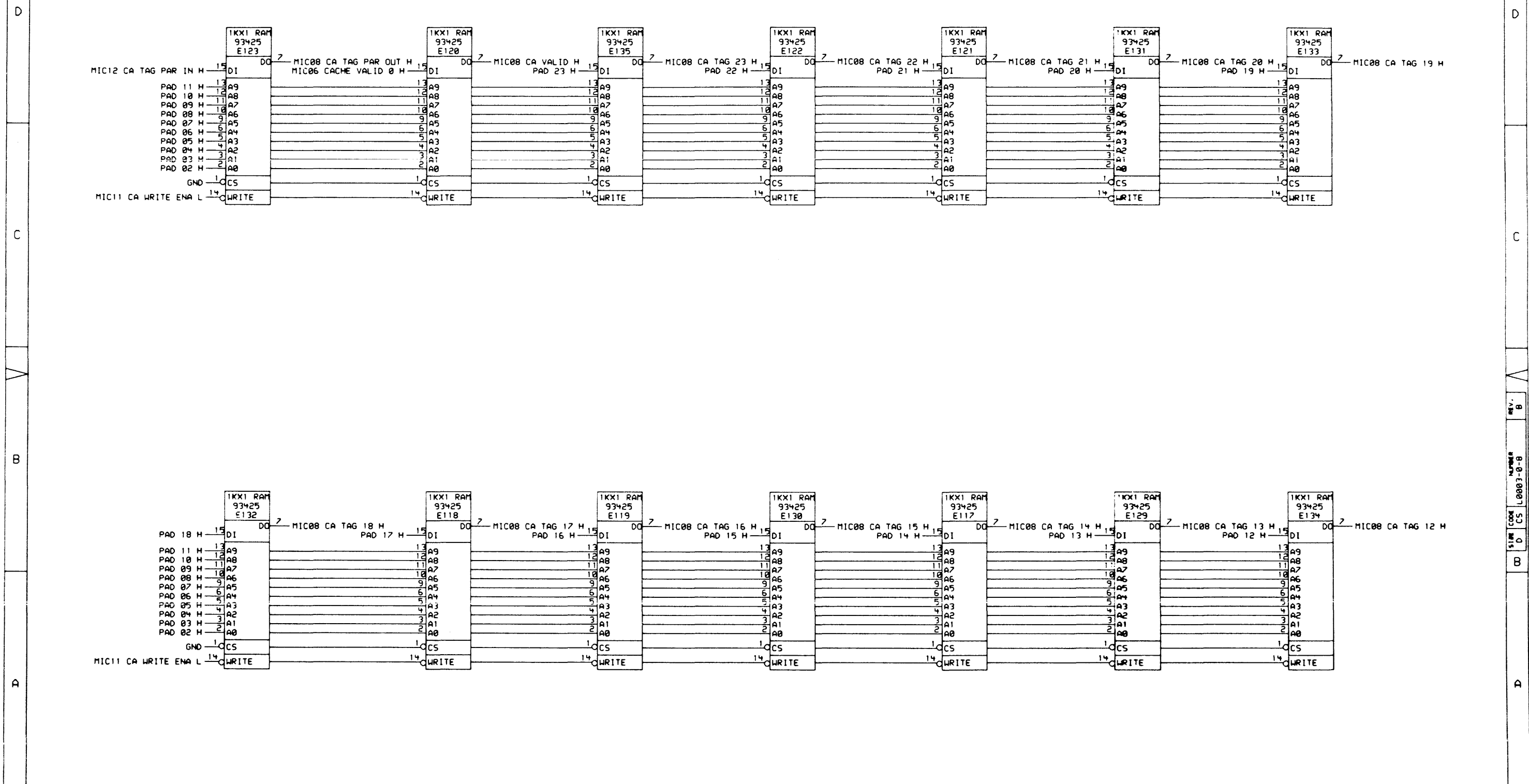
digital	DRN. W.M.	DATE 26-MAR-80	ENG.	DATE	TITLE: MEMORY INTERFACE CONTROL SHT1
	CHK'D.	DATE	BOARD LOCATION: AC3	SHEET	OF
FIRST USED ON OPTION/MODEL: 11/250			NEXT HIGHER ASSEMBLY: B-DD-L0003-0-0		SIZE CODE D CS
NUMBER L0003-0-6				REV. B	



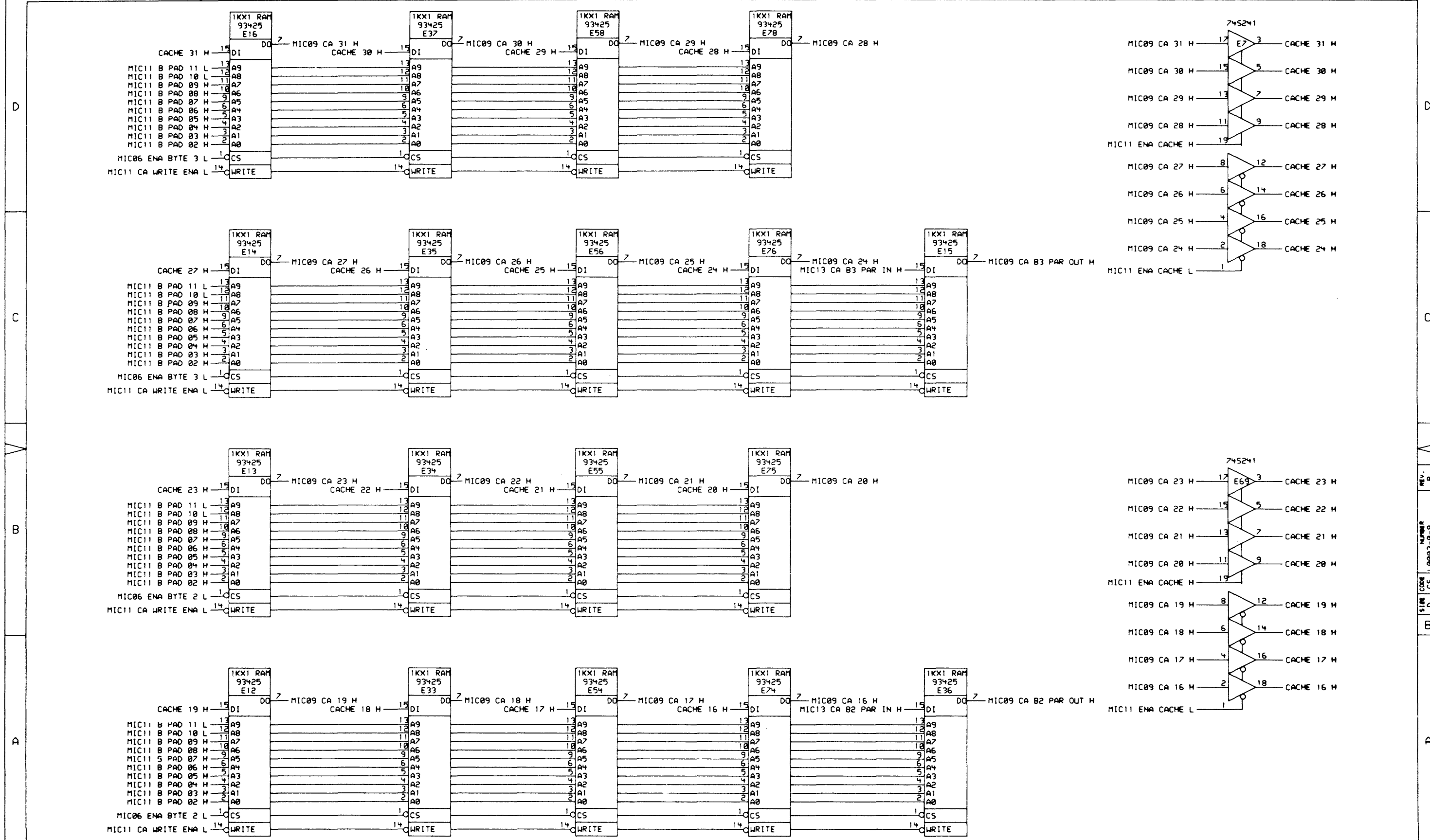
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REVISIONS	
CHK	CHANGE NO. REV

digital	DRN. <i>WJM</i>	DATE	ENG.	DATE	TITLE: MEMORY INTERFACE CONTROL SH2
	CHK'D.	DATE	BOARD LOCATION: AC3	SHEET	OF 1
[160.1271] MIC07.DRW		06-NOV-80 18:13	NEXT HIGHER ASSEMBLY:	SIZE	CODE
FIRST USED ON OPTION/MODEL: 11/250		B-DD-L0003-0-0		D	CS L0003-0-7
					NUMBER
					REV. C



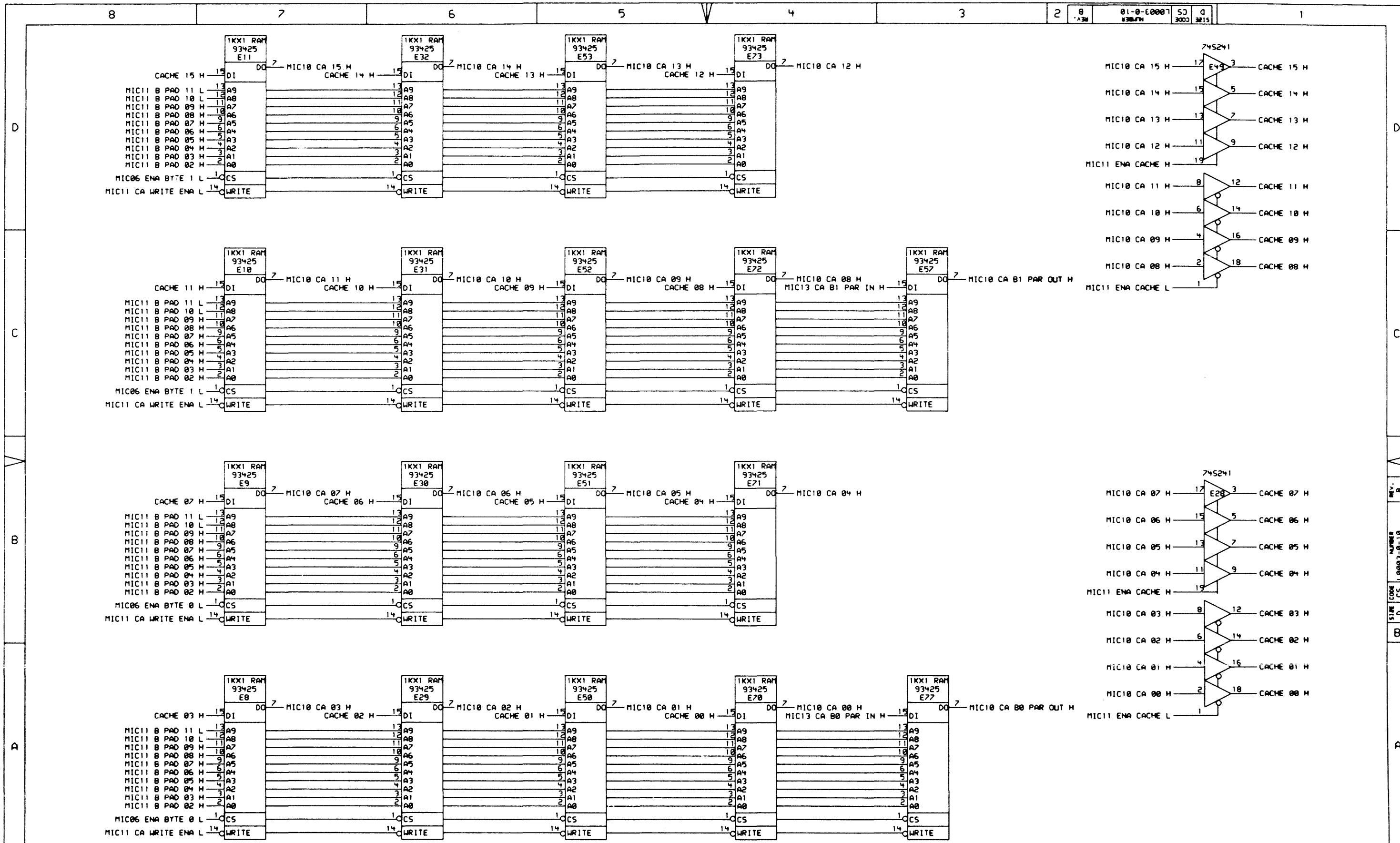
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[160,1271]MIC08.DRW 17-FEB-88 15:38 NEXT HIGHER ASSEMBLY: B-DD-L0003-0-0 FIRST USED ON OPTION/MODEL: 11/750						SIZE CODE D CS		NUMBER L0003-0-8		REV. B	



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REVISIONS		
CHK	CHANGE NO.	REV

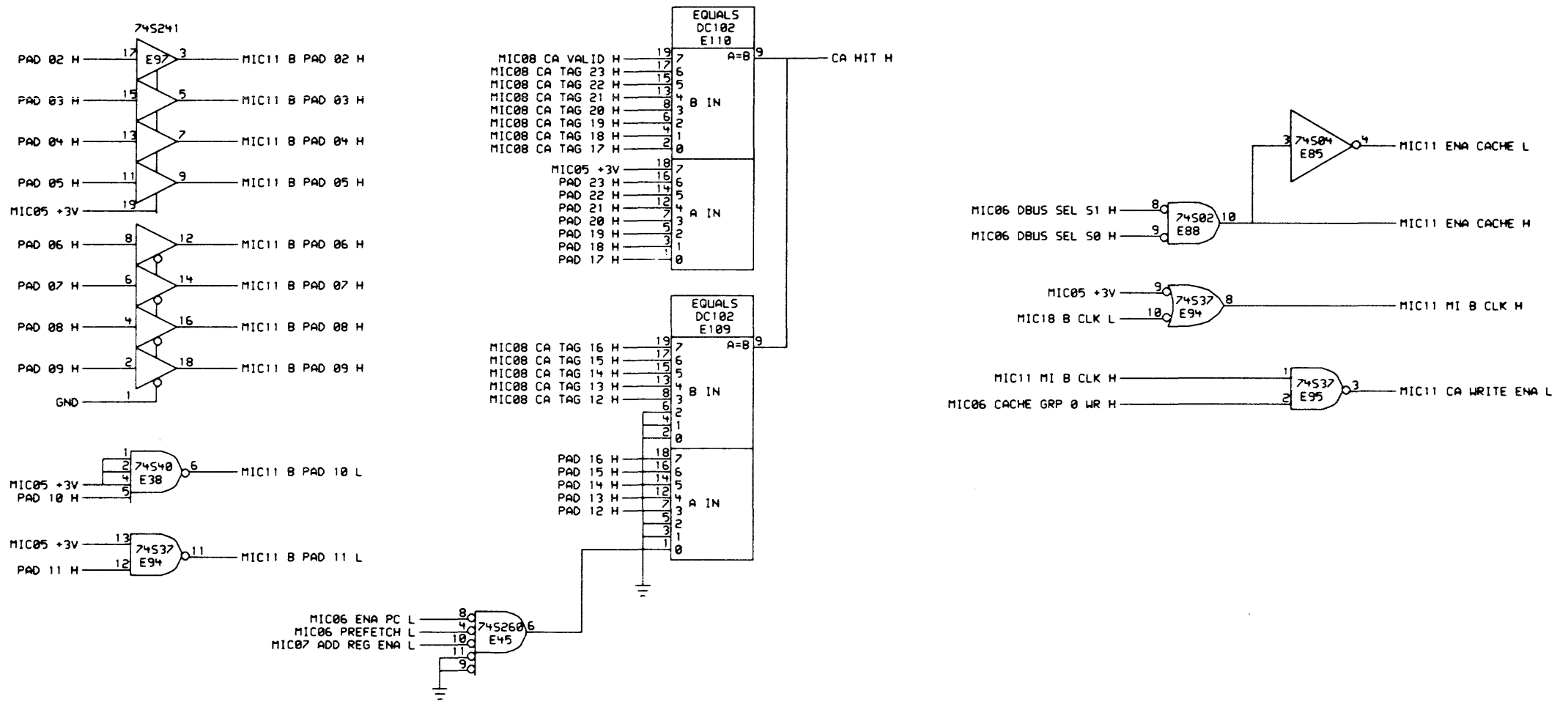
digit@l	DRN. <i>W.M.</i>	DATE 26-NOV-80	ENG.	DATE	TITLE: CACHE DATA STORE BYTES 3 & 2
	CHK'D.	DATE	BOARD LOCATION: AC3	SHEET 1 OF 1	
C160,1271 MIC09.DRW 117-FEB-80 15:40		NEXT HIGHER ASSEMBLY: B-DD-L0003-0-0		SIZE CODE D CS	NUMBER L0003-0-9
FIRST USED ON OPTION/MODEL: 11/750					REV. B



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REVISIONS	
CHK	CHANGE NO. REV

	DRN. <i>W.M.</i>	DATE ENG. 26-MAR-80	DATE	TITLE: CACHE DATA STORE
	CHK'D.	DATE BOARD LOCATION: AC3	SHEET 1 OF 1	BYTES 1 & 0
[160,127] MIC10.DRW 17-FEB-80 15:42 NEXT HIGHER ASSEMBLY:		SIZE CODE NUMBER REV.		
FIRST USED ON OPTION MODEL: 11750		B-DD-L0003-0-0		D CS L0003-0-10 B



D
C
B
A

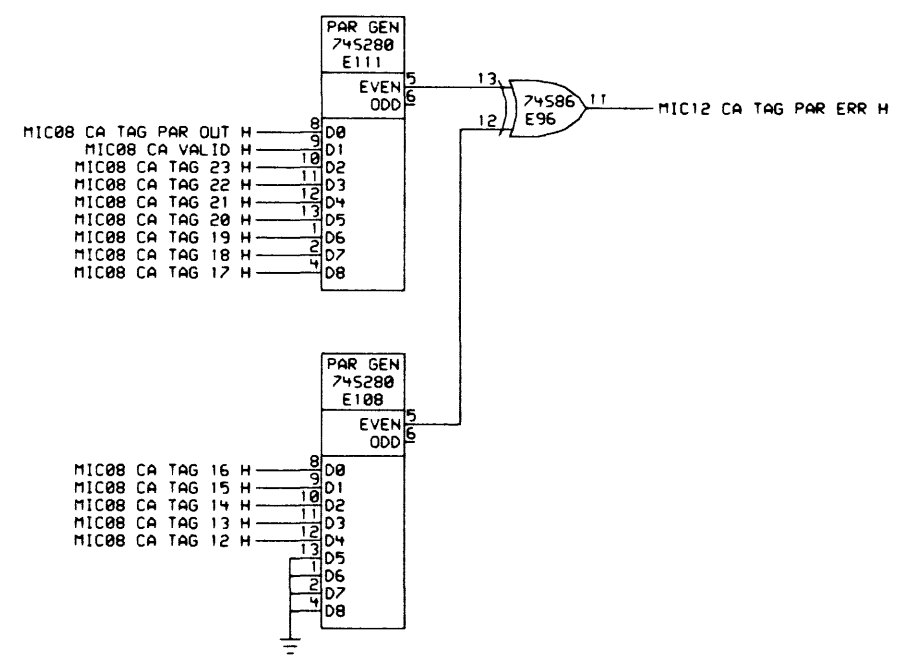
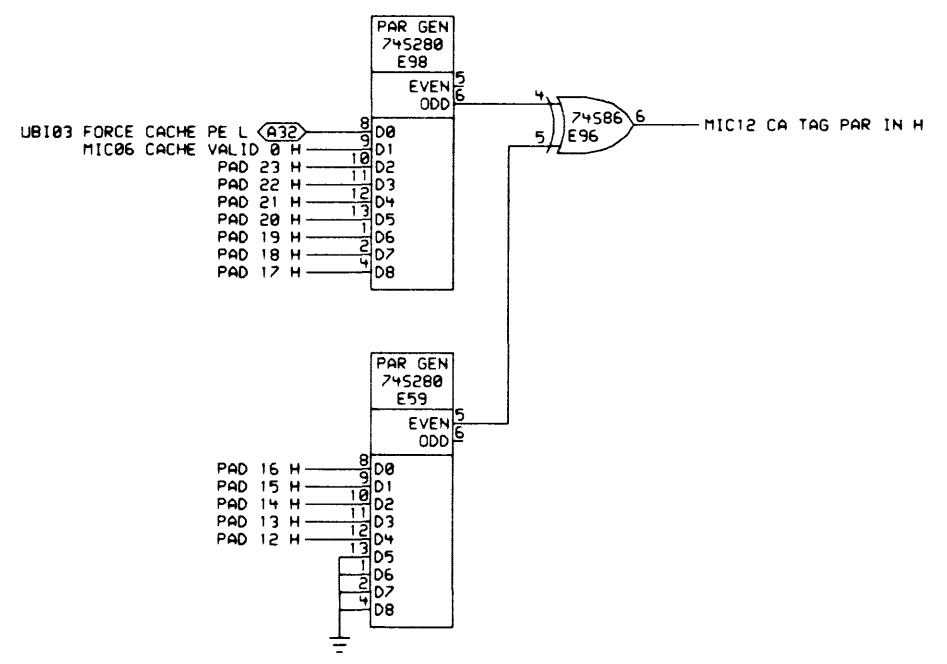
D
C
B
A

REV. B
 NUMBER L0003-0-11
 CS D B

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REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN. W.M.	DATE 26-MAR-80	ENG.	DATE	TITLE: CACHE CONTROL
	CHK'D.	DATE	BOARD LOCATION: AC3	SHEET 1 OF 1	
[160,127] MIC11.DRW		17-FEB-80 15:44	NEXT HIGHER ASSEMBLY:	SIZE CODE D CS	NUMBER L0003-0-11
FIRST USED ON OPTION/MODEL: 11/750		B-DD-L0003-0-0		REV. B	



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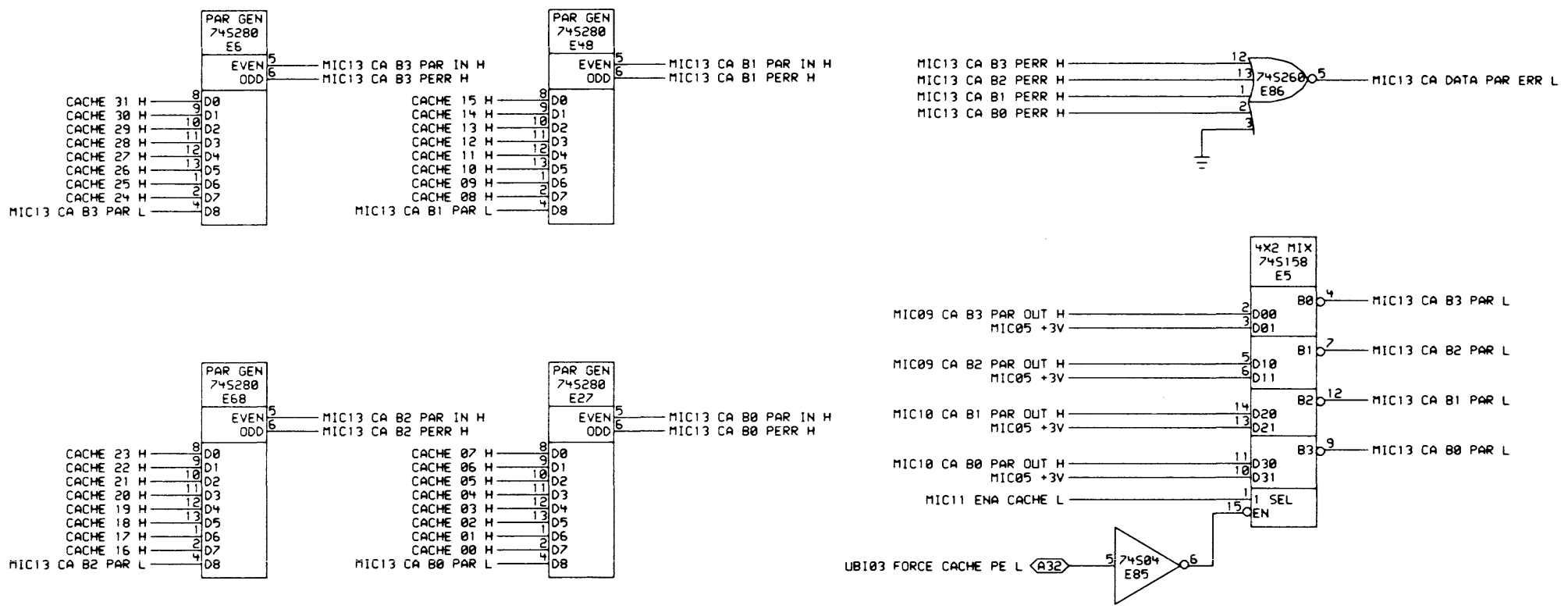
REVISIONS		
CHK	CHANGE NO.	REV

digital

DRN. *W.M.* DATE *26-FEB-80* ENG. DATE
CHK'D. DATE BOARD LOCATION: *AC3*
SHEET 1 OF 1
NEXT HIGHER ASSEMBLY: *B-DD-L0003-0-0*

TITLE: **CACHE TAG PARITY**
FIRST USED ON OPTION/MODEL: *11/750*
SIZE CODE NUMBER REV.
D CS L0003-0-12 B

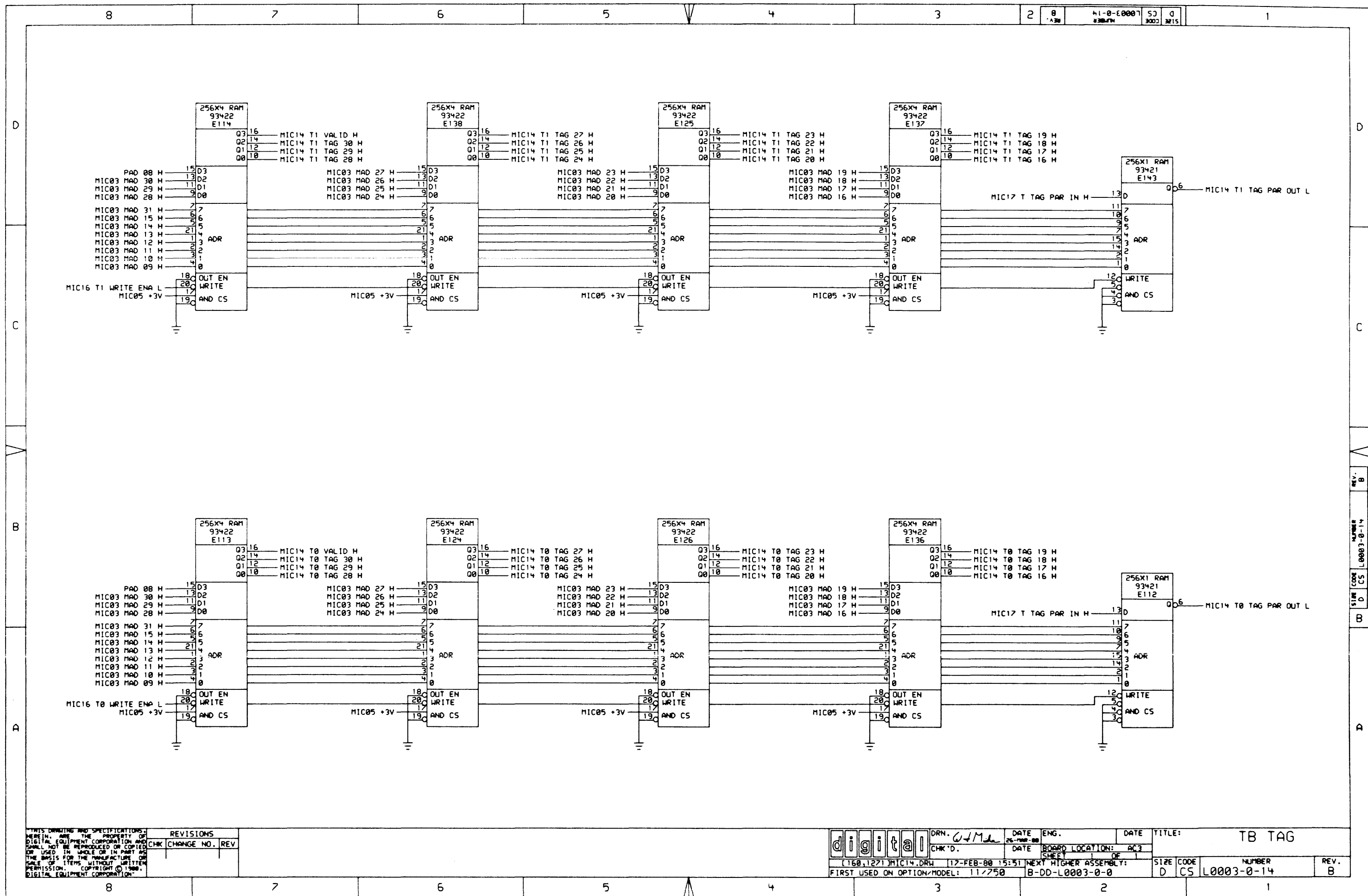
REV. B
NUMBER L0003-0-12
CS L0003-0-12
D



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REVISIONS		
CHK	CHANGE NO.	REV

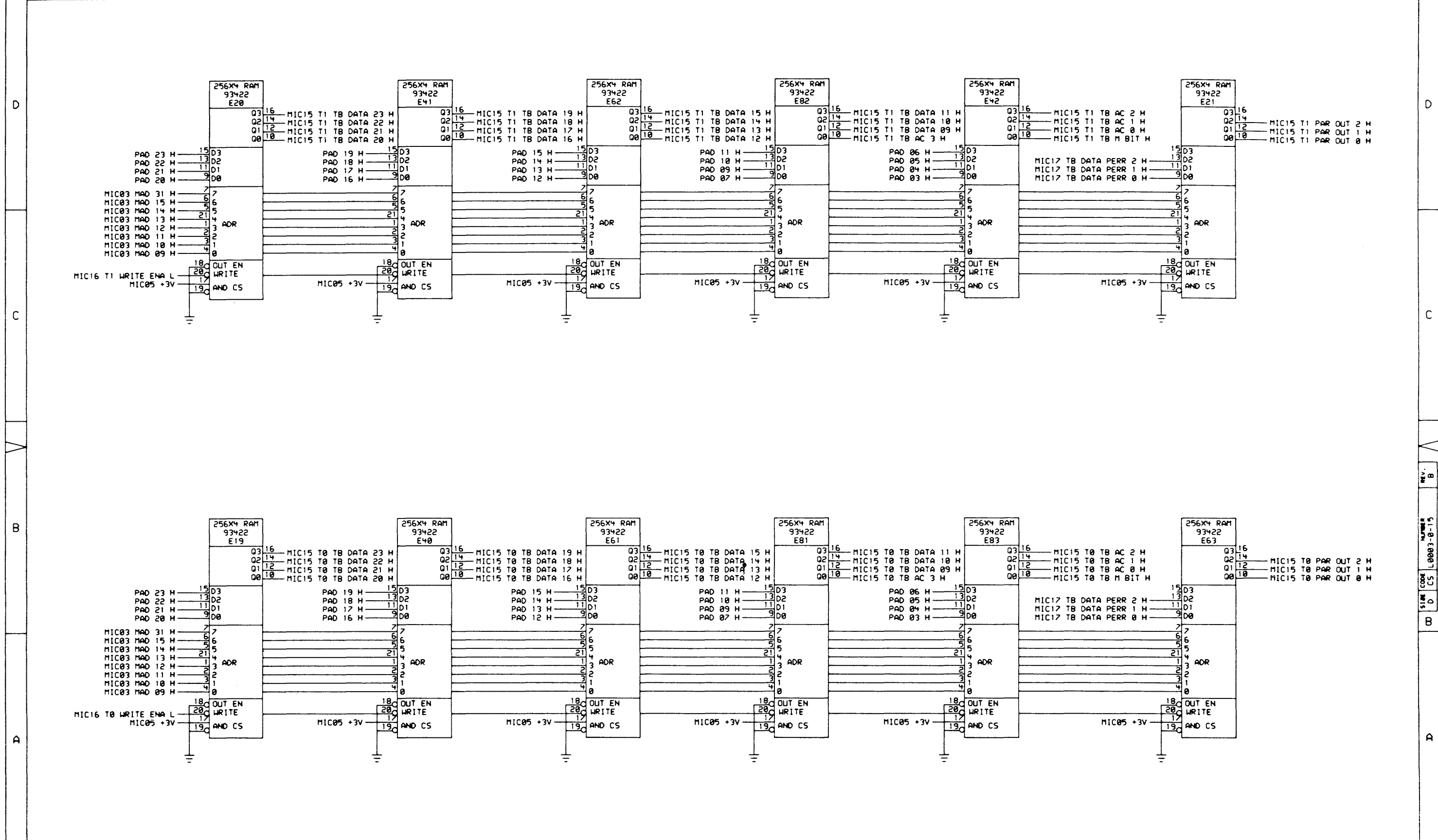
digital	DRN. WJM	DATE 26-MAR-80	ENG.	DATE	TITLE: CACHE DATA PARITY
	CHK'D.	DATE 17-FEB-80	15:50	BOARD LOCATION: AC3	SHEET 1 OF 1
FIRST USED ON OPTION/MODEL: 11/750			NEXT HIGHER ASSEMBLY: B-DD-L0003-0-0		SIZE CODE D CS
NUMBER L0003-0-13				REV. B	



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CHK	CHANGE NO. REV.

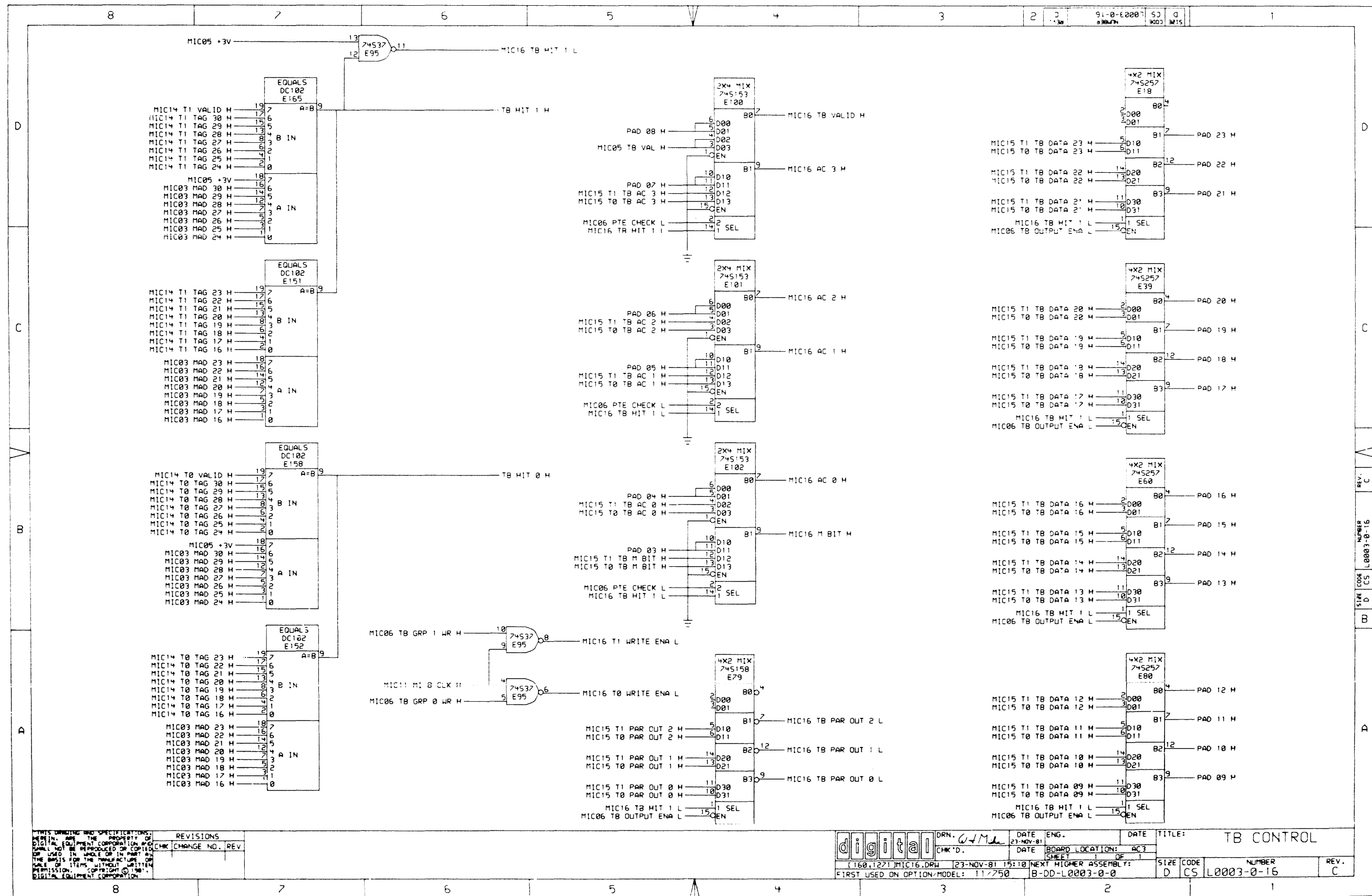
digital	DRN. <i>WJM</i>	DATE 26-MAR-80	ENG.	DATE	TITLE: TB TAG
	CHK'D.	DATE	BOARD LOCATION: AC3	SHEET 1 OF 1	
FIRST USED ON OPTION/MODEL: 117750 B-DD-L0003-0-0					SIZE CODE D CS
					NUMBER L0003-0-14
					REV. B



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REVISIONS		
CHK	CHANGE NO.	REV

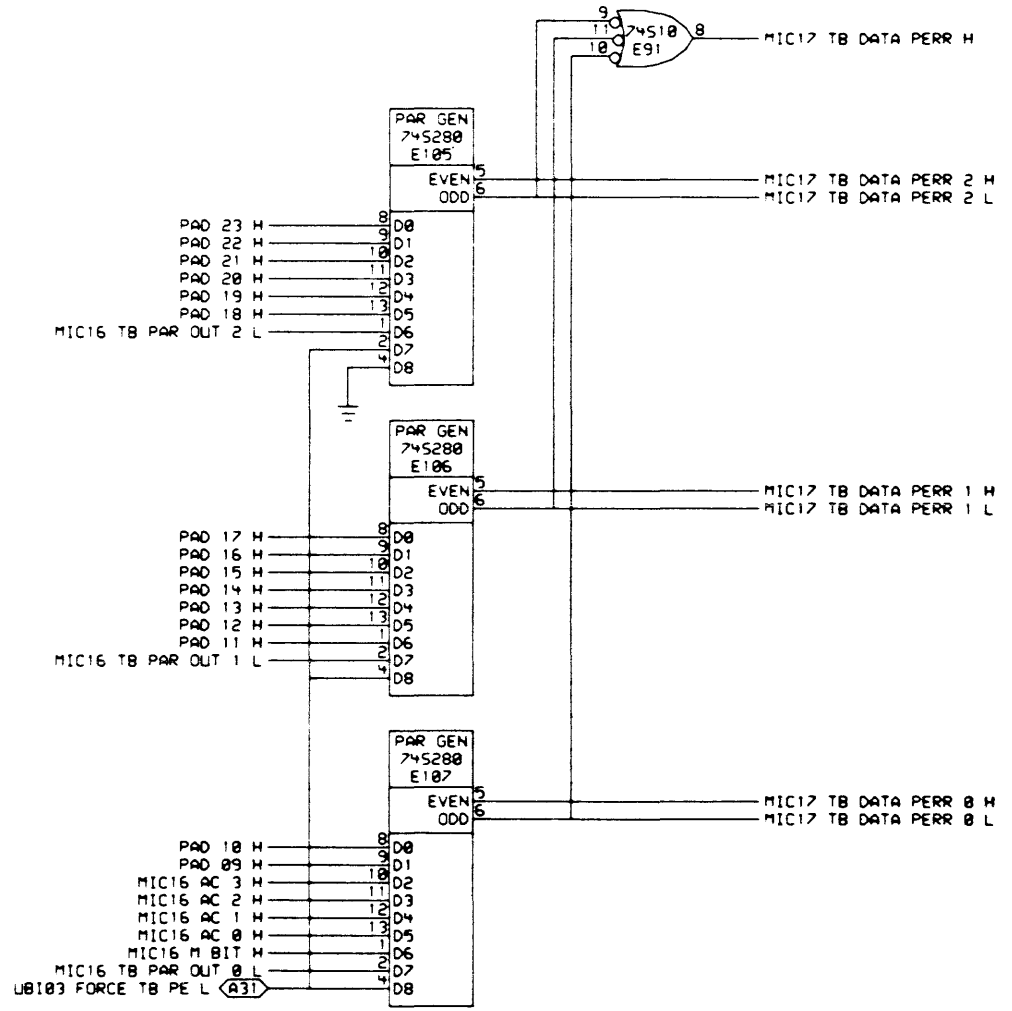
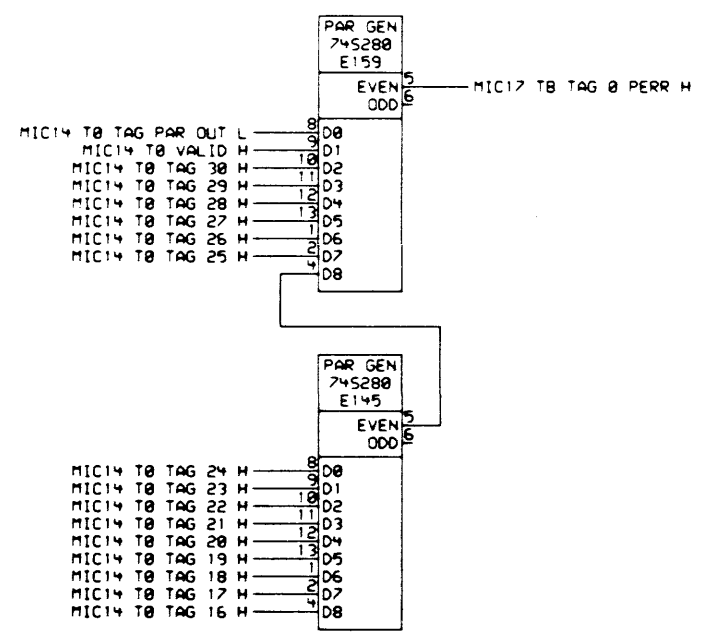
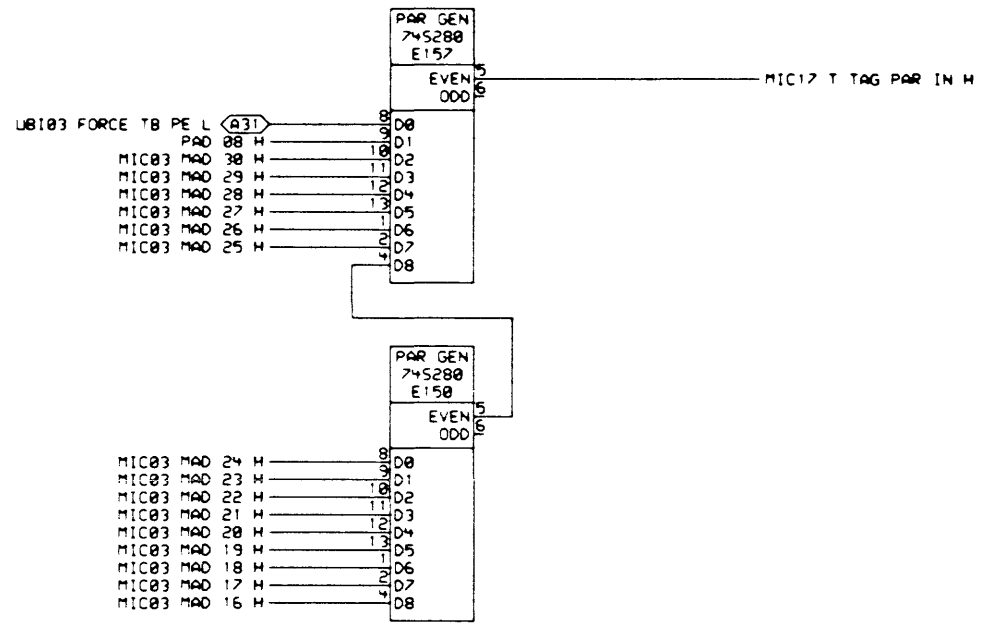
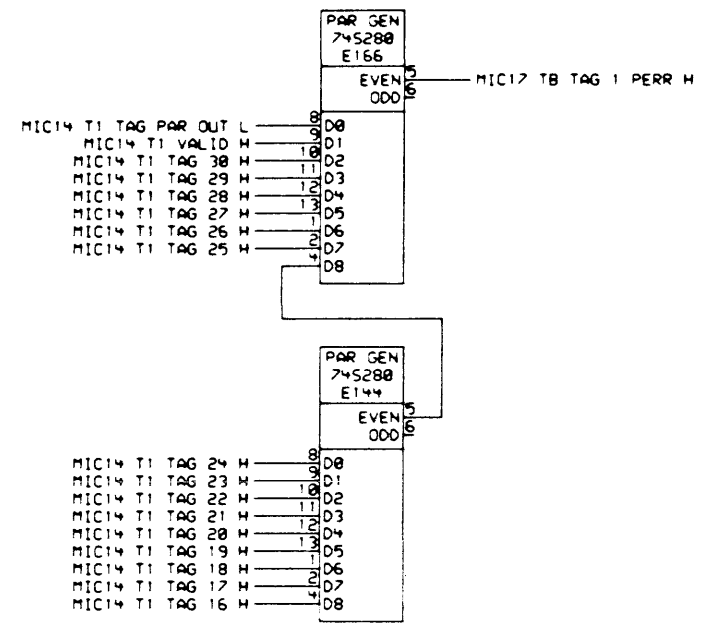
digital	DRN. <i>W.M.</i>	DATE	ENG.	DATE	TITLE: TB DATA STORE
	CHK'D.	26-MAR-80			
[160,127] MIC15.DRW		12-FEB-80 15:52	NEXT HIGHER ASSEMBLY:	SIZE	CODE
FIRST USED ON OPTION MODEL: 11/750			B-DD-L0003-0-0	D	CS
				NUMBER	REV.
				L0003-0-15	B



REVISIONS	
CHANGE NO.	REV

digital	DRN. <i>WMA</i>	DATE	ENG.	DATE	TITLE: TB CONTROL
	CHK'D.	23-NOV-81	AC3	AC3	
C160.1271 MIC16.DRW		23-NOV-81 15:10	NEXT HIGHER ASSEMBLY:	SIZE CODE	NUMBER
FIRST USED ON OPTION/MODEL: 11/750		B-DD-L0003-0-0		D CS	L0003-0-16
				REV. C	

COMETCHIPS, C160.1271 MIC16.DPL, SCALE 2, 10" RELEASE BOX
 COMETCHIPS MIC16.PLO(C160.1271) 23-NOV-81 15:13



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REVISIONS	
CHK	CHANGE NO. REV

digital	DRN. <i>WJM</i>	DATE 26-FEB-80	ENG.	DATE	TITLE: TB PARITY
	CHK'D.	DATE	BOARD LOCATION: AC3	SHEET	OF
[160,127] MIC17.DRW		17-FEB-80 15:55	NEXT HIGHER ASSEMBLY:		SIZE CODE
FIRST USED ON OPTION MODEL: 11/750		B-DD-L0003-0-0		D	CS
NUMBER L0003-0-17				REV. B	

SIGNAL NAME	PAGE NUMBER(S)	SIGNAL NAME	PAGE NUMBER(S)	SIGNAL NAME	PAGE NUMBER(S)
CA HIT H	07,06,11	CMI DATA 10 H	01	DPM17 M CLK ENABLE H	18,04,05,07,06
CACHE 00 H	13,10,01	CMI DATA 11 H	01	DPM17 PHASE 1 H	06,04,07
CACHE 01 H	13,10,01	CMI DATA 12 H	02	DPM17 PSL CM H	18,06
CACHE 02 H	13,10,01	CMI DATA 13 H	02	DPM18 DST RMODE H	07,06
CACHE 03 H	10,13,01	CMI DATA 14 H	02	DPM19 D SIZE 0 H	07,06
CACHE 04 H	13,10,02	CMI DATA 15 H	02	DPM19 D SIZE 1 H	07,06
CACHE 05 H	13,10,02	CMI DATA 16 H	01	DPM19 ISIZE 0 L	04,06
CACHE 06 H	13,10,02	CMI DATA 17 H	01	DPM19 ISIZE 1 L	04,06
CACHE 07 H	10,13,02	CMI DATA 18 H	01	DPM20 CS PARITY ERROR H	07
CACHE 08 H	13,10,01	CMI DATA 19 H	01	DPM22 V OUT H	04
CACHE 09 H	13,10,01	CMI DATA 20 H	02	FPA21 FP RES OP L	07
CACHE 10 H	10,13,01	CMI DATA 21 H	02	MBUS 00 L	01
CACHE 11 H	10,13,01	CMI DATA 22 H	02	MBUS 01 L	01
CACHE 12 H	13,10,02	CMI DATA 23 H	02	MBUS 02 L	01
CACHE 13 H	13,10,02	CMI DATA 24 H	01	MBUS 03 L	01
CACHE 14 H	10,13,02	CMI DATA 25 H	07,01	MBUS 04 L	02
CACHE 15 H	10,13,02	CMI DATA 26 H	07,01	MBUS 05 L	02
CACHE 16 H	13,09,01	CMI DATA 27 H	07,01	MBUS 06 L	02
CACHE 17 H	09,13,01	CMI DATA 28 H	07,02	MBUS 07 L	02
CACHE 18 H	09,13,01	CMI DATA 29 H	07,02	MBUS 08 L	01
CACHE 19 H	09,13,01	CMI DATA 30 H	07,02	MBUS 09 L	01
CACHE 20 H	13,09,02	CMI DATA 31 H	07,02	MBUS 10 L	01
CACHE 21 H	09,13,02	CMI DBB2 L	07	MBUS 11 L	01
CACHE 22 H	09,13,02	CMI HOLD L	07	MBUS 12 L	02
CACHE 23 H	09,13,02	CMI STATUS 00 L	07	MBUS 13 L	02
CACHE 24 H	13,09,01	CMI STATUS 01 L	07	MBUS 14 L	02
CACHE 25 H	13,09,01	CMI WAIT L	04	MBUS 15 L	04,02
CACHE 26 H	13,09,01	CS BUS 0 H	05	MBUS 16 L	01
CACHE 27 H	13,09,01	CS BUS 1 H	05	MBUS 17 L	01
CACHE 28 H	13,09,02	CS BUS 2 H	05	MBUS 18 L	01
CACHE 29 H	13,09,02	CS BUS 3 H	05	MBUS 19 L	01
CACHE 30 H	13,09,02	CS BUS 4 H	05,07,06	MBUS 20 L	02
CACHE 31 H	13,09,02	CS MSRC 0 H	05	MBUS 21 L	02
CMI ARB 1 L	04	CS MSRC 1 H	05	MBUS 22 L	02
CMI ARB 2 L	04	CS MSRC 2 H	05	MBUS 23 L	02
CMI ARB 3 L	04	CS MSRC 3 H	05	MBUS 24 L	01
CMI ARB 4 L	04	CS MSRC 4 H	05	MBUS 25 L	01
CMI ARB 5 L	04	CS WCTRL 0 H	05	MBUS 26 L	01
CMI ARB 6 L	04	CS WCTRL 1 H	05	MBUS 27 L	01
CMI ARB 7 L	04	CS WCTRL 2 H	05	MBUS 28 L	02
CMI DATA 00 H	01	CS WCTRL 3 H	05	MBUS 29 L	02
CMI DATA 01 H	01	CS WCTRL 4 H	05	MBUS 30 L	02
CMI DATA 02 H	01	CS WCTRL 5 H	05	MBUS 31 L	02
CMI DATA 03 H	01	DPM11 MCS TMR L	04	MIC01 XBUF 00 H	01
CMI DATA 04 H	02	DPM14 LD OSR L	18	MIC01 XBUF 01 H	01
CMI DATA 05 H	02	DPM16 IRDI H	04,06	MIC01 XBUF 02 H	01
CMI DATA 06 H	02	DPM17 B CLK L	18	MIC01 XBUF 03 H	01
CMI DATA 07 H	02	DPM17 B CLK ENABLE H	07,06	MIC02 XBUF 04 H	02
CMI DATA 08 H	01	DPM17 DO SRVC L	07	MIC02 XBUF 05 H	02
CMI DATA 09 H	01	DPM17 INSTR FETCH H	04,18	MIC02 XBUF 06 H	02

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	SIZE CODE D CS L0003-0-19		NUMBER REV. C

SIGNAL NAME	PAGE NUMBER(S)
MIC02 XBUF 07 H	02
MIC03 MAD 00 H	07,06,01,03
MIC03 MAD 01 H	07,06,03,01
MIC03 MAD 02 H	07,03,01
MIC03 MAD 03 H	03,01
MIC03 MAD 04 H	03,02
MIC03 MAD 05 H	02,03
MIC03 MAD 06 H	03,02
MIC03 MAD 07 H	03,02
MIC03 MAD 08 H	01,03
MIC03 MAD 09 H	15,14,03,01
MIC03 MAD 10 H	15,14,03,01
MIC03 MAD 11 H	15,14,01,03
MIC03 MAD 12 H	15,14,03,02
MIC03 MAD 13 H	15,14,02,03
MIC03 MAD 14 H	15,14,03,02
MIC03 MAD 15 H	15,14,02,03
MIC03 MAD 16 H	14,01,17,16,03
MIC03 MAD 17 H	14,17,16,01,03
MIC03 MAD 18 H	14,17,16,03,01
MIC03 MAD 19 H	14,01,17,16,03
MIC03 MAD 20 H	14,17,16,02,03
MIC03 MAD 21 H	14,02,17,16,03
MIC03 MAD 22 H	14,17,16,03,02
MIC03 MAD 23 H	14,02,17,16,03
MIC03 MAD 24 H	14,01,17,16,03
MIC03 MAD 25 H	14,01,17,16,03
MIC03 MAD 26 H	14,17,16,01,03
MIC03 MAD 27 H	14,01,17,16,03
MIC03 MAD 28 H	14,02,17,16,03
MIC03 MAD 29 H	14,02,17,16,03
MIC03 MAD 30 H	14,17,16,02,03
MIC03 MAD 31 H	15,14,02,03
MIC03 PAGE BNDRY H	07,03
MIC03 XB PC 00 H	06,01,02,03
MIC03 XB PC 01 H	06,01,02,03
MIC04 ASRC SEL S0 H	04,03
MIC04 ASRC SEL S1 H	04,03
MIC04 ASRC SEL S2 H	04,03
MIC04 CLK SMDR L	04,01,02
MIC04 CMI CPU PRI L	04,07
MIC04 ENA PC BACKUP L	04,03
MIC04 ENA PROT BITS L	04,05
MIC04 ENA SMDR L	04,01,02
MIC04 TRD1 L	18,04
MIC04 LATCHED MBUS 15 L	04
MIC04 MBUS ENA H	04,01,02
MIC04 MEM STALL H	04
MIC04 MSRC MI H	04
MIC04 MSRC MI L	04

SIGNAL NAME	PAGE NUMBER(S)
MIC04 MSRC XB H	04,07
MIC04 PHASE 1 L	04
MIC04 PROC INIT L	04,07
MIC04 RTUT DINH H	04
MIC04 SMDR DEC H	04
MIC04 STATUS VALID H	04
MIC04 V OUT H	04
MIC04 V WAIT H	04,07
MIC05 +3V	05,13,15,18,11,06,16,14,01,03
MIC05 CS CLK L	05
MIC05 IO ADDRESS L	05,06,18
MIC05 LATCHED BUS 0 H	05,07,06
MIC05 LATCHED BUS 0 L	05,18
MIC05 LATCHED BUS 1 H	05,04,07,06
MIC05 LATCHED BUS 2 H	05,07,06
MIC05 LATCHED BUS 3 H	05,18,04,07,06
MIC05 LATCHED BUS 4 H	05,18,06
MIC05 LATCHED MSRC 0 H	05,04,06
MIC05 LATCHED MSRC 0 L	05,04
MIC05 LATCHED MSRC 1 H	05,06
MIC05 LATCHED MSRC 1 L	05,04
MIC05 LATCHED MSRC 2 H	05,04,06,01,02
MIC05 LATCHED MSRC 3 H	05,04,06
MIC05 LATCHED MSRC 3 L	05,04
MIC05 LATCHED MSRC 4 H	05,06
MIC05 LATCHED MSRC 4 L	05,04
MIC05 LATCHED WCTRL 0 H	05,06,07
MIC05 LATCHED WCTRL 1 H	05,04,06,07
MIC05 LATCHED WCTRL 2 H	05,06,07
MIC05 LATCHED WCTRL 3 H	05,06,07
MIC05 LATCHED WCTRL 3 L	05
MIC05 LATCHED WCTRL 4 H	05,06,07
MIC05 LATCHED WCTRL 5 H	05,06,07
MIC05 M CLK ENA L	18,05
MIC05 TB VAL H	05,16
MIC05 UB REQ H	04,05
MIC05 WCTRL H#LXXX L	05,07
MIC06 AMUX SEL S0 H	06,01,02
MIC06 AMUX SEL S1 H	06,01,02
MIC06 BSRC SEL S0 H	06,03
MIC06 BSRC SEL S1 H	06,03
MIC06 CACHE GRP 0 LR H	11,06
MIC06 CACHE INT L	07,06
MIC06 CACHE VALID 0 H	12,06,08
MIC06 CLK SEL S0 H	06,01,02
MIC06 CLK SEL S1 H	06,01,02
MIC06 COMP MODE H	06,03
MIC06 DBUS ROT S0 H	06,01,02
MIC06 DBUS ROT S1 H	06,01,02
MIC06 DBUS SEL S0 H	11,06,01,02

SIGNAL NAME	PAGE NUMBER(S)
MIC06 DBUS SEL S1 H	11,06,01,02
MIC06 ENA BYTE 0 L	10,06
MIC06 ENA BYTE 1 L	10,06
MIC06 ENA BYTE 2 L	09,06
MIC06 ENA BYTE 3 L	09,06
MIC06 ENA PC L	18,11,06,03
MIC06 ENA VA L	06,03
MIC06 ENA VA SAVE L	06,03
MIC06 ENABLE ACV STALL H	04,06
MIC06 INVAL PREF L	06,07
MIC06 LATCH MA L	06,03
MIC06 MA SELECT S0 H	06,03
MIC06 MA SELECT S1 H	06,03
MIC06 MMUX SEL S1 H	04,07,06,01,02
MIC06 PREFETCH L	11,06,07
MIC06 PTE CHECK L	16,06
MIC06 STALL L	04,06
MIC06 TB GRP 0 LR H	16,06
MIC06 TB GRP 1 LR H	16,06
MIC06 TB OUTPUT ENA L	16,06
MIC06 TB PARITY ENA H	04,07,06
MIC06 XB SELECT H	07,06,01,02
MIC06 XB IN USE L	07,06
MIC06 XB1 IN USE L	07,06
MIC07 ACV H	04,07
MIC07 ADD REG ENA L	11,05,07,01,02,18
MIC07 CORR DATA INT L	04,07
MIC07 ENA CHI L	07,01,02
MIC07 ENC UTRAP 0 L	07
MIC07 ENC UTRAP 1 L	07
MIC07 ENC UTRAP 2 L	07
MIC07 FORCE MA 09 H	07,03
MIC07 GEN DEST INH L	04,07
MIC07 GRANT STALL L	18,06,07
MIC07 INHIBIT CHI H	07
MIC07 PROC INIT H	04,07
MIC07 PTE CHK DR PROBE H	07
MIC07 SNAPSHOT CMI L	06,07,01,02
MIC07 STATUS 0 H	07
MIC07 STATUS 1 H	07
MIC07 STATUS VALID L	04,07,06
MIC07 UTRAP L	18,04,07,06
MIC07 WR BUS ERR INT L	04,07
MIC07 WRITE VECT OCC L	06,07
MIC08 CA TAG 12 H	12,11,08
MIC08 CA TAG 13 H	12,11,08
MIC08 CA TAG 14 H	12,11,08
MIC08 CA TAG 15 H	12,11,08
MIC08 CA TAG 16 H	12,11,08
MIC08 CA TAG 17 H	11,12,08

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		(160,1271) MIC20.DRW 06-NOV-88 18:27 NEXT HIGHER ASSEMBLY: B-DD-L0003-0-0 FIRST USED ON OPTION/MODEL: 11/750	SIZE CODE NUMBER REV. D CS L0003-0-20 C

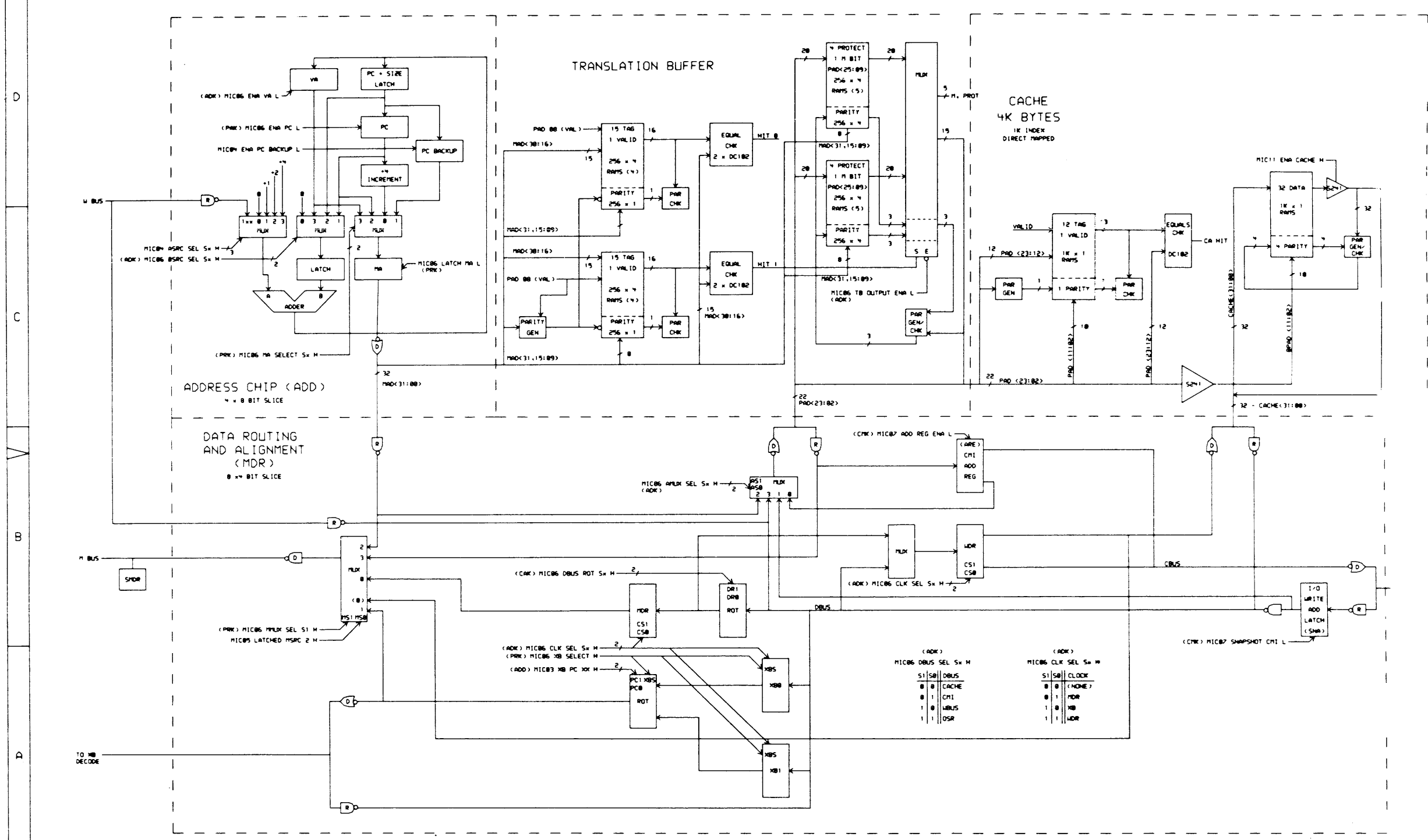
SIGNAL NAME	PAGE NUMBER(S)	SIGNAL NAME	PAGE NUMBER(S)	SIGNAL NAME	PAGE NUMBER(S)
MIC15 T1 TB DATA 22 H	16,15	PAD 17 H	16,15,12,17,11,00,01	XBLF 08 H	01
MIC15 T1 TB DATA 23 H	16,15	PAD 18 H	16,15,05,12,17,11,00,01	XBLF 09 H	01
MIC15 T1 TB M BIT H	15,16	PAD 19 H	16,15,05,12,17,11,00,01	XBLF 10 H	01
MIC16 AC 0 H	05,16,17,07	PAD 20 H	15,05,16,12,17,11,00,02	XBLF 11 H	01
MIC16 AC 1 H	05,16,17,07	PAD 21 H	16,15,05,12,17,11,00,02	XBLF 12 H	02
MIC16 AC 2 H	05,16,17,07	PAD 22 H	16,15,05,12,17,11,00,02	XBLF 13 H	02
MIC16 AC 3 H	05,16,17,07	PAD 23 H	16,15,05,12,17,11,00,02	XBLF 14 H	02
MIC16 M BIT H	05,16,17,07	RDM V CLOCK H	04	XBLF 15 H	02
MIC16 T0 WRITE ENA L	15,16,14	RDM V LOAD H	04		
MIC16 T1 WRITE ENA L	15,16,14	SCNO UB H	05		
MIC16 TB HIT 1 L	16	TB HIT 0 H	05,07,06,16		
MIC16 TB PAR OUT 0 L	16,17	TB HIT 1 H	16,05,07,06		
MIC16 TB PAR OUT 1 L	16,17	UB103 FORCE CACHE PE L	13,12		
MIC16 TB PAR OUT 2 L	16,17	UB103 FORCE TB PE L	17		
MIC16 TB VALID H	16,07	UB103 RTUT DINH L	04,07,06		
MIC17 T TAG PAR IN H	14,17	UB112 CMI UB INH L	04		
MIC17 TB DATA PERR 0 H	15,04,17	UB113 MSEO INIT L	07,06		
MIC17 TB DATA PERR 0 L	17	UB114 UB INT GRANT H	07		
MIC17 TB DATA PERR 1 H	15,04,17	WBUS 00 H	01,03		
MIC17 TB DATA PERR 1 L	17	WBUS 01 H	03,01		
MIC17 TB DATA PERR 2 H	15,04,17	WBUS 02 H	03,01		
MIC17 TB DATA PERR 2 L	17	WBUS 03 H	03,01		
MIC17 TB DATA PERR H	17,07	WBUS 04 H	03,02		
MIC17 TB TAG 0 PERR H	07,17	WBUS 05 H	02,03		
MIC17 TB TAG 1 PERR H	07,17	WBUS 06 H	03,02		
MIC18 B CLK L	18,06,04,05,11,07,01,02,03	WBUS 07 H	03,02		
MIC18 INH LATCH MA H	06,10	WBUS 08 H	01,03		
MIC18 INTERRUPT INH H	18	WBUS 09 H	03,01		
MIC18 LATCHED UTRAP H	18,04	WBUS 10 H	03,01		
MIC18 MIC LD OSR L	18,04,06	WBUS 11 H	01,03		
MIC18 X STALL H	18,04	WBUS 12 H	03,02		
MIC18 X STALL L	18,04	WBUS 13 H	02,03		
MICRO VECTOR 0 H	04,07	WBUS 14 H	03,02		
MICRO VECTOR 1 H	04,07	WBUS 15 H	02,03		
MICRO VECTOR 2 H	04,07	WBUS 16 H	01,03		
MICRO VECTOR 3 H	04,07	WBUS 17 H	01,03		
PAD 02 H	11,00,01	WBUS 18 H	03,01		
PAD 03 H	15,11,05,16,00,01	WBUS 19 H	01,03		
PAD 04 H	15,11,05,16,00,02	WBUS 20 H	02,03		
PAD 05 H	15,11,05,16,00,02	WBUS 21 H	02,03		
PAD 06 H	15,11,05,16,00,02	WBUS 22 H	03,02		
PAD 07 H	15,11,05,16,00,02	WBUS 23 H	02,03		
PAD 08 H	11,05,16,14,00,01,17	WBUS 24 H	06,07,01,03		
PAD 09 H	16,15,11,17,00,01	WBUS 25 H	06,07,01,03		
PAD 10 H	11,16,15,17,00,01	WBUS 26 H	06,07,01,03		
PAD 11 H	16,15,11,17,00,01	WBUS 27 H	06,07,01,03		
PAD 12 H	12,15,16,17,11,00,02	WBUS 28 H	02,03		
PAD 13 H	12,16,15,17,11,00,02	WBUS 29 H	02,03		
PAD 14 H	12,16,15,17,11,00,02	WBUS 30 H	02,03		
PAD 15 H	12,16,15,17,11,00,02	WBUS 31 H	02,03		
PAD 16 H	15,12,16,17,11,00,01				

NOTES:
1. THIS PAGE LISTS THE SCHEMATIC PAGE NUMBER(S) WHERE A SIGNAL NAME IS REFERENCED.

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REV	CHANGE NO.	DESCRIPTION

digital	DRW. <i>W. M.</i>	DATE	ENG.	DATE	TITLE: MIC FORWARD REFERENCE
	CHK'D.	06-NOV-88			
(160,1271) MIC22.DRW		06-NOV-88 18:32	NEXT HIGHER ASSEMBLY:	SIZE CODE	NUMBER
FIRST USED ON OPTION/MODEL: 117750		B-DD-L0003-0-0	D	CS	L0003-0-22



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REVISIONS	
CHK	CHANGE NO. REV

digital	DRN.	DATE	ENG.	DATE	TITLE:
	CHK'D.	DATE	BOARD LOCATION:	SHEET	OF
1160.1271 MICBD.DRW		06-NOV-80	13:01	NEXT HIGHER ASSEMBLY:	
FIRST USED ON OPTION/MODEL:		B-DD-L0003-0-0		SIZE	CODE
117750		B-DD-L0003-0-0		D	BD
MIC BLOCK DIAGRAM				NUMBER	REV.
L0003-0-23				D	C

DRAWING NO.	NO. OF SHTS.	PART NO.	DESCRIPTION	REVISIONS																			
				C	D	E	F	H	J														
			MODULE REVISION	C	D	E	F	H	J														
B-DD-L0004-0	2		UBI DRAWING DIRECTORY	C	D	E	F	H	J														
E-UA-L0004-0-C	2		UBI UNIT ASSEMBLY	C	D	E	F	H	J														
K-PL-L0004-0-DBP	4		UBI PARTS LIST	C	C	D	E	E	F														
E-MD-5013827-C-0	6		UBI DRILL & ETCH DRAWINGS	C	D	D	D	D	E														
		5013827	ETCHED BOARDS	D	D	D	D	D	EP2														
K-PC-L0004-0-DBC			UBI PC DESIGN DATA BASE CALDEC	D	D	D	D	D	EP2														
E-EC-5013827-C-0	3		UBI ETCH CUT DRAWINGS	C	D	E	F	H	J														
K-CS-L0004-0-DBS			UBI DESIGN DATA BASE SUDS	C	D	E	F	H	J														
D-CS-L0004-0-1	1	*	TOY OFFSET MEMORY	C	C	C	D	D	D														
D-CS-L0004-0-2	1	*	TIME OF YEAR CLOCK	C	C	C	C	C	D														
D-CS-L0004-0-3	1	*	LATCH-PAR-GEN HELP- SIGNALS	C	C	C	D	D	E														
E-CS-L0004-0-4	1	*	UBUS RESISTOR PACKS	C	C	C	C	C	C														
D-CS-L0004-0-5	1	*	CUI - UBUS ADDRESS 1 OF 2	C	C	C	C	C	C														
D-CS-L0004-0-6	1	*	CUI - UBUS ADDRESS 2 OF 2	C	C	C	C	C	C														
E-CS-L0004-0-7	1	*	DATA PATH	C	C	C	C	C	C														
D-CS-L0004-0-8	1	*	CUI MAP	C	C	C	C	C	C														
D-CS-L0004-0-9	1	*	CUI MAP DECODE	C	C	C	C	C	C														
D-CS-L0004-0-10	1	*	CONTROL LOGIC	C	C	C	C	D	D														
D-CS-L0004-0-11	1	*	CUI CONTROL ROM	C	C	C	C	C	C														
D-CS-L0004-0-12	1	*	CONSOLE INTERFACE	C	D	D	D	D	D														
D-CS-L0004-0-13	1	*	UBUS CONTROL	C	C	C	D	D	D														
D-CS-L0004-0-14	1	*	AC - DC LC MSEQ INIT	C	C	C	C	C	C														
D-CS-L0004-0-15	1	*	INT & ID LOGIC ROM INTERFACE	C	C	D	E	F	H														
D-CS-L0004-0-16	1	*	FORWARD REFERENCE	C	C	C	D	D	D														

NOTES:
 * CONTROL SOURCE IS THE SUDS DATA BASE
 NO CONTROLLED PAPER ORIGINALS EXIST

 ALL DOCUMENTATION WAS RELEASED AT REVISION 'C'

REVISIONS		REV.	D	E	F	H	J
DATE	CHG NO.						
6-80	TW001						
9-80	TW002						
6-81	TW003						
7-81	TW004						
11-81	TW005						

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USED ON OPTION/MODEL 11/750	DRN. J. CASEY	TITLE UBI
	CHK'D J. CASEY	SIZE B CODE DD NUMBER L0004-0 REV. J
	ENG. S. SMITH	SHÉET 1 OF 2
	PROD. V. PARKER	

DRAWING NO.	NO. OF SHTS.	PART NO.	DESCRIPTION	REVISIONS																									
				1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26
D-CS-L0004-0-17	1	*	FORWARD REFERENCE	C	c	C	D	D	D																				
D-CS-L0004-0-18	1	*	FORWARD REFERENCE	C	D	D	E	F	F																				
D-CS-L0004-0-19	1	*	FORWARD REFERENCE	C	c	C	C	C	C																				
D-BD-L0004-0-20	1		UBI BLOCK DIAGRAM	C	c	C	C	C	C																				
K-MP-L0004-0-21	26		UBI MICROCODE LISTING	C	c	C	C	C	C																				
K-MC-L0004-0-0			UBI MICROCODE TAPE	C	c	C	C	C	C																				

NOTES: * CONTROL SOURCE IS THE SUDS DATA BASE
 NO CONTROLLED PAPER ORIGINALS EXIST
 ALL DOCUMENTATION WAS RELEASED AT REVISION 'C'

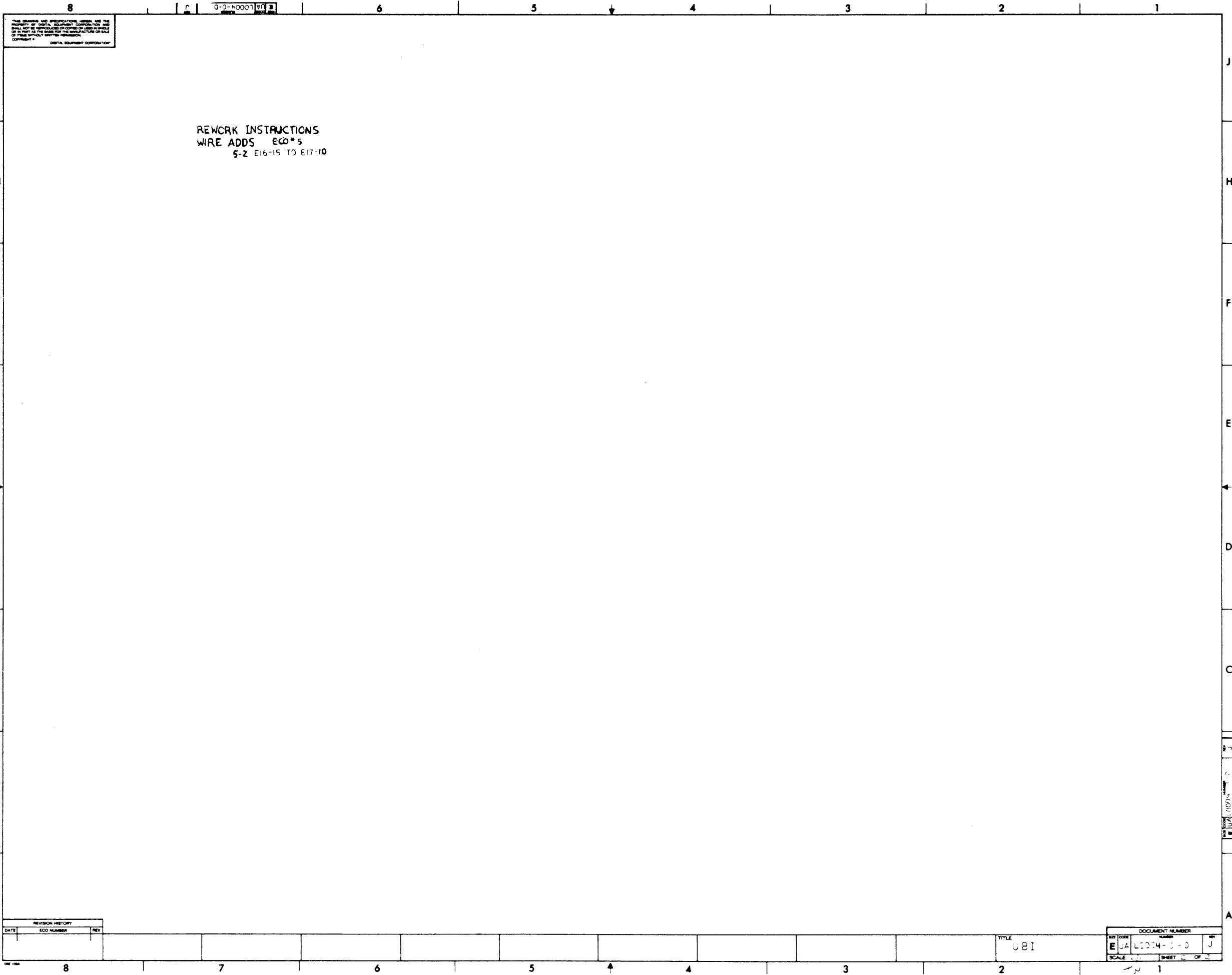
REVISIONS	DATE	CHG NO.	REV.

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USED ON OPTION/MODEL 11/750	DRN. J. CASEY	TITLE UBI
	CHK'D J. CASEY	SIZE CODE NUMBER B DD L0004-0
	ENG. S. SMITH	REV. J
	PROD. V. PARKER	SHEET 2 OF 2

ru



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REWORK INSTRUCTIONS
 WIRE ADDS ECO'S
 5-2 E16-15 TO E17-10

REVISION HISTORY		
DATE	ECO NUMBER	REV

TITLE	UBI
REV CODE	EJA
DOCUMENT NUMBER	U0004-0-0
SCALE	1
SHEET	1 OF 1

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY	PER VARIATION	REFERENCE DESIGNATOR
					00		
1		E-MD-5013827-0-0	5013827-00	CIRCUIT BOARD (PCS)	1		
2			1000019-00	150.0 MMF 100V 5%200PPM MICA	1		C8
3			1000023-00	330.0 MMF 100V 5%200PPM MICA	4		C1-C4
4			1000024-00	470.0 MMF 100V 5%200PPM MICA	1		C5
5			1000043-00	1000.0 MMF 250V 20% Y5F DISC	2		C63,C64
6		SEE NOTES	1012084-01	8 MFD 25V +75-10% AL EL	5		C10-C15
7			1009964-00	.68 MFD 100V 10% S.TANT	1		C6
8			1010978-40	.22 MFD 50V 10% CER	1		C65
9			1012734-00	.047 MFD 50V +80-20% CER	48		C16-C62,C66
10			1010978-24	.01 MFD 50V 10% CER	2		C9,C67
11			1104860-00	1N 746A VZ= 3.3 5%	1		D1
12			1105796-00	1N 4004 PIV=400 I= 1A DO41 SP	1		D2-D5
13			1105871-01	1/4M3.OAZ1 - 3.0 1% .25W N	1		D6
14			1210711-02	/REPLACED BY 12-16988-02	1		
15			1211164-04	*** THIS ITEM IS NOT USED ***	-		
16			1215924-00	SKT, IC 48PIN DIP GOLD PLATE	8		XE48, XE58, XE68, XE78, XE88, XE98, XE108, XE118
17			1215935-00	GASKET, THERMAL .50"X.80"	8		
18			1215936-00	HEAT SINK, FORCED CONVECTION	8		
19			1300005-04	R NETWORK 15-470 5.0 % 16PIN	1		E37
20			1300005-07	R NETWORK 15-4.7K 5.0 % 16PIN	1		E40
21			1300316-00	470.0 .25 W 5.0 % CC	1		R20
22			1300365-00	1.0 K .25 W 5.0 % CC	14		R1, R8, R12-R19, R22, R23, R30, R31
23			1300398-00	1.80 K .25 W 5.0 % CC	2		R4, R28
24			1300432-00	3.0 K .25 W 5.0 % CC	1		R11
25			1300447-00	4.70 K .25 W 5.0 % CC	3		R2, R3
26			1302394-00	30.0 K .25 W 5.0 % CC	1		R23
27			1305346-00	27.0 K .25 W 5.0 % CC	1		R25
28			1301322-00	180.0 .25 W 5.0 % CC	1		R21
29			1301423-00	*** THIS ITEM IS NOT USED ***	-		

REVISION HISTORY		BASIC PART NO: L0004		DRN: K.FRIEDGEN	DATE: 04-MAY-79	D I G I T A L	
ENG:	ECC NUMBER	REV:	SECTION A OF A	CHK'D: E.T.GERRY	DATE: 04-MAY-79	TITLE PARTS LIST	
	INITIAL		SECTION VARIATION INDEX	DES.ENG: S.SMITH	DATE: 4-MAY-79	U.B.I.	
TK	L0004-TW002	[A]	00	RESP.ENG.: S.SMITH	DATE: 04-MAY-79	DOCUMENT NUMBER	
D L	L0004-TW003	[B]		MFG.ENG.: VANCE PARKER	DATE: 8-FEB-80	SIZE: K	CODE: PL
LL	TW005	[C]		ASSEMBLY NUMBER: E-UA-L0004-0-0	TOP DOCUMENT NUMBER: #B-DD-L0004-0	NUMBER: L0004-0-DBP	REV: F
		[D]				FILE NAME: Z1256F.PLS	EDIT #: 20
		[E]					
		[F]					
		[G]					
		[H]					
		[I]					
		[J]					
		[K]					
		[L]					
		[M]					
		[N]					

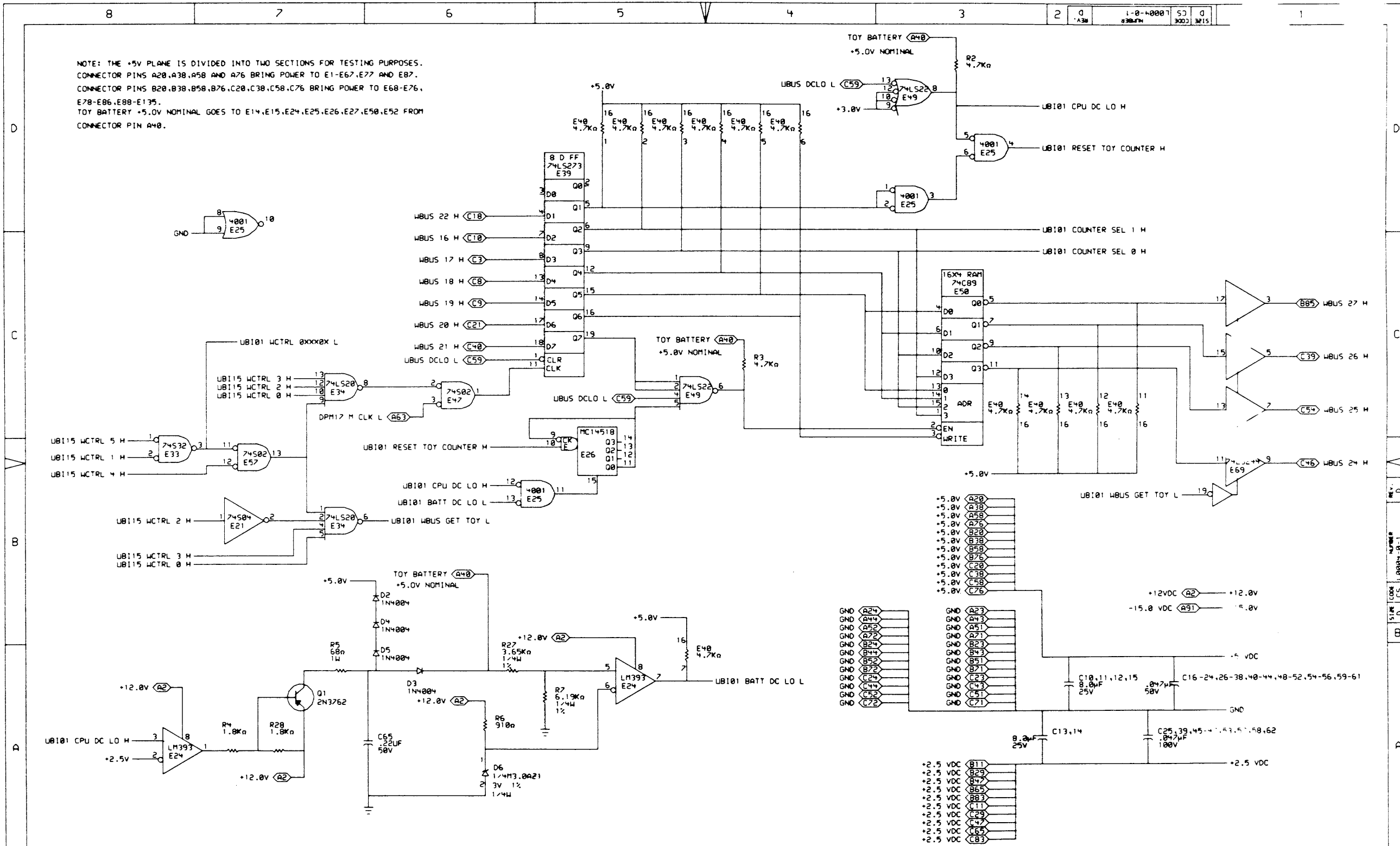
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LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY	PER VARIATION	REFERENCE DESIGNATOR
77	77	1914693-00	DC 619D BIPOLAR,LS,400-GATE	1		E48
78	78	1914704-00	DC 630B BIPOLAR,LS,400-GATE	1		E98
79	79	1915193-00	LS244 DRIVER,LINE,OCTAL,T	9	CONT	E59, E69, E79, E80, E86, E97, E99, E101, E110
80	80	1915697-00	RAM 256X4 TRI-STATE	10		E60-E62, E70-E72, E81, E82, E92, E102
81	81	2112623-02	DUAL BAUD RATE GEN/PROG DIVIDER	1		E74
82	82	2113603-00	4001UBNOR GATE-QUAD 2IN CM	1		E25
83	83	2113647-00	4513B COUNTER,DUAL UP BCD	1		E26
84	84	2113653-00	74C89 RAM 64BIT CMOS TRIST	1		E50
85	85	2114462-00	4040B COUNTER/DIVIDER,BINA	3		E14, E27, E52
86	86	23524A2-00	A2-05	1		E7
87	87	23525A2-00	A2-05	1		E8
88	88	23526A2-00	A2-05	1		E9
89	89	23527A2-00	A2-05	1		E10
90	90	23528A2-00	A2-05	1		E11
91	91	23529A2-00	A2-05	1		E12
92	92	23000A2-05	*** THIS ITEM IS NOT USED ***	-		
93	93	9000024-01	EYELET,ROLL FLANGE .1210DX .192	12		
94	94	1002476-00	510.0 MMF 100V 5%200PPM MICA	1		C7
95	95	1304835-00	/REPLACED BY 13-05337-00	1		R27
96	96	1305374-00	910.0 .25 W 5.0 % CC	1		R6
97	97	1314187-00	5.19 K .25 W 1.0 % RN55D-F10	1		R7
98	98	1503121-00	2N 2369 NPN 350MW SI N	1		Q2
99	99	9009185-00	JUMPER, WIRE, INSULATED, BLACK B	1		W1
100	100	1302379-00	75.0 .25 W 5.0 % CC	2		R32, R34
101	101	1910542-00	74S64 A-O-I GATE 4-2-3-2	1		E38
102	102	1913670-00	74S373 LATCH 8BIT TRASP TR	1		E91
103	103	1300202-00	47.0 .25 W 5.0 % CC	1		R35
104	104	1118585-00	5953 VZ= 5.2 .40W DO	1		D7
105	105	9007200-00	TRANSIPADS #10134	1		XQ2

106 NOTE: SPARE I.C. LOCATIONS ARE:E130,E135
 107 NOTE: SOME MODULES WILL HAVE 10-05306 INSTEAD OF 10-12034-01

D	I	G	I	T	A	L	TITLE	U.B.I.	SECTION A	OF A	SIZE	CODE	DOCUMENT NUMBER	REV
											K	PL	L0004-0-DBP	F

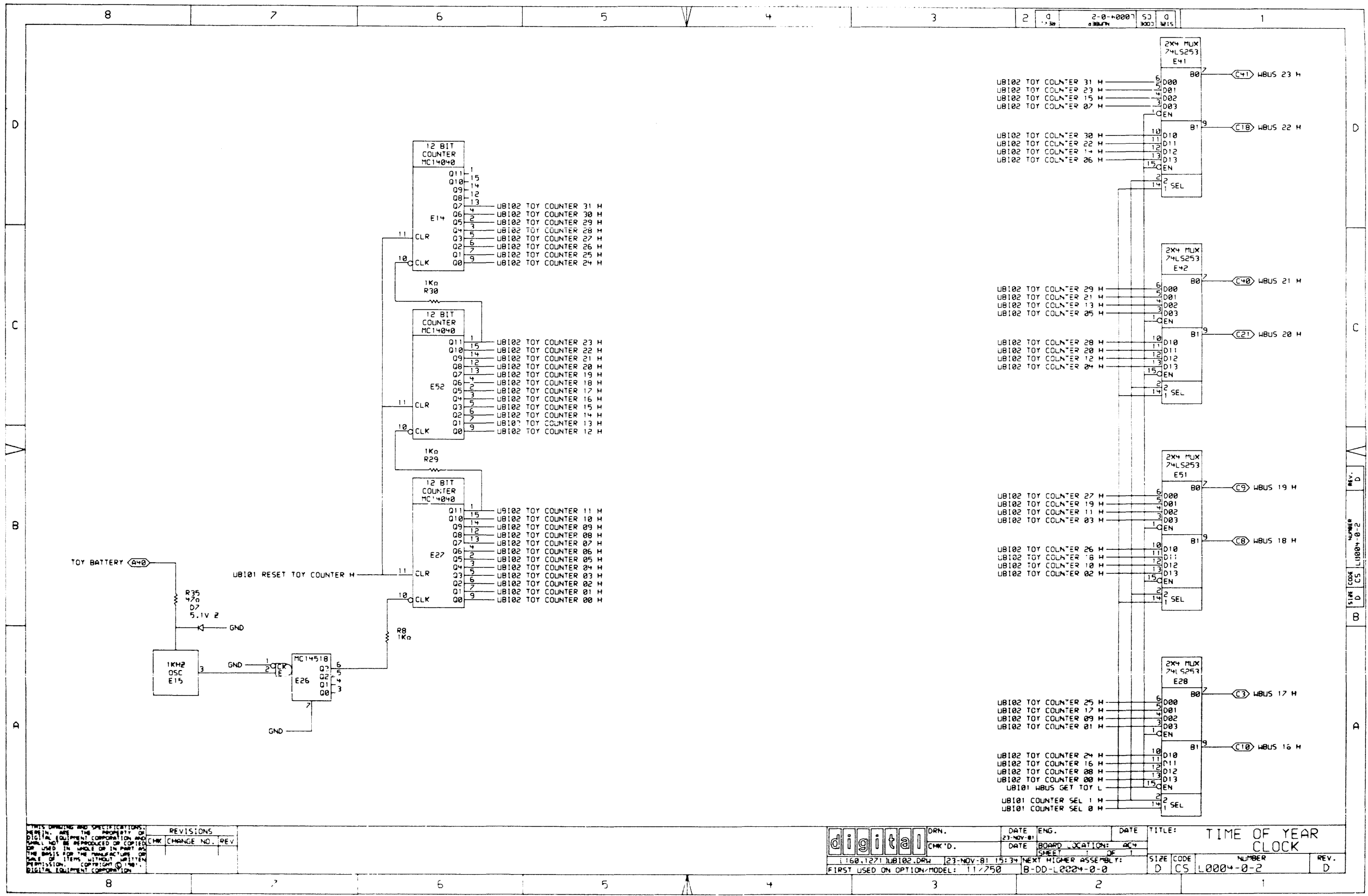
NOTE: THE +5V PLANE IS DIVIDED INTO TWO SECTIONS FOR TESTING PURPOSES.
 CONNECTOR PINS A20, A38, A58 AND A76 BRING POWER TO E1-E67, E77 AND E87.
 CONNECTOR PINS B20, B38, B58, B76, C20, C38, C58, C76 BRING POWER TO E68-E76,
 E78-E86, E88-E135.
 TOY BATTERY +5.0V NOMINAL GOES TO E14, E15, E24, E25, E26, E27, E50, E52 FROM
 CONNECTOR PIN A40.



REVISIONS	CHK	CHANGE NO.	REV

DATE	ENG.	DATE	TITLE:
26-MAR-81	DRN. R.C./M.C.		TOY OFFSET MEMORY

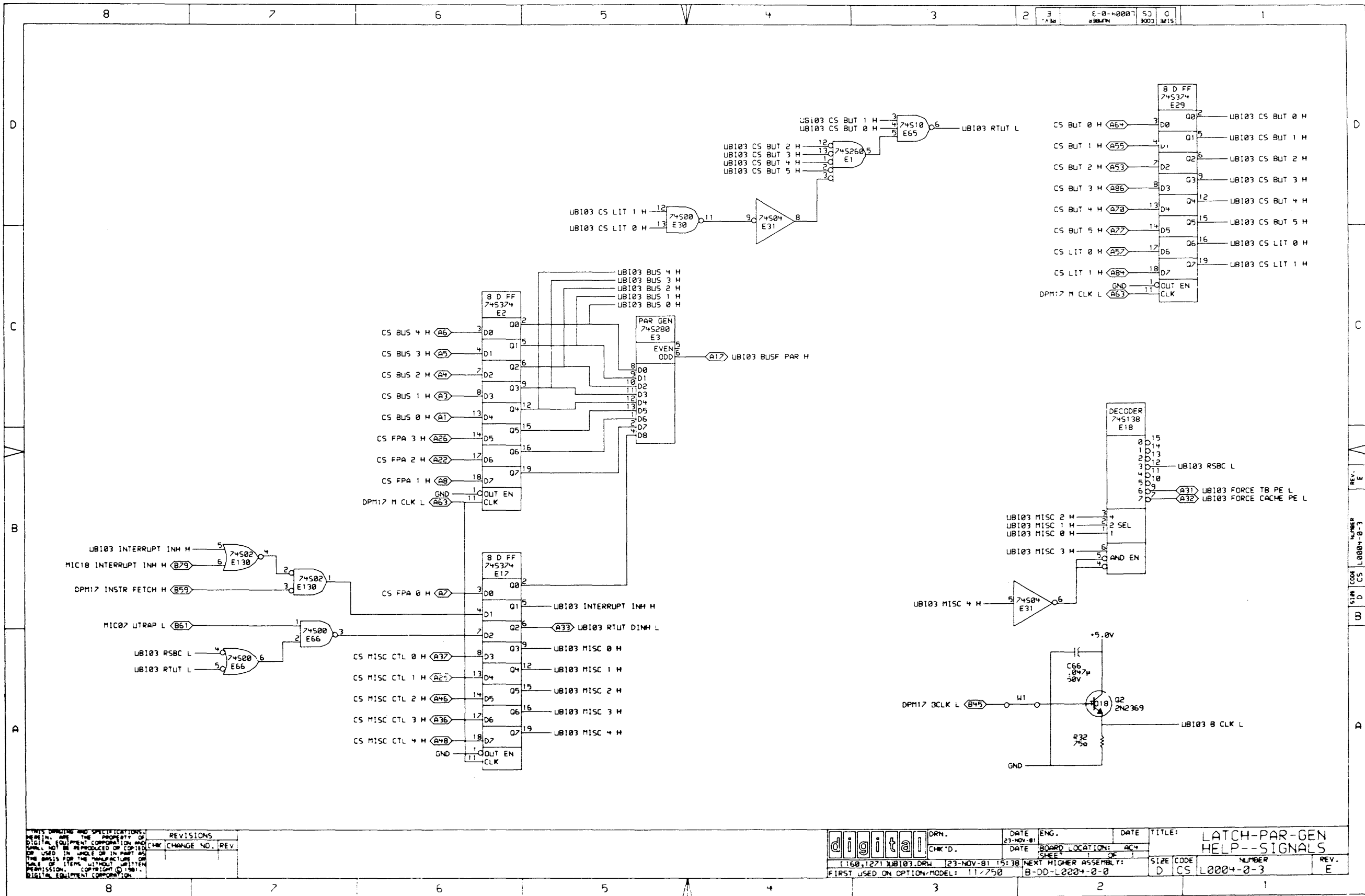
DATE	ENG.	DATE	TITLE:
11-7-80	DRN. R.C./M.C.		TOY OFFSET MEMORY



REVISIONS	
CHK	CHANGE NO. REV

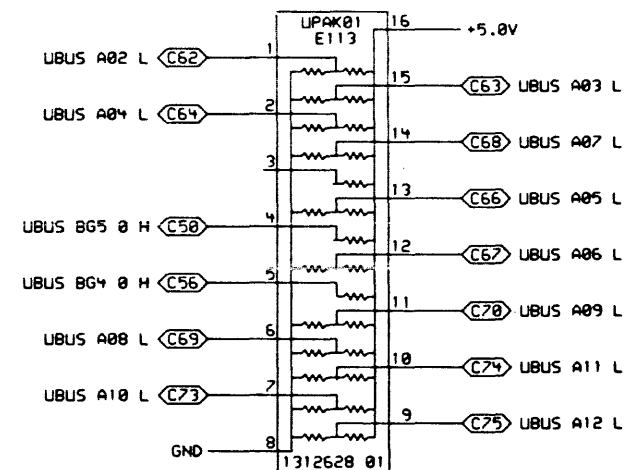
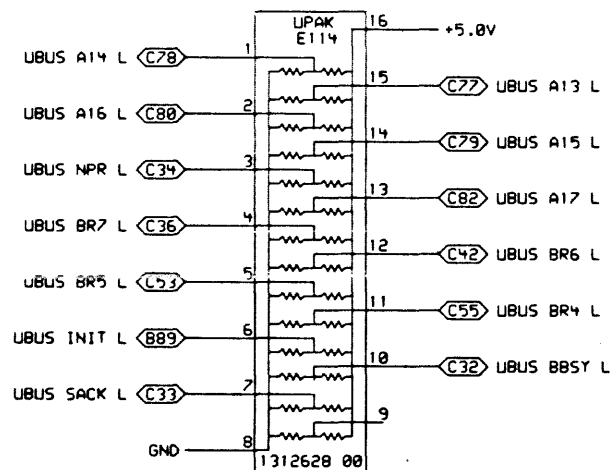
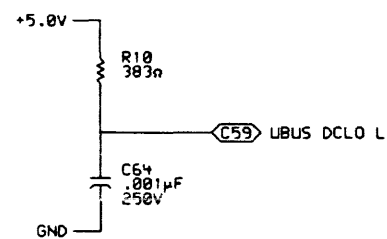
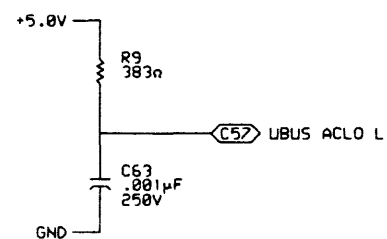
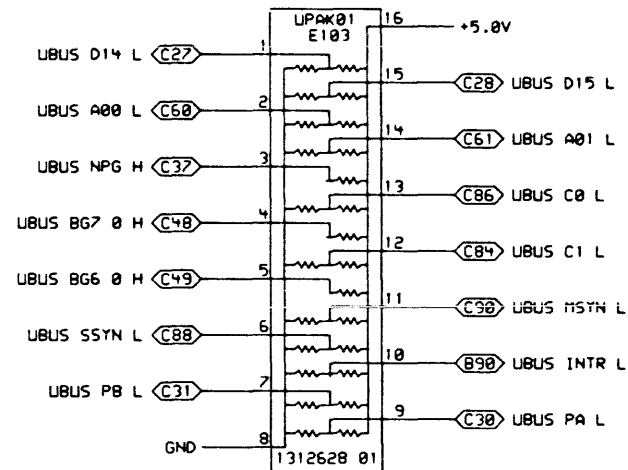
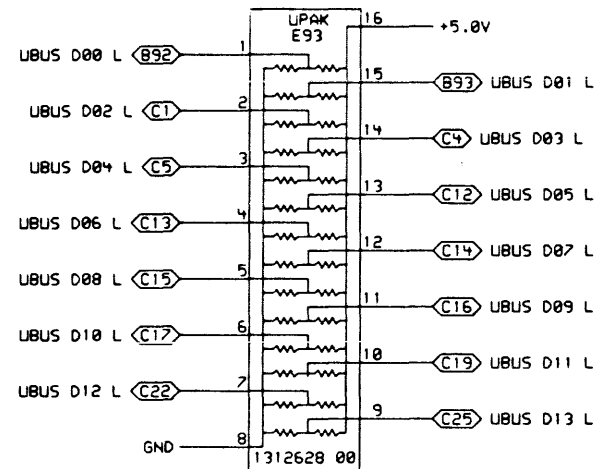
digital	DRN.	DATE	ENG.	DATE	TITLE:		
	CHK'D.	23-NOV-81	AC4		TIME OF YEAR CLOCK		
FIRST USED ON OPTION/MODEL: 11/750					SIZE CODE	NUMBER	REV.
					D CS	L0004-0-2	D

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 COPYRIGHTS: UB102.PLOX(160,1271) 23-NOV-81 16:14



REV.	CHANGE NO.	REV.

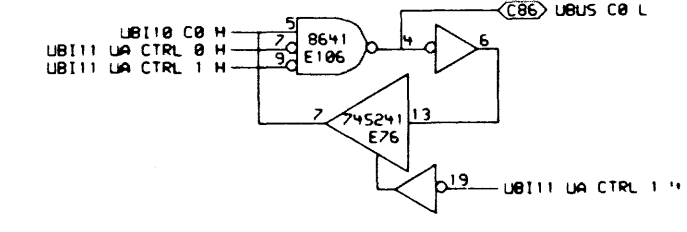
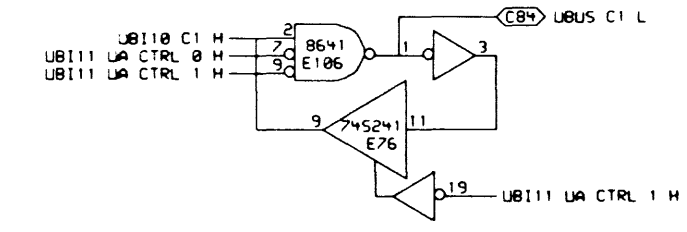
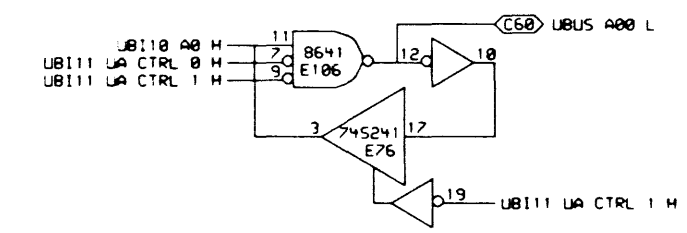
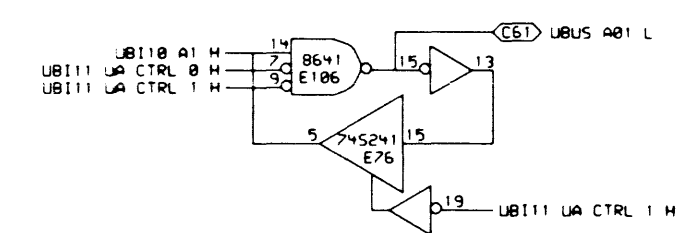
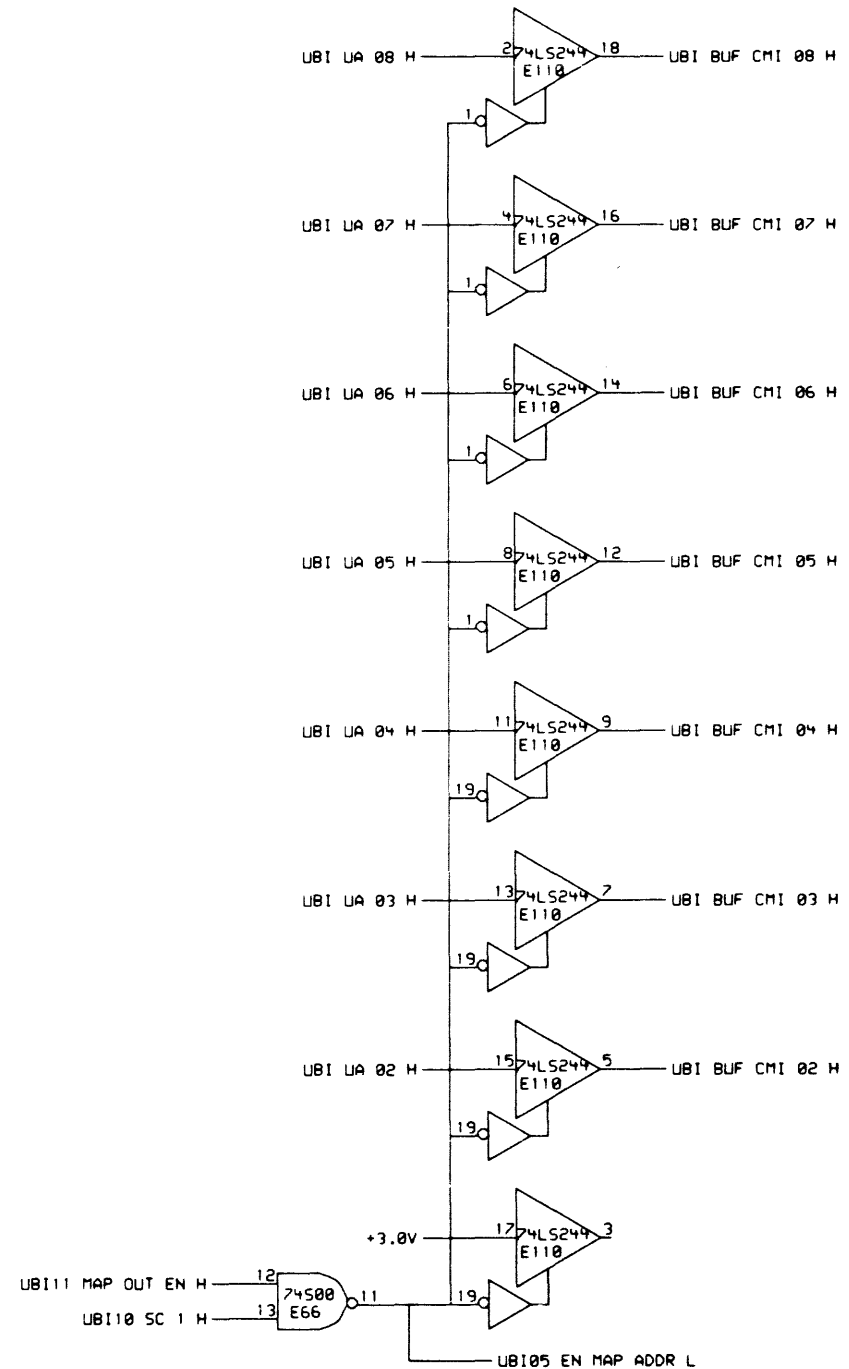
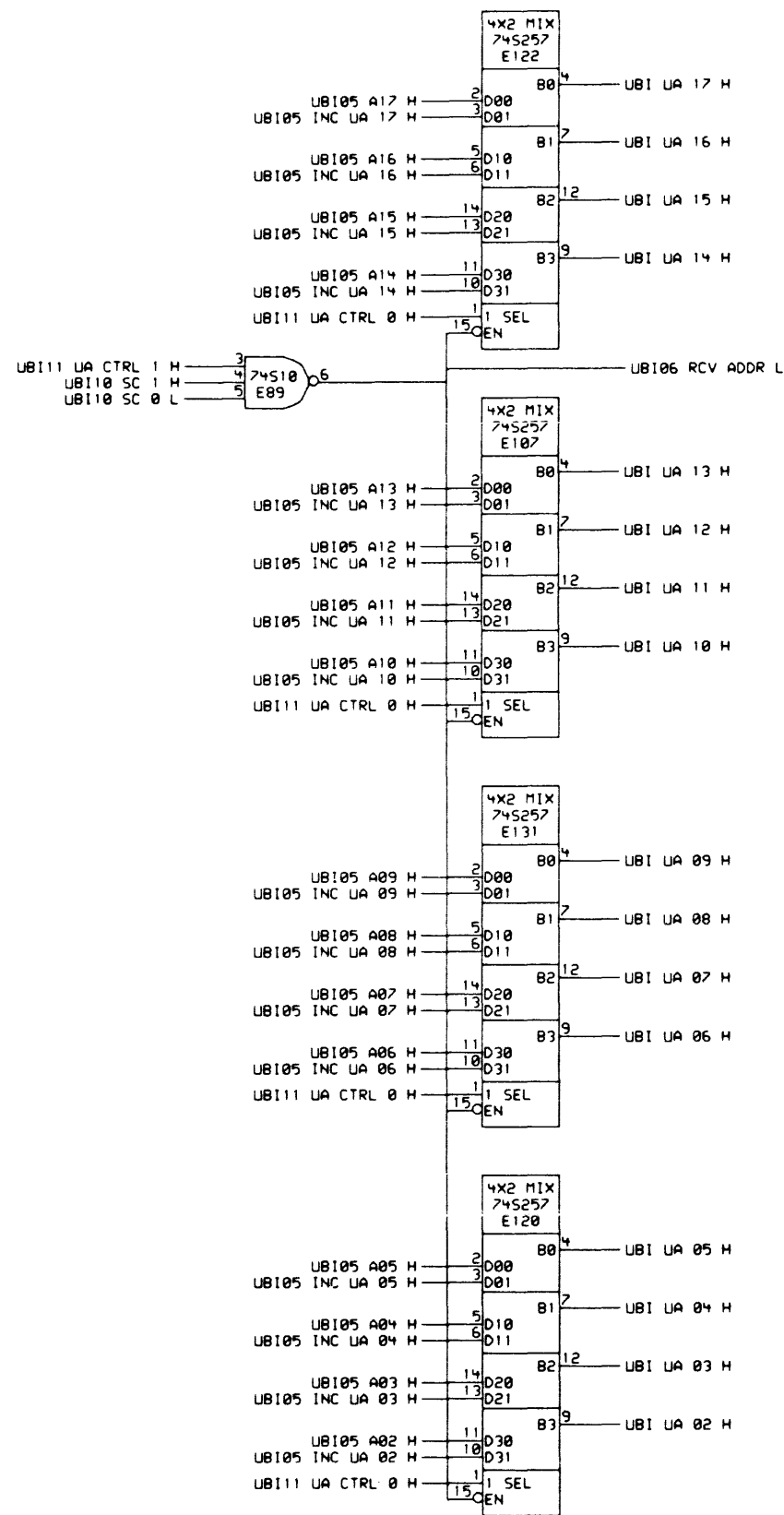
digital	DRN.	DATE	ENG.	DATE	TITLE:
	CHK'D.	23-NOV-81			LATCH-PAR-GEN HELP--SIGNALS
(160,1271) UBI03.DRW 123-NOV-81 15:38 NEXT HIGHER ASSEMBLY: B-DD-L-0004-0-0 FIRST USED ON OPTION/MODEL: 11/750					SIZE CODE NUMBER REV. D CS L0004-0-3 E



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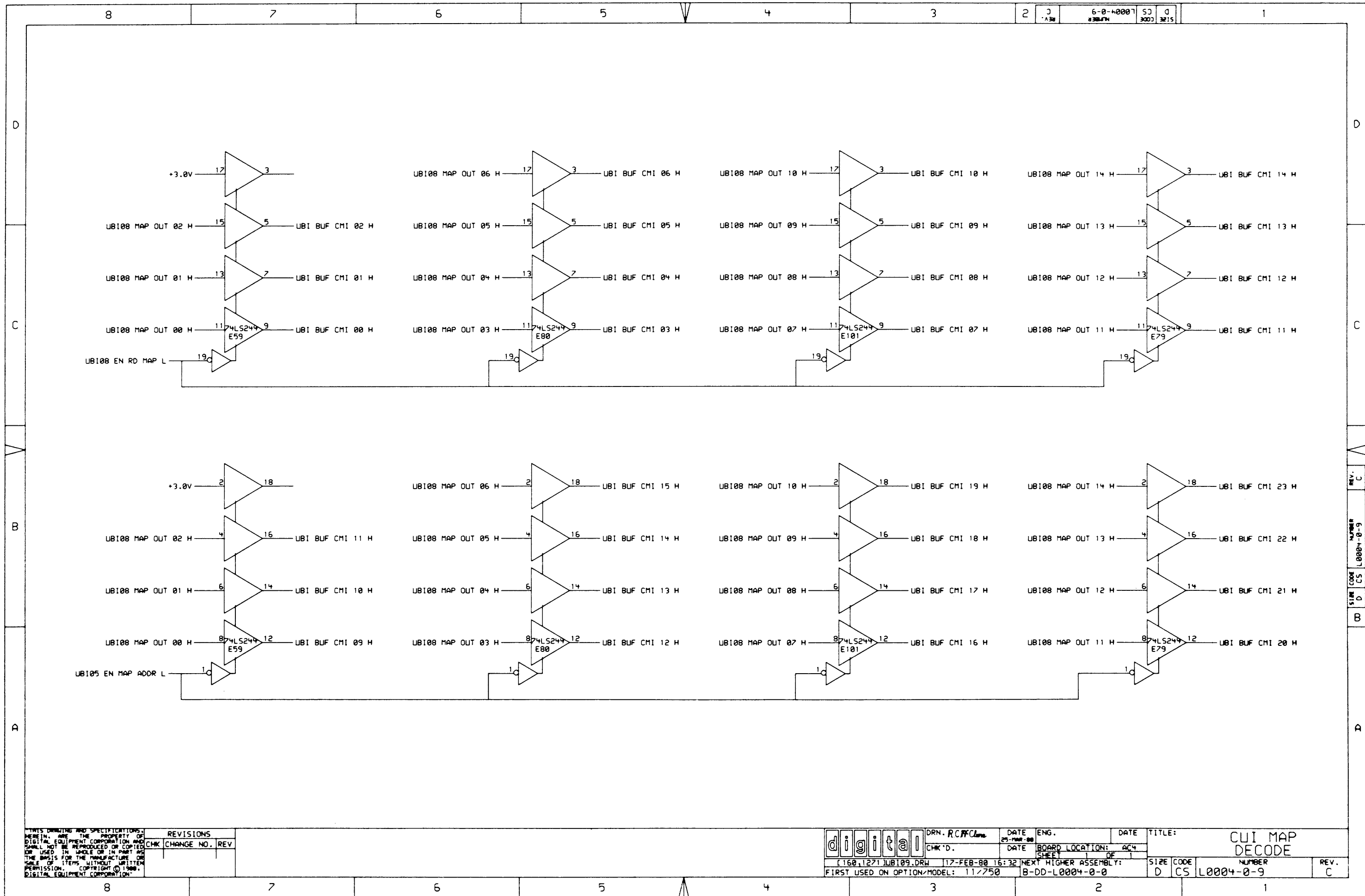
digital	DRN. R.C.F. <i>Clara</i>	DATE 21-MAR-88	ENG.	DATE	TITLE: UNIBUS RESISTOR PAKS
	CHK'D.	DATE	BOARD LOCATION: AC4	SHEET OF	SIZE CODE D CS
FIRST USED ON OPTION MODEL: 11/750					NUMBER L0004-0-4
NEXT HIGHER ASSEMBLY: B-DD-L0003-0-0					REV. C



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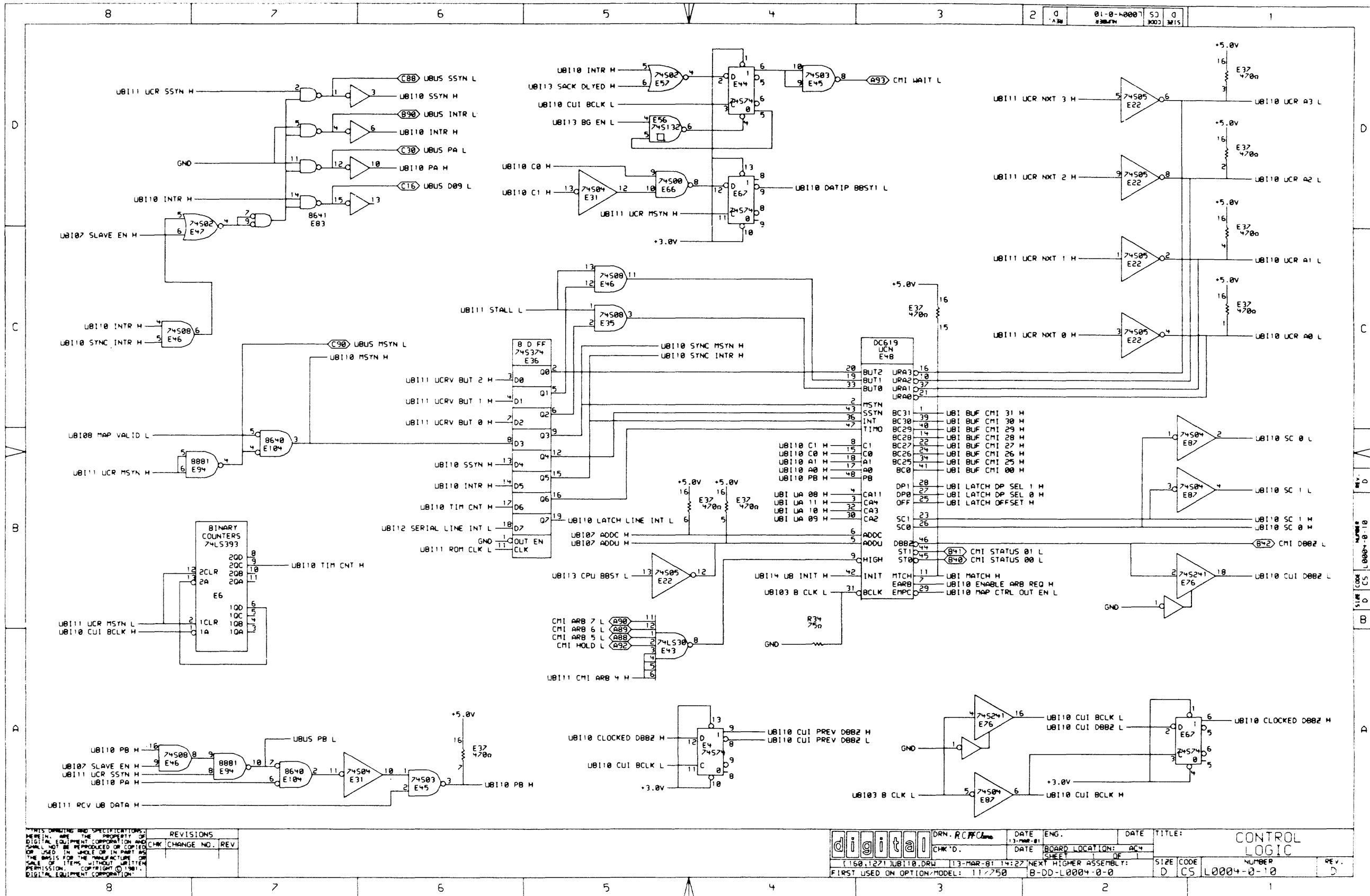
digital	DRN. RCAF/Clare	DATE	ENG.	DATE	TITLE: CUI UBUS ADDRESS
	CHK'D.	DATE	BOARD LOCATION: AC4	SHEET	SHT. 2 OF 2
[160,1271]UBI06.DRW 17-FEB-80 16:27			NEXT HIGHER ASSEMBLY: B-DD-L0003-0-0		SIZE CODE NUMBER REV.
FIRST USED ON OPTION/MODEL: 11/750					D CS L0004-0-6 C



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CHK	CHANGE NO.	REV

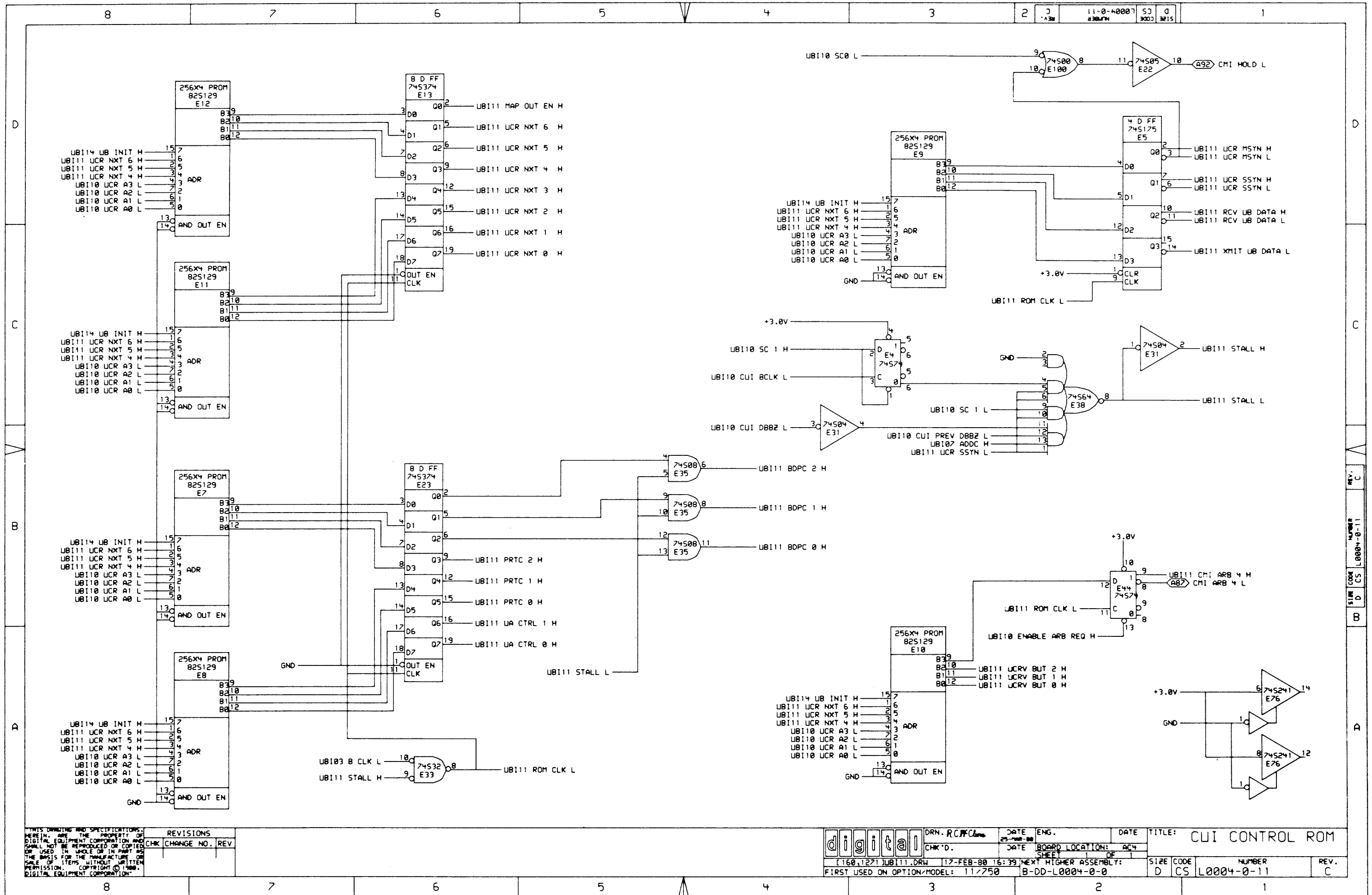
digital	DRN. R.C.F. <i>Clara</i>	DATE 25-MAR-88	ENG.	DATE	TITLE: CUI MAP DECODE
	CHK'D.	DATE 17-FEB-88 16:32	BOARD LOCATION: AC4	SHEET 1 OF 1	NUMBER
FIRST USED ON OPTION/MODEL: 11/750			NEXT HIGHER ASSEMBLY: B-DD-L0004-0-0		REV. C



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REV.	CHANGE NO.	DESCRIPTION

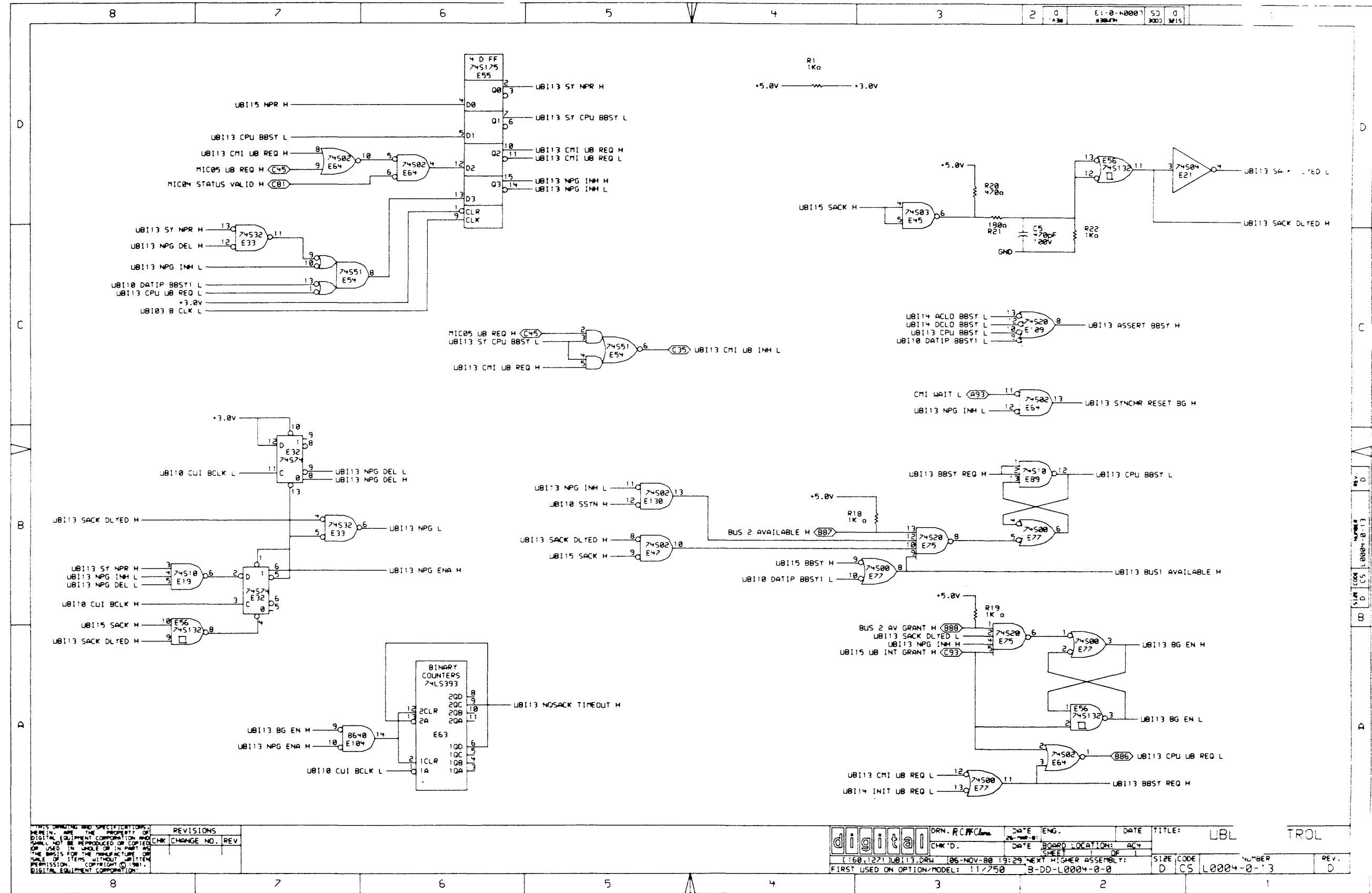
digit	DRN. R.C.F.C.	DATE	ENG.	DATE	TITLE:
150.1271	UB110.DRW	13-MAR-81	14:27	13-MAR-81	CONTROL LOGIC
FIRST USED ON OPTION MODEL:	11/750	NEXT HIGHER ASSEMBLY:	B-DD-L0004-0-0	SIZE CODE	D CS
				NUMBER	L0004-0-10
				REV.	0



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CHK	CHANGE NO. REV

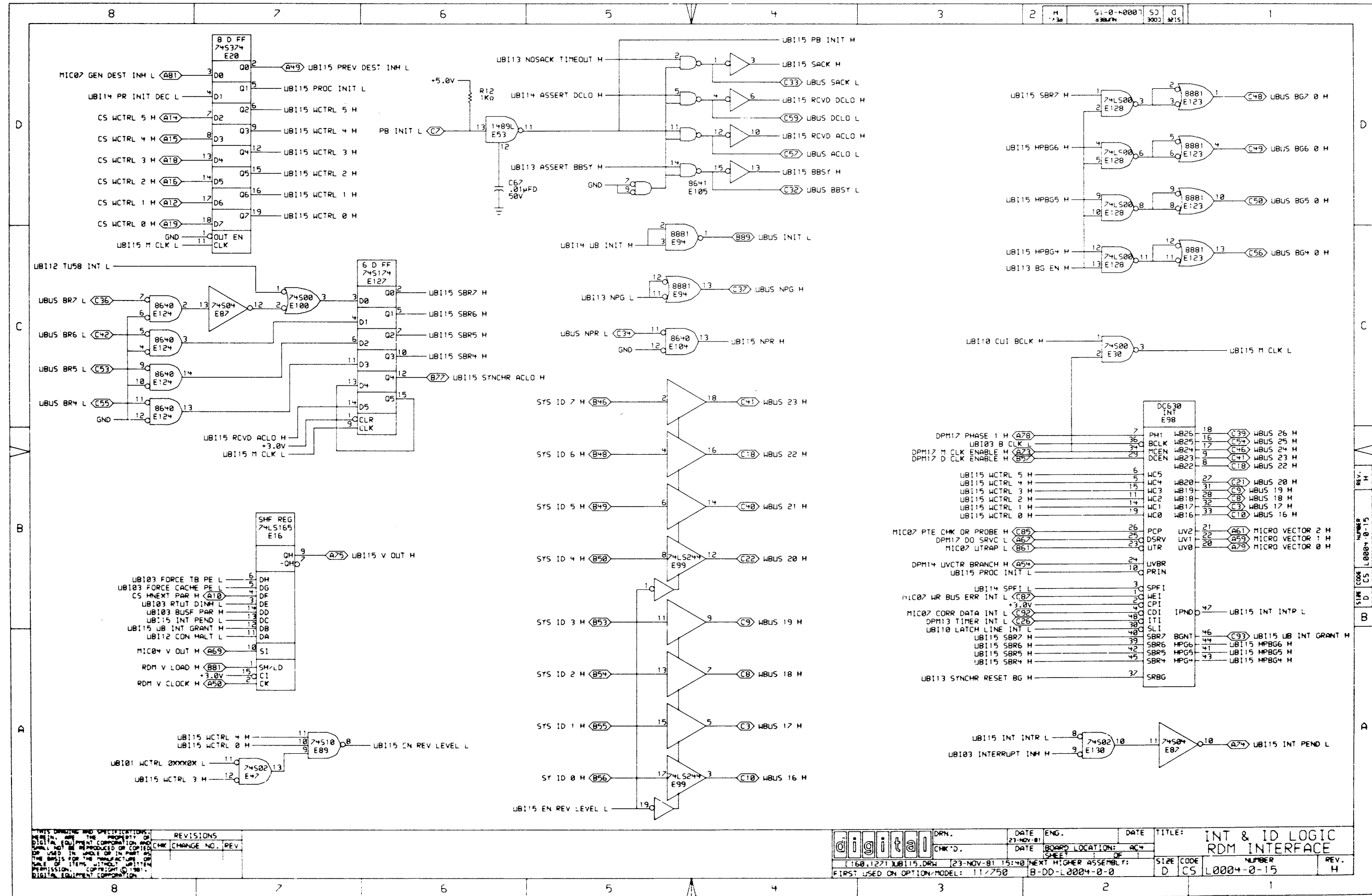
		DRN. RC/FCL	DATE	ENG.	DATE	TITLE: CUI CONTROL ROM
(160,127)UB111.DRW 12-FEB-80 16:39 NEXT HIGHER ASSEMBLY:		CHK'D.	DATE	BOARD LOCATION: A4	SHEET	OF
FIRST USED ON OPTION/MODEL: 11/750		B-DD-L0004-0-0		SIZE	CODE	NUMBER
				D	CS	L0004-0-11
				REV.	C	



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digital	DRN. R.C.F.C.	DATE	ENG.	DATE	TITLE: UBL TROL
	CHK'D.	DATE	BOARD LOCATION: AC4		
FIRST USED ON OPTION MODEL: 11/750		NEXT HIGHER ASSEMBLY: B-DD-L0004-0-0			



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REVISIONS	
CHK	CHANGE NO. REV.

digital	DRN.	DATE	ENG.	DATE	TITLE:
	CHK'D.	DATE	BOARD LOCATION: AC4		INT & ID LOGIC RDM INTERFACE
(160,1271)UB115.DRW 23-NOV-81 15:40 NEXT HIGHER ASSEMBLY: B-DD-L0004-0-0					SIZE CODE
FIRST USED ON OPTION/MODEL: 11/750					D CS
					NUMBER
					L0004-0-15
					REV.
					H

SIGNAL NAME	PAGE NUMBER(S)
BUS 2 AV GRANT H	13
BUS 2 AVAILABLE H	13
CMI ARB 4 L	11
CMI ARB 5 L	10
CMI ARB 6 L	10
CMI ARB 7 L	10
CMI DATA 00 H	07
CMI DATA 01 H	07
CMI DATA 02 H	07
CMI DATA 03 H	07
CMI DATA 04 H	07
CMI DATA 05 H	07
CMI DATA 06 H	07
CMI DATA 07 H	07
CMI DATA 08 H	07
CMI DATA 09 H	07
CMI DATA 10 H	07
CMI DATA 11 H	07
CMI DATA 12 H	07
CMI DATA 13 H	07
CMI DATA 14 H	07
CMI DATA 15 H	07
CMI DATA 16 H	07
CMI DATA 17 H	07
CMI DATA 18 H	07
CMI DATA 19 H	07
CMI DATA 20 H	07
CMI DATA 21 H	07
CMI DATA 22 H	07
CMI DATA 23 H	07
CMI DATA 24 H	07
CMI DATA 25 H	07
CMI DATA 26 H	07
CMI DATA 27 H	07
CMI DATA 28 H	07
CMI DATA 29 H	07
CMI DATA 30 H	07
CMI DATA 31 H	07
CMI DBBZ L	10
CMI HOLD L	11,10
CMI STATUS 00 L	10
CMI STATUS 01 L	10
CMI WAIT L	10,13
CON BAUD OSC H	12
CON BAUD OSC IN H	12
CON BR A L	12
CON BR B L	12
CON BR C L	12
CON BR D L	12
CS ADDR 02 H	3

SIGNAL NAME	PAGE NUMBER(S)
CS ADDR 12 H	3
CS BUS 0 H	03,14
CS BUS 1 H	03,14
CS BUS 2 H	14,03
CS BUS 3 H	14,03
CS BUS 4 H	14,03
CS BUT 0 H	03
CS BUT 1 H	03
CS BUT 2 H	03
CS BUT 3 H	03
CS BUT 4 H	03
CS BUT 5 H	03
CS FPA 0 H	03
CS FPA 1 H	03
CS FPA 2 H	03
CS FPA 3 H	03
CS HNEXT PAR H	15
CS LIT 0 H	03
CS LIT 1 H	03
CS MISC CTL 0 H	03
CS MISC CTL 1 H	03
CS MISC CTL 2 H	03
CS MISC CTL 3 H	03
CS MISC CTL 4 H	03
CS PAR 1 H	3
CS WCTRL 0 H	15
CS WCTRL 1 H	15
CS WCTRL 2 H	15
CS WCTRL 3 H	15
CS WCTRL 4 H	15
CS WCTRL 5 H	15
DPM13 TIMER INT L	15
DPM14 UVCTR BRANCH H	15
DPM17 BCLK L	03
DPM17 D CLK ENABLE H	15,12
DPM17 DO SRVC L	15
DPM17 INSTR FETCH H	12,13
DPM17 M CLK ENABLE H	15
DPM17 M CLK L	03,01,12
DPM17 PHASE 1 H	15
EIA CON SI L	12
EIA CON SO L	12
EIA TU SI L	12
EIA TU SO L	12
FORCE CON FAST L	12
FRNT PNL LOCK H	12
MIC04 STATUS VALID H	13
MIC04 V OUT H	15
MIC05 UB REQ H	13
MIC07 CORR DATA INT L	15

SIGNAL NAME	PAGE NUMBER(S)
MIC07 PTE CHK OR PROBE H	15
MIC07 UTRAP L	03,15
MIC07 WR BUS ERR INT L	15
MIC10 INTERRUPT INH H	03
MICRO VECTOR 0 H	15
MICRO VECTOR 1 H	15
MICRO VECTOR 2 H	15
PB INIT L	15
RD INT INH H	12
RDM V CLOCK H	15
RDM V LOAD H	15
RDM PRESENT L	12
SY ID 0 H	15
SY ID 1 H	15
SY ID 2 H	15
SY ID 3 H	15
SY ID 4 H	15
SY ID 5 H	15
SY ID 6 H	15
SY ID 7 H	15
TOT BATTERY	01
UB1 BUF CMI 00 H	10,07,09,08
UB1 BUF CMI 01 H	07,09,08
UB1 BUF CMI 02 H	09,08,07,06,05
UB1 BUF CMI 03 H	07,08,09,06,05
UB1 BUF CMI 04 H	08,09,07,06,05
UB1 BUF CMI 05 H	08,09,07,06,05
UB1 BUF CMI 06 H	08,07,09,06,05
UB1 BUF CMI 07 H	07,08,09,06,05
UB1 BUF CMI 08 H	07,08,09,06,05
UB1 BUF CMI 09 H	07,09,08,05
UB1 BUF CMI 10 H	09,07,08,05
UB1 BUF CMI 11 H	09,07,08,05
UB1 BUF CMI 12 H	08,09,07,05
UB1 BUF CMI 13 H	08,09,07,05
UB1 BUF CMI 14 H	08,07,09,05
UB1 BUF CMI 15 H	07,09,05
UB1 BUF CMI 16 H	07,09,05
UB1 BUF CMI 17 H	07,09,05
UB1 BUF CMI 18 H	07,09
UB1 BUF CMI 19 H	07,09
UB1 BUF CMI 20 H	09,07
UB1 BUF CMI 21 H	08,09,07
UB1 BUF CMI 22 H	08,07,09
UB1 BUF CMI 23 H	07,09
UB1 BUF CMI 24 H	1
UB1 BUF CMI 25 H	10,07,08
UB1 BUF CMI 26 H	10,07
UB1 BUF CMI 27 H	10,07
UB1 BUF CMI 28 H	10,07
UB1 BUF CMI 29 H	10,07

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digital	DRN. <i>DE</i>	DATE	ENG.	DATE	TITLE: UBI
	CHK'D.	26-NOV-81			FORWARD REFERENCE
(160,1271)UB116.DRW		06-NOV-80	9:41	NEXT HIGHER ASSEMBLY:	SIZE CODE NUMBER REV.
FIRST USED ON OPTION/MODEL: 11/250		B-DD-L0004-0-0		.D CS L0004-0-16 0	

SIGNAL NAME	PAGE NUMBER(S)
UB1 BUF CMI 30 H	10.07
UB1 BUF CMI 31 H	10.08,07
UB1 DP SEL 0 H	08
UB1 DP SEL 1 H	08
UB1 LATCH DP SEL 0 H	10.07,08
UB1 LATCH DP SEL 1 H	10.07,08
UB1 LATCH OFFSET H	10.07,08
UB1 MATCH H	10.07
UB1 OFFSET H	08
UB1 UA 02 H	07.06
UB1 UA 03 H	07.06
UB1 UA 04 H	07.06
UB1 UA 05 H	07.06
UB1 UA 06 H	07.06
UB1 UA 07 H	07.06
UB1 UA 08 H	10.08,07,06
UB1 UA 09 H	10.08,07,06
UB1 UA 10 H	10.08,07,06
UB1 UA 11 H	10.08,07,06
UB1 UA 12 H	08,07,06
UB1 UA 13 H	08,07,06
UB1 UA 14 H	08,07,06
UB1 UA 15 H	07,08,06
UB1 UA 16 H	07,08,06
UB1 UA 17 H	08,07,06
UB101 BATT DC LO L	01
UB101 COUNTER SEL 0 H	02,01
UB101 COUNTER SEL 1 H	02,01
UB101 CPU DC LO H	01
UB101 RESET TOY COUNTER H	02,01
UB101 WBUS GET TOY L	02,01
UB101 WCTRL BXXXXX L	01,15
UB102 TOY COUNTER 00 H	02
UB102 TOY COUNTER 01 H	02
UB102 TOY COUNTER 02 H	02
UB102 TOY COUNTER 03 H	02
UB102 TOY COUNTER 04 H	02
UB102 TOY COUNTER 05 H	02
UB102 TOY COUNTER 06 H	02
UB102 TOY COUNTER 07 H	02
UB102 TOY COUNTER 08 H	02
UB102 TOY COUNTER 09 H	02
UB102 TOY COUNTER 10 H	02
UB102 TOY COUNTER 11 H	02
UB102 TOY COUNTER 12 H	02
UB102 TOY COUNTER 13 H	02
UB102 TOY COUNTER 14 H	02
UB102 TOY COUNTER 15 H	02
UB102 TOY COUNTER 16 H	02
UB102 TOY COUNTER 17 H	02

SIGNAL NAME	PAGE NUMBER(S)
UB102 TOY COUNTER 18 H	02
UB102 TOY COUNTER 19 H	02
UB102 TOY COUNTER 20 H	02
UB102 TOY COUNTER 21 H	02
UB102 TOY COUNTER 22 H	02
UB102 TOY COUNTER 23 H	02
UB102 TOY COUNTER 24 H	02
UB102 TOY COUNTER 25 H	02
UB102 TOY COUNTER 26 H	02
UB102 TOY COUNTER 27 H	02
UB102 TOY COUNTER 28 H	02
UB102 TOY COUNTER 29 H	02
UB102 TOY COUNTER 30 H	02
UB102 TOY COUNTER 31 H	02
UB103 B CLK L	11,10,13,07,15,03
UB103 BUS 0 H	03
UB103 BUS 1 H	03
UB103 BUS 2 H	03
UB103 BUS 3 H	03
UB103 BUS 4 H	03
UB103 BUSF PAR H	03,15
UB103 CS BUT 0 H	03
UB103 CS BUT 1 H	03
UB103 CS BUT 2 H	03
UB103 CS BUT 3 H	03
UB103 CS BUT 4 H	03
UB103 CS BUT 5 H	03
UB103 CS LIT 0 H	03
UB103 CS LIT 1 H	03
UB103 FORCE CACHE PE L	15,03
UB103 FORCE TB PE L	15,03
UB103 INTERRUPT INH H	03,15
UB103 MISC 0 H	03
UB103 MISC 1 H	03
UB103 MISC 2 H	03
UB103 MISC 3 H	03
UB103 MISC 4 H	03
UB103 RSBC L	03
UB103 RTUT DINH L	15,03,14
UB103 RTUT L	03
UB105 A02 H	05,06
UB105 A03 H	05,06
UB105 A04 H	05,06
UB105 A05 H	05,06
UB105 A06 H	05,06
UB105 A07 H	05,06
UB105 A08 H	05,06
UB105 A09 H	05,06
UB105 A10 H	06,05
UB105 A11 H	06,05
UB105 A12 H	06,05

SIGNAL NAME	PAGE NUMBER(S)
UB105 A13 H	06,05
UB105 A14 H	06,05
UB105 A15 H	06,05
UB105 A16 H	06,05
UB105 A17 H	06,05
UB105 EN MAP ADDR L	09,06
UB105 INC JA 02 H	06,05
UB105 INC JA 03 H	06,05
UB105 INC JA 04 H	06,05
UB105 INC JA 05 H	06,05
UB105 INC JA 06 H	05,06
UB105 INC JA 07 H	05,06
UB105 INC JA 08 H	05,06
UB105 INC JA 09 H	05,06
UB105 INC JA 10 H	06,05
UB105 INC JA 11 H	06,05
UB105 INC JA 12 H	06,05
UB105 INC JA 13 H	06,05
UB105 INC JA 14 H	06,05
UB105 INC JA 15 H	06,05
UB105 INC JA 16 H	06,05
UB105 INC JA 17 H	06,05
UB106 RCY ADDR L	06
UB107 ADDC H	10,11,07
UB107 ADDU H	10,07
UB107 EN LBO L	07
UB107 SLAVE EN H	07,10
UB107 UD 00 H	07
UB107 UD 01 H	07
UB107 UD 02 H	07
UB107 UD 03 H	07
UB107 UD 04 H	07
UB107 UD 05 H	07
UB107 UD 06 H	07
UB107 UD 07 H	07
UB107 UD 08 H	07
UB107 UD 09 H	07
UB107 UD 10 H	07
UB107 UD 11 H	07
UB107 UD 12 H	07
UB107 UD 13 H	07
UB107 UD 14 H	07
UB107 UD 15 H	07
UB108 EN RD MAP L	09,08
UB108 MAP OUT 00 H	09,08
UB108 MAP OUT 01 H	09,08
UB108 MAP OUT 02 H	09,08
UB108 MAP OUT 03 H	08,09
UB108 MAP OUT 04 H	08,09
UB108 MAP OUT 05 H	08,09

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CHK CHANGE NO. REV

digital	DRN. <i>DE</i>	DATE <i>26-NOV-81</i>	ENG.	DATE	TITLE: UB1 FORWARD REFERENCE
	CHK'D.	DATE	BOARD LOCATION: <i>AC4</i>	SHEET	OF
FIRST USED ON OPTION/MODEL: <i>117752</i>		NEXT HIGHER ASSEMBLY: <i>B-DD-L0004-0-0</i>		SIZE: <i>D</i>	CODE: <i>CS</i>
				NUMBER: <i>0004-0-17</i>	REV. <i>D</i>

SIGNAL NAME PAGE NUMBER(S)

UB100 MAP OUT 06 H 00,09
 UB100 MAP OUT 07 H 00,09
 UB100 MAP OUT 08 H 00,09
 UB100 MAP OUT 09 H 00,09
 UB100 MAP OUT 10 H 00,09
 UB100 MAP OUT 11 H 00,09
 UB100 MAP OUT 12 H 00,09
 UB100 MAP OUT 13 H 00,09
 UB100 MAP OUT 14 H 00,09
 UB100 MAP VALID H 00

UB100 MAP VALID L 00,10
 UB100 WRITE MAP L 00
 UB110 A0 H 10,07,06
 UB110 A1 H 10,07,06
 UB110 C0 H 10,06
 UB110 C1 H 10,06
 UB110 CLOCKED DBB2 H 10
 UB110 CUI BCLK H 11,15,13,00
 UB110 CUI BCLK L 11,10,13
 UB110 CUI DBB2 L 11,07,10

UB110 CUI PREV DBB2 H 10,07
 UB110 CUI PREV DBB2 L 10,11
 UB110 DATIP BBST1 L 13,10
 UB110 ENABLE ARB REQ H 11,10
 UB110 INTR H 10
 UB110 LATCH LINE INT L 10,15
 UB110 MAP CTRL OUT EN L 10,08
 UB110 MSYN H 10,07
 UB110 PA H 10
 UB110 PB H 10
 UB110 SC 0 H 10,07,08
 UB110 SC 0 L 10,06,08
 UB110 SC 1 H 11,10,07,06
 UB110 SC 1 L 11,08,10
 UB110 SC0 L 1
 UB110 SSYN H 10,13
 UB110 SYNC INTR H 10
 UB110 SYNC MSYN H 10,07
 UB110 TIM CNT H 10
 UB110 UCR A0 L 11,10
 UB110 UCR A1 L 11,10

UB110 UCR A2 L 11,10
 UB110 UCR A3 L 11,10
 UB111 BOPC 0 H 11,07
 UB111 BOPC 1 H 11,07
 UB111 BOPC 2 H 11,07
 UB111 CMI ARB 4 H 10,11
 UB111 MAP OUT EN H 11,06
 UB111 PRTC 0 H 11,07
 UB111 PRTC 1 H 11,07
 UB111 PRTC 2 H 11,07

SIGNAL NAME PAGE NUMBER(S)

UB111 RCV UB DATA H 11,10
 UB111 RCV UB DATA L 11,07
 UB111 ROM CLK L 11,10
 UB111 STALL H 11
 UB111 STALL L 11,10,08
 UB111 UA CTRL 0 H 11,06,05
 UB111 UA CTRL 1 H 11,06,05
 UB111 UCR MSYN H 11,08,10
 UB111 UCR MSYN L 11,10
 UB111 UCR NXT 0 H 11,10

UB111 UCR NXT 1 H 11,10
 UB111 UCR NXT 2 H 11,10
 UB111 UCR NXT 3 H 11,10
 UB111 UCR NXT 4 H 11
 UB111 UCR NXT 5 H 11
 UB111 UCR NXT 6 H 11
 UB111 UCR SSYN H 11,10
 UB111 UCR SSYN L 11,07
 UB111 UCRV BUT 0 H 11,10
 UB111 UCRV BUT 1 H 11,10

UB111 UCRV BUT 2 H 11,10
 UB111 XMIT UB DATA L 11,07
 UB112 BREAK CLK H 12
 UB112 BREAK L 12
 UB112 CON BR CLK H 12
 UB112 CON DONE SYNC H 12
 UB112 CON HALT L 15,12
 UB112 CON T READY SYNC H 12
 UB112 HALT DET BR SYNC H 12
 UB112 HALT DET SYNC H 12
 UB112 SERIAL LINE INT L 10,12

UB112 SET BREAK L 12
 UB112 TU BR CLK H 12
 UB112 TU DONE SYNC H 12
 UB112 TU T READY SYNC H 12
 UB112 TUS0 INT L 15,12
 UB113 ASSERT BBST H 15,13
 UB113 BBST REQ H 13
 UB113 BG EN H 13,15
 UB113 BG EN L 13,10
 UB113 BUS1 AVAILABLE H 13

UB113 CMI UB INH L 13
 UB113 CMI UB REQ H 13
 UB113 CMI UB REQ L 13
 UB113 CPU BBST L 10,13
 UB113 CPU UB REQ L 13
 UB113 NOSACK TIMEOUT H 13,15
 UB113 NPG DEL H 13
 UB113 NPG DEL L 13
 UB113 NPG ENA H 13
 UB113 NPG INH H 13

SIGNAL NAME PAGE NUMBER(S)

UB113 NPG INH L 13
 UB113 NPG L 13,15
 UB113 SACK DLYED H 13,10
 UB113 SACK DLYED L 13
 UB113 ST CPU BBST L 13
 UB113 ST NPG H 13
 UB113 SYNCHR RESET BG H 13,15
 UB114 ACLO BBST L 13,14
 UB114 ASSERT DCLD H 15,14
 UB114 DCLD BBST L 14,13

UB114 INIT TIME L 14
 UB114 INIT UB REQ H 14
 UB114 INIT UB REQ L 13,14
 UB114 MSEQ INIT L 14,12
 UB114 PR INIT DEC L 14,15
 UB114 SPFI L 14,15
 UB114 UB INIT H 11,10,15,14
 UB115 BBST H 13,15
 UB115 EN REV LEVEL L 15
 UB115 KPSG4 H 15
 UB115 KPSG5 H 15
 UB115 KPSG6 H 15
 UB115 INT INTR L 15
 UB115 INT PEND L 15
 UB115 INT CLK L 15,14
 UB115 NPG H 13,15
 UB115 PG INIT H 15,14
 UB115 PROC INIT L 15
 UB115 PCVD ACLO H 15
 UB115 PCVD DCLD H 14,15
 UB115 SACK H 13,15

UB115 SB04 H 15
 UB115 SB05 H 15
 UB115 SB06 H 15
 UB115 SB07 H 15
 UB115 SYNCHR ACLO H 14,15
 UB115 UB INT GRANT H 15,13
 UB115 X OUT H 15
 UB115 XCTRL 0 H 15,01,12
 UB115 XCTRL 1 H 15,01,12
 UB115 XCTRL 2 H 15,01,12

UB115 XCTRL 3 H 15,01,12
 UB115 XCTRL 4 H 15,01,12
 UB115 XCTRL 5 H 15,01,12
 UBUS A00 L 04,06
 UBUS A01 L 04,06
 UBUS A02 L 04,05
 UBUS A03 L 04,05
 UBUS A04 L 04,05
 UBUS A05 L 04,05
 UBUS A06 L 04,05

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REV.	CHG.	CHANGE NO.	REV.

digital	DRN. 100	DATE 13-MAR-81	ENG.	DATE	TITLE: UBI FORWARD REFERENCE
	CHK'D.	DATE 13-MAR-81 15:39	BOARD LOCATION: AC4	SHEET 1	NUMBER
FIRST USED ON OPTION/MODEL: 160,1271/UB118,DRW		NEXT HIGHER ASSEMBLY: B-DD-L0004-0-0		SIZE CODE D CS	NUMBER L0004-0-18
					REV. F

SIGNAL NAME	PAGE NUMBER(S)
UBUS A07 L	04,05
UBUS A08 L	04,05
UBUS A09 L	04,05
UBUS A10 L	04,05
UBUS A11 L	04,05
UBUS A12 L	04,05
UBUS A13 L	04,05
UBUS A14 L	04,05
UBUS A15 L	04,05
UBUS A16 L	04,05
UBUS A17 L	04,05
UBUS ACLO L	04,15
UBUS B85Y L	15,04
UBUS BG4 0 H	04,15
UBUS BG5 0 H	04,15
UBUS BG6 0 H	04,15
UBUS BG7 0 H	04,15
UBUS BR4 L	04,15
UBUS BR5 L	04,15
UBUS BR6 L	04,15
UBUS BR7 L	04,15
UBUS C0 L	04,06
UBUS C1 L	04,06
UBUS D00 L	04,07
UBUS D01 L	04,07
UBUS D02 L	04,07
UBUS D03 L	04,07
UBUS D04 L	04,07
UBUS D05 L	04,07
UBUS D06 L	04,07
UBUS D07 L	04,07
UBUS D08 L	07,04
UBUS D09 L	10,07,04
UBUS D10 L	07,04
UBUS D11 L	07,04
UBUS D12 L	07,04
UBUS D13 L	07,04
UBUS D14 L	07,04
UBUS D15 L	07,04
UBUS DCLO L	04,01,15
UBUS INIT L	15,04
UBUS INTR L	10,04
UBUS MSYN L	10,04
UBUS NPG H	15,04
UBUS NPR L	15,04
UBUS PA L	10,04
UBUS PB L	10,04
UBUS SACK L	15,04
UBUS SSYN L	10,04
WBUS 16 H	02,01,15,12

SIGNAL NAME	PAGE NUMBER(S)
WBUS 17 H	02,01,15,12
WBUS 18 H	01,02,15,12
WBUS 19 H	01,02,15,12
WBUS 20 H	01,02,15,12
WBUS 21 H	01,02,15,12
WBUS 22 H	01,02,15,12
WBUS 23 H	02,15,12
WBUS 24 H	01,15,12
WBUS 25 H	01,15,12
WBUS 26 H	01,15
WBUS 27 H	01

SIGNAL NAME	PAGE NUMBER(S)
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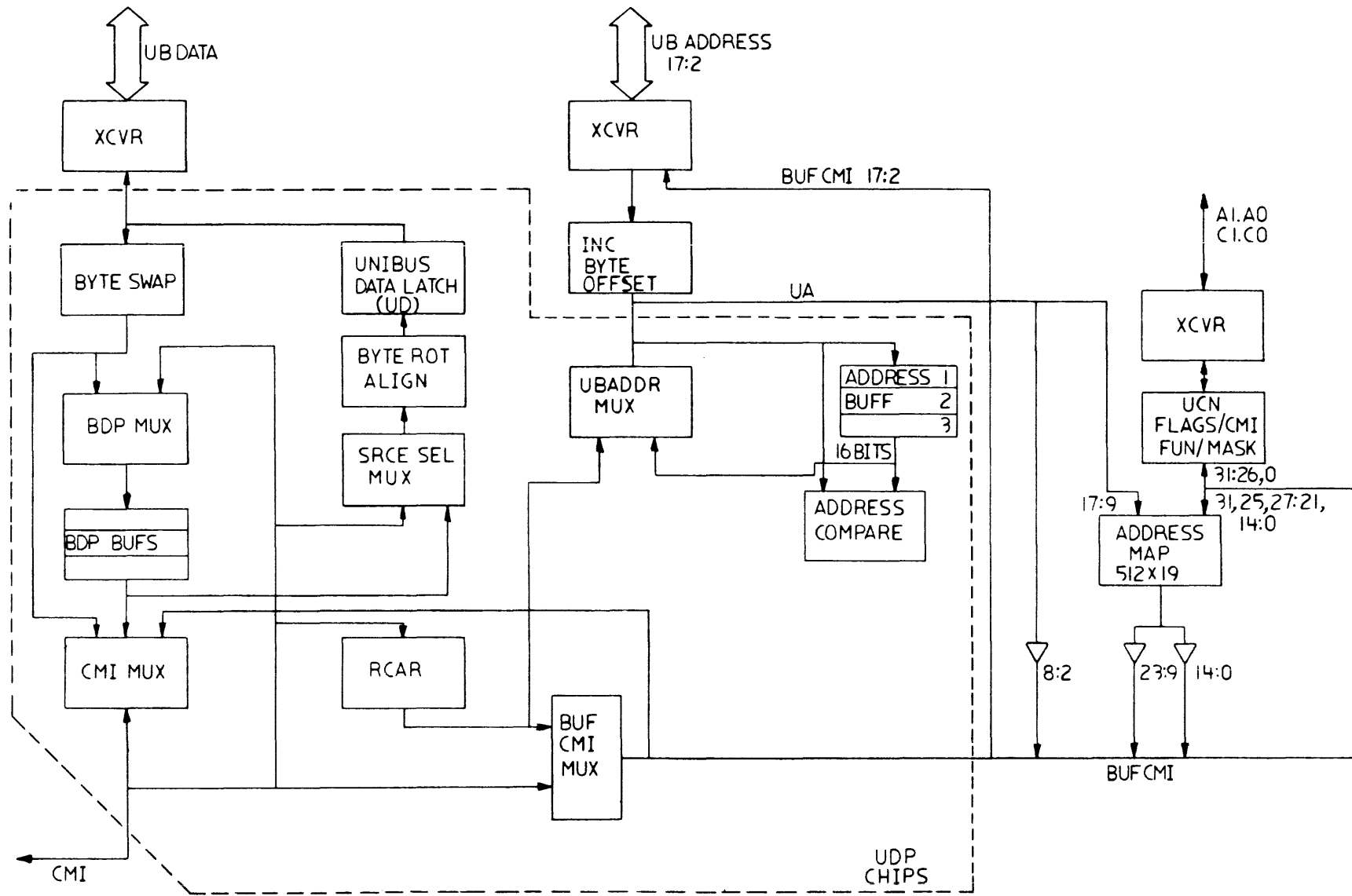
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REVISIONS	
CHK	CHANGE NO. REV

digital	DRN. <i>DE</i>	DATE 21-MAR-88	ENG.	DATE	TITLE: UBI FORWARD REFERENCE
	CHK'D.	DATE	BOARD LOCATION: ACH	SHEET 1 OF 1	
C160,1271 JUB119.DRW		21-MAR-88 14:31	NEXT HIGHER ASSEMBLY: B-DD-L0004-0-0	SIZE CODE D CS	NUMBER L0004-0-19
FIRST USED ON OPTION/MODEL: 11/750					REV. C

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BDPC			
2	1	0	
0	0	0	IDLE
0	0	1	BDP ← CMI UD ← CMI
0	1	1	UD ← CMI FOR WRAP AROUND
1	0	0	BDP ← UNIBUS BYTE ↓
1	0	1	BDP ← UNIBUS (2 BYTES)
1	1	0	BDP ← UNIBUS (1 BYTES)

PRTC			
2	1	0	
0	0	0	IDLE
0	0	1	UNIBUS DEVICE DOING DATA
0	1	0	PUT BUF CMI ON CMI FOR ADDRESS
0	1	1	PUT ADDRESS REG IN UDP ON UA (PURGE)
1	0	0	UNIBUS DEVICE DOING DATA
1	0	1	CPU DOING UNIBUS WRITE
1	1	1	CPU DOING UNIBUS READ

SC			
1	1	0	
0	0	0	WRITE MAP/CSR
0	1	0	READ MAP/CSR
1	1	0	IDLE

REV.	CHANGE NO.

DESCRIPTION	DWG./PART NO.	ITEM NO.					
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES							
ANGLES 40° 30'	CLASS OF ACCURACY	NOMINAL DIMENSION RANGE INCHES					
SURFACE QUALITY IN MICROINCHES	(CHECK ONE)	OVER 0 TO 0.2	OVER 0.2 TO 1.2	OVER 1.2 TO 4.9	OVER 4.9 TO 12.9	OVER 12.9 TO 48.9	OVER 48.9 TO 80.9
		MEDIUM	PREFERRED	2.004	2.008	2.012	2.016
QUANTITY & VARIATION	DRN. I. McLaughlin 1/2/80	FIRST USED ON 11750					
THIRD ANGLE PROJECTION	CHK'D L. Kern 3/2/80	TITLE UNIBUS INTERFACE					
REMOVE BURRS AND BREAK SHARP CORNERS	ENG. S. Smith 3/2/80	PROJ. ENG. D. Case 3/2/80					
DO NOT SCALE DWG	PROD. M. P. 3/2/80	NEXT HIGHER ASSY.					
MATERIAL	100040-0	SIZE CODE					
FINISH	SCALE	NUMBER					
	SHEET 1 OF 1	DIST.					

: 1 COMET UNIBUS INTERFACE MICROCODE REV 015 12/19/79
: 33 FIELD DEFINITIONS
: 137 FIRST FORK BREAKOUT
: 231 CPU READS AND WRITES TO THE UNIBUS ARE HANDLED IN THIS SECTION
: 293 DATO THROUGH BUFFERED DATA PATH
: 348 BDP DATI'S
: 454 THIS SECTION HANDLES DATO'S TO THE DDP
: 563 THIS PAGE IS WHERE WE COME FOR DATI'S THROUGH THE DIRECT DATA PATH
: 618 PURGE CODE
: 650 POWER UP CODE

: 11 .TOC "COMET UNIBUS INTERFACE MICROCODE REV 015 12/19/79"
: 12 :
: 13 :001 7/23/79 DC FIXED DEFINITION OF UA,CTRL,RCV AND RVC,INCR WERE SWAPPED
: 14 : FIXED DDP AND BDP DATI CODE TO HOLD BYTE 0 OF DATA IN UD
: 15 : LATCH FOR OFFSET CASE.
: 16 : ONLY E8,E7 CHANGE
: 17 :002 7/23/79 DC FIXED SEQUENCE AT BEGINNING OF READ SO AS TO NOT
: 18 : CAUSE UDP'S TO DRIVE UA BUS. E8,E7 CHANGE
: 19 :003 7/23/79 PB CHANGED DEFAULT OF UA,CTRL TO "2" TO GO ALONG WITH CHANGE
: 110 : FOR REV 001.
: 111 :004 7/24/79 DC ADDED CONSTRAINED WORD IN DDP,DATO WRAP CODE
: 112 : TO ALLOW UBI TO HOLD DATO DATA ON BUS PROPERLY
: 113 : ALL ROMS
: 114 :005 7/24/79 DC FIXED BDP DATI CODE TO HOLD BYTE 0 ON WRAP-CHANGE
: 115 : TO URDATA FIELD TO KEEP HI-Z. E9 CHANGES
: 116 :006 7/24/79 DC FIXED DATO BDP NO WRITE TO ASSERT SSYN BEFORE
: 117 : CHECKING TO SEE IF ITS THERE. E12,E11 CHANGE
: 118 : 6A 8/13 DC CHANGED PFORMAT CONTROL FILE TO BLAST BDPD FIELD LOW TRUE
: 119 : ONLY E7 CHANGES
: 120 :007 9/14/79 DC ADDED APB FOR CMI DURING WRAP AROUND READS AND WRITES
: 121 : TO KEEP CMI DURING BOTH SETS
: 122 : 7A 9/18/79 DC CHANGED PFORMAT CONTROL TIO BLAST BUT<1> LOW TRUE
: 123 : ONLY E10 CHANGES
: 124 :009 9/21/79 CHANGED MAIN.20, ALL ROMS
: 125 : 9A 9/24/79 DC CHANGED PFORMAT TO BLAST BUT<0> LOW TRUE
: 126 : E10 CHANGES
: 127 :010 9/26/79 CHANGED BDP,DATI,50, DDP,40 AND DDP,47 TO BUT ON
: 128 : SSYN INSTEAD OF MSYN
: 129 :013 10/9 DC NUMEROUS CHANGES TO FIX UNALIGNED AND PB PROBLEMS
: 130 :015 12/19/79 DC FIX TO CPU,RD TO PREVENT IT FROM LEAVING
: 131 : GARBAGE ON THE CMI. E8,E9 CHANGE
: 132 :

```
133 .TOC "FIELD DEFINITIONS"
134 .RTOI
135 .HEXADECIMAL
136 ;
137 ;
138 ;CODE DIVIDED INTO ROMS AS FOLLOWS:
139 ; <23120> E12
140 ; <19116> F11
141 ; <15112> F7
142 ; <11108> E8
143 ; <7104> E9
144 ; <3100> E10
145 ;
146 ;
147 BUFCMI/= <23123>, .DEFAULT=0
148 ADDR=1 ;PUT MAP PFN AND LOW BITS OF UBUS ADDR ON BUFCMI
149 HI-Z=0
150
151 NEXT/= <22116>, .NEXTADDRESS
152
153 BOPC/= <15113>, .DEFAULT=0 ;CONTROLS BDP DATA/ADDR LATCHES
154 DATI=1 ;PDP<-CMI, UD<-CMI/RDP (NOT BYTE 0 IF BYTE OFFSET),ADDR
155 DATI=3 ;UD<-BDP/CMI
156 DATO=4 ;BDP BYTE 0<-UNIBUS DATA,ADDR
157 DATO=5 ;RDP<-UNIBUS DATA (2 BYTES) FUNCTION OF A1,OFFSET,ADDR
158 DATO=6 ;BDP<-UNIBUS DATA (1 BYTE) FUNCTION OF A1,OFFSET,ADDR
159
160 PRIC/= <12110>, .DEFAULT=0 ;CONTROLS DATA PORTS ON UDP CHIPS
161 DATI=1 ;URUS DEVICE DOING DATI(P)
162 UR.ADDR=2 ;ADDRESS FROM UBUS TO CMI
163 PURGE.ADDR=3 ;ADDRESS FROM BAR TO CMI
164 DATO=4
165 CPU.WRT=5
166 CPU.PD=7
167
168 UA.CTRL/= <919>, .DEFAULT=2 ;CONTROLS UNIBUS ADDRESS XCVRS
169 XMIT=0 ;DRIVE UNIBUS ADDRESS LINES
170 HI-Z=1
171 RCV.INCR=3 ;RECEIVE AND INCREMENT UNIBUS ADDRESS
172 RCV=2 ;RECEIVE UNIBUS ADDRESS
173
174 MSYN/= <717>, .DEFAULT=0
175 ASSERT=1
176
177
```

```
178 SSYN/= <616>, .DEFAULT=0
179 ASSERT=1
180
181 UBDATA/= <514>, .DEFAULT=2 ;CONTROLS URUS DATA XCVRS
182 RCV=2
183 DRIVE.UD=1 ;DRIVE UBUS DATA LINES
184 DRIVE.UD.NOPB=3 ;DRIVE UBUS DATA BUT NOT PB LINES
185 HI-Z=0
186
187 CMI.ARB/= <313>, .DEFAULT=0
188 REQUEST=1
189
190 RUT/= <210>, .DEFAULT=0
191
192 EMPTY=1 ;<MSYN, EMPTY PURGE>
193 ARB=2 ;<MSYN, WON THE BUS L>
194 SET.FLAG=3 ;<MSYN, WON THE BUS L>, FLAGS<-0001
195 UB.STATUS=4 ;<MSYN, SSYN OR TIMEOUT>
196 CLK.FLAGS=5 ;<MSYN, SSYN OR TIMEOUT>, CLOCK FLAGS
197 CMI.STATUS=6 ;<WRAP L, DBBZ L, NXM L>
198 FIRST.FORK=7 ;0000 BDP DATOB CMI WRITE NEEDED
199 ;0001 BDP DATO CMI WRITE NEEDED
200 ;0010 RDP DATOB NO WRITE
201 ;0011 RDP DATO NO WRITE, OR WRAP WITH NO MATCH
202 ;0100 BDP DATI WRAP 1ST WORD AVAILABLE
203 ;0101 BDP DATI DATA AVAILABLE
204 ;0110 RDP DATOB OFFSET PUTS IN NEXT LONGWORD
205 ;0111 BDP DATI NO DATA AVAILABLE
206 ;1000 CPU WRITE
207 ;1001 CPU READ
208 ;1010 DDP DATO(B)
209 ;1011 DDP DATI(P)
210 ;1100 PURGE
211 ;1101 PURGE
212 ;1110 DDP DATOB OFFSET PUTS IN NEXT LONGWORD, OR INT
213 ;1111 NOTHING GOING ON
214
```

```

1115 REG.WRT? "CMI.ARB/REQUEST,PRTC/UB.ADDR,BUT/ARB,BUFCMI/ADDR"
1116 REG.RD? "CMI.ARB/REQUEST,PRTC/UB.ADDR,BUT/ARB,UBDATA/HI-Z,BUFCMI/ADDR"
1117 REG.PUR? "CMI.ARB/REQUEST,PRTC/PURGE,ADDR,UA,CTRL/HI-Z,BUT/ARB,BUFCMI/ADDR"
1118 EMPTY? "BUT/EMPTY"
1119 FIRST.FORK? "BUT/FIRST.FORK,NEXT/MAIN.LOOP"
1120 CMI.STAT? "BUT/CMI.STATUS"
1121 UB.STAT? "BUT/UB.STATUS"
1122
1123 SSYN "SSYN/ASSERT"
1124 MSYN "MSYN/ASSEPT"
1125 INCR "UA,CTRL/RCV,INCR"
1126 REQ "CMI.ARB/REQUEST"
1127
1128 DP_CMI "BDPC/DATI,PRTC/DATI,UBDATA/HI-Z"
1129 DP_CMI.W "BDPC/DATI.W,PRTC/DATI,UBDATA/HI-Z,BUFCMI/ADDR"
1130 UB_CMI.WRT "PRTC/CPU,WRT,UBDATA/DRIVE,UD,UA,CTRL/XMIT"
1131 UB_CMI.WRT.NOPB "PRTC/CPU,WRT,UBDATA/DRIVE,UD,NOPB,UA,CTRL/XMIT"
1132 UB_CMI.ADDR "PRTC/CPU,RD,UA,CTRL/XMIT"
1133 UB.RD_DP "PRTC/DATI,UBDATA/DRIVE,UD"
1134 REQ.XTRA? "CMI.ARB/REQUEST,PRTC/UB.ADDR,BUT/SET,FLAG,UA,CTRL/RCV,INCR,BUFCMI/ADDR"
1135 HOLD.B0 "BDPC/DATI,UBDATA/HI-Z"
1136
    
```

```

1137 .TOC "FIRST FORK BREAKOUT"
1138 *0000
1139 MAIN.LOOP: ;THIS IS THE TOP OF FIRST FORK
1140 ;0000-----;
1141 BUT/CLK.FLAGS, ;BDP DATOB; CMI WRITE
1142 BDPC/DATOB,
U 000, 4FC2,25 1143 NEXT/BDP.DATO
1144
1145 ;0001-----;
1146 BUT/CLK.FLAGS, ;BDP DATO; CMI WRITE
1147 BDPC/DATO, ;PUT DATA IN BUFFER
U 001, 4BA2,25 1148 NEXT/BDP.DATO
1149
1150 ;0010-----;
1151 BDPC/DATOB, ;BDP DATOB,BUFFER NOT FULL
1152 BUT/CLK.FLAGS,
U 002, 1FC2,25 1153 NEXT/BDP.DATO,20
1154
1155 ;0011-----;
1156 BDPC/DATO, ;BDP DATO,BUFFER NOT FULL
1157 BUT/CLK.FLAGS, ;OR OFFSET CAUSING WRAPAROUND
U 003, 12A2,25 1158 NEXT/MAIN,20
1159
1160 ;0100-----;
1161 BDPC/DATI.W, ;BDP DATI, LONGWORD WRAP
1162 REQ.RD?, INCR,
U 004, D46B,0A 1163 NEXT/BDP.DATI,30 ;FIRST WORD IS IN THE BUFFER
1164
1165 ;0101-----;
1166 PRTC/DATI,BDPC/DATI.W, ;BDP DATI, DATA AVAILABLE IN
1167 UBDATA/HI-Z,REQ, ;BUFFER*****
U 005, 2F66,08 1168 NEXT/BDP.DATI,45
1169
1170 ;0110-----;
1171 BDPC/DATOB, ;BDP DATOB, OFFSET PUTS BYTE
1172 INCR,BUT/SET.FLAG, ;IN NEXT LONGWORD
U 006, 1F83,23 1173 NEXT/BDP.DATO,20
1174
1175 ;0111-----;
1176 REQ.RD?, ;BDP DATI, BUFFER EMPTY
U 007, D00A,0A 1177 NEXT/BDP.DATI,10
1178
    
```

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1179 ;THIS PAGE HOLDS THE SECOND EIGHT PLACES WHERE THE FIRST FORK GOES TO
1180
1181 ;1000-----;
1182 PRTC/CPU.WRT, ;CPU DOING WRITE TO UNIBUS
1183 UB,DATA/HI=Z, ;GET READY TO ASSERT STUFF
1184 UA,CTRL/HI=Z, ;ON UNIBUS
1185 UB,STAT?, ;CHECK SSYN REMOVED FROM UNIBUS
U 008, 1015,04 1186 NEXT/CPU.WRT ;GO TIME DESKEW INTERVAL
1187
1188 ;1001-----;
1189 PRTC/CPU.RD, ;CPU DOING READ FROM UNIBUS
1190 UA,CTRL/HI=Z,
1191 UB,STAT?, ;SEE COMMENTS ABOVE
U 009, 1410,24 1192 NEXT/CPU.RD
1193
1194 ;1010-----;
1195 REQ,WRT?, ;DDP DATO(B)
U 00A, DC0A,2A 1196 NEXT/DDP.DATO
1197
1198 ;1011-----;
1199 REQ,RD?, ;DDP DATI
U 00B, F00A,0A 1200 NEXT/DDP.DATI
1201
1202 ;1100-----;
1203 EMPTY?, ;PURGE, CHECK FOR EMPTY
U 00C, 7602,21 1204 NEXT/PURGE
1205
1206 ;1101-----;
1207 EMPTY?, ;PURGE, CHECK FOR EMPTY
U 00D, 7602,21 1208 NEXT/PURGE
1209
1210 ;1110-----;
1211 REQ,XTRA?, ;DDP DATOB WRAP TO NEXT LONGWORD
U 00E, E00B,2B 1212 NEXT/DDP.DATO,20 ;ALSO HERE FOR INTERRUPTS
1213
1214 IDLE:
1215 ;1111-----;
U 00F, 0002,27 1216 FIRST,FORK? ;NOTHING GOING ON, KEEP TRYING
1217
1218 #1*
1219 MAIN,20: ;HERE TO SEE IF ;0011 WAS NO WRITE OR OFFSET
1220 ;-----;
U 012, 1302,26 1221 BUT/CHI,STATUS ;NOW THAT UDP ADDR IS THERE, TRY AGAIN
1222
1223 #011
1224 ;1011-----;
1225 REQ,WRT?, ;WRAP AROUND, WRITE NEEDED
U 013, C00A,2A 1226 NEXT/DDP.DATO,05
1227
1228 ;1111-----;
U 017, 6002,60 1229 SSYN,NEXT/DDP,45 ;NO WRITE NEEDED
1230

```

```

1231 .TOC "CPU READS AND WRITES TO THE UNIBUS ARE HANDLED IN THIS SECTION"
1232 #0
1233 CPU,WRT:
1234 ;10-----;
U 010, 1614,10 1235 UB,CMI,WRT, ;BRANCH COMES HERE IF SSYN NOT ASSERTED
1236 NEXT/CPU.WRT,10 ;ASSERT ADDRESS AND DATA ON UNIBUS
1237
1238 ;11-----;
1239 UB,CMI,WRT, ;HERE IF SSYN LEFT ASSERTED FROM
U 011, 1014,14 1240 UB,STAT?, ;LAST UNIBUS TRANSACTION
1241 NEXT/CPU.WRT
1242
1243 CPU,WRT,10:
1244 ;-----;
U 016, 2A14,30 1245 UB,CMI,WRT,NOPB ;EATING UP TIME FOR ADDR/DATA
1246 ;TO MSYN DESKEW
1247
1248 #10
1249 CPU,WRT,20:
1250 ;10-----;
1251 MSYN,UB,STAT?, ;ASSERT MSYN AND WAIT FOR SSYN
U 02A, 2A14,84 1252 UB,CMI,WRT,NOPB,
1253 NEXT/CPU.WRT,20
1254
1255 ;11-----;
U 02B, 2014,30 1256 UB,CMI,WRT,NOPB ;MSYN REMOVED,BECAUSE SSYN ARRIVED
1257
1258 CPU,WRT,25:
1259 ;-----;
U 028, 0F01,20 1260 UA,CTRL/HI=Z, ;PREVENT TRISTATE OVERLAP
1261 NEXT/IDLE

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K-MP-L0004-0-21-C

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1262 ;THIS SECTION FOR CPU READS TO UNIBUS
1263 #0
1264 CPU_RD:
1265 ;0-----;
1266 UB_CMI.ADDR, ;SSYN REMOVED FROM LAST TRANSACTION
1267 NEXT/CPU_RD,10 ;ASSERT ADDRESS AND BEGIN DESKEW TIME
1268
1269 ;1-----;
1270 UB_CMI.ADDR, ;SSYN STILL ASSERTED, DON'T COUNT
1271 UB_STAT?, ;DESKEW TIME YET
1272 NEXT/CPU_RD
1273
1274 CPU_RD,10:
1275 ;-----;
1276 UB_CMI.ADDR ;EAT 125 FOR DESKEW
1277
1278 #10
1279 CPU_RD,20:
1280 ;10-----;
1281 UB_CMI.ADDR, ;ASSERT MSYN AND WAIT FOR SSYN
1282 MSYN,UB_STAT?,
1283 NEXT/CPU_RD,20
1284
1285 ;11-----;
1286 UB_CMI.ADDR,MSYN ;KEEP MSYN SO SLAVE HOLDS DATA
1287
1288 ;-----;
1289 PRTC/CPU_WRT, ;KEEP ADDRESS ON UNIBUS
1290 URDATA/HI=Z,UA_CTRL/XMIT,
1291 NEXT/CPU_WRT,25
1292

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```

1293 .IOC "DATO THROUGH BUFFERED DATA PATH"
1294 ;DATO(B) THROUGH BDP THAT NEEDS TO DO A CMI WRITE
1295 #00
1296 BDP_DATO,05:
1297 ;00-----;MSYN DISAPPEARED JUST AS WE WON THE BUS
1298 PRTC/DATO,CMI_STAT?, ;GO THROUGH WITH IT ANYWAY, WE'RE ALREADY ON BUS
1299 BUFCMI/ADDR,
1300 NEXT/BDP_DATO,10
1301
1302 ;01-----;
1303 NEXT/IDLE ;LOST MSYN, ABORT
1304
1305 ;10-----;
1306 PRTC/DATO,CMI_STAT?, ;BUS WON, ASSERT DATA NEXT CYCLE
1307 BUFCMI/ADDR,
1308 NEXT/BDP_DATO,10 ;AND CHECK FOR LONGWORD WRAP&DBBZ
1309
1310 BDP_DATO:
1311 ;11-----;
1312 REQ_WRT?, ;WAITING TO WIN THE CMI
1313 NEXT/BDP_DATO,05
1314
1315 #000
1316 BDP_DATO,10:
1317 ;THESE FOUR ENTRIES OCCUR FOR THE OFFSET CASE WHERE THE DATA WRAPS
1318 ;000-----;
1319 PRTC/DATO,CMI_STAT?, ;DBBZ STILL HELD ON BUS, KEEP WAITING
1320 NEXT/BDP_DATO,10
1321
1322 ;001-----;
1323 PRTC/DATO,CMI_STAT?, ;DBBZ STILL HELD ON BUS, KEEP WAITING
1324 NEXT/BDP_DATO,10
1325
1326 ;010-----;
1327 NEXT/DDP,50 ;INXM
1328
1329 ;011-----;
1330 INCR,BDPC/DATOW, ;DBBZ HAS GONE AWAY, PUT BYTE IN
1331 BUT/SET,?LAG, ;BDP REGS AND SET FLAGS
1332 NEXT/BDP_DATO,20
1333
1334 ;THESE FOUR ARE FOR NO WRAP AROUND
1335 ;100-----;
1336 PRTC/DATO,CMI_STAT?, ;NO WRAP AROUND, DBBZ STILL ASSERTED
1337 NEXT/BDP_DATO,10 ;SO KEEP THE DATA ON THE BUS
1338
1339 ;101-----;
1340 PRTC/DATO,CMI_STAT?, ;NO WRAP AROUND, DBBZ STILL ASSERTED
1341 NEXT/BDP_DATO,10 ;SO KEEP THE DATA ON THE BUS
1342
1343 ;110-----;
1344 NEXT/DDP,50 ;INXM
1345
1346 BDP_DATO,20:
1347 ;111-----;
1348 SSYN,NEXT/DDP,45 ;DBBZ WENT AWAY, ASSERT SSYN
1349

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```
1348 .TOC "BDP DATI'S"  
1349 ;HERE FOR BDP DATI'S THAT NEED CMI ACTION  
1350 #00  
1351 BDP,DATI,10:  
1352 ;00-----  
1353 CMI,STAT?, ;GOT THE BUS, JUST AS WE LOST MSYN  
1354 BUFCMI/ADDR,  
U 050, A002,26 1355 NEXT/BDP,DATI,20  
1356  
1357 ;01-----  
U 051, 0F02,20 1358 NEXT/IDLE ;LOST MYSN, ABORT  
1359  
1360 ;10-----  
1361 CMI,STAT?, ;GOT THE BUS ,GET READY FOR DATA  
1362 BUFCMI/ADDR,  
U 052, A002,26 1363 NEXT/BDP,DATI,20  
1364  
1365 ;11-----  
1366 REQ,RD?, ;WF GOT HERE CAUSE WE DIDN'T WIN THE BUS  
U 053, D00A,0A 1367 NEXT/BDP,DATI,10  
1368  
#002  
1369 BDP,DATI,20:  
1370 ;THESE FOUR ARE FOR THE DATA WRAP AROUND CASE  
1371 ;00-----  
1372 DP_CMI,CMI,STAT?,REQ, ;DBBZ STILL ASSERTED, KEEP WAITING  
U 020, 2026,0E 1373 NEXT/BDP,DATI,20  
1374  
1375 ;001-----  
1376 DP_CMI,CMI,STAT?,REQ, ;DBBZ STILL ASSERTED, KEEP WAITING  
U 021, 2026,0E 1377 NEXT/BDP,DATI,20  
1378  
1379 ;010-----  
U 022, 6F02,20 1380 NEXT/DDP,50 ;DBBZ GONE, NXM STATUS RETURNED  
1381  
1382 ;011-----  
1383 BDP/DATI, ;DATA IN BUFFER, NOW MOVE TO UD  
1384 URDATA/HI-Z,REQ,  
U 023, 5762,08 1385 NEXT/BDP,DATI,35  
1386  
1387 ;THESE FOUR ENTRIES ARE FOR NO WRAP-AROUND  
1388 ;100-----  
1389 DP_CMI,CMI,STAT?, ;DBBZ STILL ASSERTED, KEEP WAITING  
U 024, 2026,0E 1390 NEXT/BDP,DATI,20,REQ ;*****REMOVE REQ WITH UCN-C  
1391  
1392 ;101-----  
1393 DP_CMI,CMI,STAT?, ;DBBZ STILL ASSERTED, KEEP WAITING  
U 025, 2026,0E 1394 NEXT/BDP,DATI,20,REQ ;*****REMOVE REQ WITH UCN-C  
1395  
1396 ;110-----  
U 026, 6F02,20 1397 NEXT/DDP,50 ;DBBZ GONE, NXM STATUS RETURNED  
1398  
1399 ;111-----  
1400 UR,RD_DP,SSYN, ;DBBZ'S GONE, WE GOT THE DATA  
U 027, 5B06,58 1401 NEXT/BDP,DATI,55,REQ ;*****REMOVE REQ WITH UCN-C  
1402
```

```
1403 #00  
1404 BDP,DATI,30:  
1405 ;00-----  
1406 HOLD,00,CMI,STAT?, ;GOT THE BUS ,GET READY FOR DATA  
U 054, AC23,06 1407 BUFCMI/ADDR,INCR,  
1408 NEXT/BDP,DATI,40  
1409  
1410 ;01-----  
U 055, 0F02,20 1411 NEXT/IDLE ;LOST MSYN,ABORT  
1412  
1413 ;10-----  
1414 HOLD,00,CMI,STAT?, ;GOT THE BUS ,GET READY FOR DATA  
U 056, AC23,06 1415 BUFCMI/ADDR,INCR,  
1416 NEXT/BDP,DATI,40  
1417  
1418 BDP,DATI,35:  
1419 ;11-----  
1420 REQ,RD?,INCR,BDPC/DATI, ;GET THE BUS FOR THE SECOND  
U 057, D42B,0A 1421 NEXT/BDP,DATI,30  
1422  
1423 #100  
1424 BDP,DATI,40:  
1425 ;100-----  
1426 DP_CMI,CMI,STAT?,INCR, ;DBBZ STILL ASSERTED, KEEP WAITING  
U 02C, 2C27,0E 1427 NEXT/BDP,DATI,40,REQ ;*****REMOVE REQ WITH UCN-C  
1428  
1429 ;101-----  
1430 DP_CMI,CMI,STAT?,INCR, ;DBBZ STILL ASSERTED, KEEP WAITING  
U 02D, 2C27,0E 1431 NEXT/BDP,DATI,40,REQ ;*****REMOVE REQ WITH UCN-C  
1432  
1433 ;110-----  
U 02E, 6F02,20 1434 NEXT/DDP,50 ;DBBZ GONE, NXM STATUS RETURNED  
1435  
1436 BDP,DATI,45:  
1437 ;111-----  
1438 UR,RD_DP,SSYN, ;DBBZ'S GONE, WE GOT THE DATA  
U 02F, 5B06,58 1439 NEXT/BDP,DATI,55,REQ ;*****REMOVE REQ WITH UCN-C  
1440  
1441 #10  
1442 BDP,DATI,50:  
1443 ;01-----  
U 05A, 0F02,27 1444 FIRST,FORK? ;NO MSYN, REMOVE DATA AND SSYN  
1445  
1446 BDP,DATI,55:  
1447 ;11-----  
1448 UR,RD_DP, ;  
1449 SSYN,UR,STAT?, ;WAIT FOR MSYN TO GO AWAY  
U 05B, 5A06,5C 1450 NEXT/BDP,DATI,50,REQ ;*****REMOVE REQ WITH UCN-C  
1451  
1452  
1453
```



```
1454 .TOC "THIS SECTION HANDLES DATO'S TO THE DDP"  
1455 #00  
1456 DDP,DATO:  
1457 ;00-----;  
1458 PRTC/DATO,BUFCMI/ADDR, ;WE GOT IT  
1459 CMI,STAT?,  
U 05C, 8012,26 1460 NEXT/DDP,DATO,10  
1461  
1462 ;11-----;  
U 05D, 0F02,20 1463 NEXT/IDLE ;MSYN DISAPPEARED  
1464  
1465 ;10-----;  
1466 PRTC/DATO,BUFCMI/ADDR, ;WE GOT IT  
1467 CMI,STAT?,  
U 05E, 8012,26 1468 NEXT/DDP,DATO,10  
1469  
1470 ;11-----;  
1471 REQ,WRT?, ;TRYING TO GET THE BUS  
U 05F, DC0A,2A 1472 NEXT/DDP,DATO  
1473  
1474 #000  
1475 DDP,DATO,10:  
1476 ;THE FOUR CASES ARE FOR THE WRAPAROUND SITUATION  
1477  
1478 ;000-----;  
1479 PRTC/DATO,CMI,STAT?, ;WAITING FOR DBBZ  
U 030, 3012,2E 1480 REQ,NEXT/DDP,DATO,10  
1481  
1482 ;001-----;  
1483 PRTC/DATO,CMI,STAT?, ;WAITING FOR DBEZ  
U 031, 3012,2E 1484 REQ,NEXT/DDP,DATO,10  
1485  
1486 ;010-----;  
U 032, 6F02,20 1487 NEXT/DDP,50 ;NXM STATUS  
1488  
1489 ;011-----;  
1490 REQ,XTRA?, ;DONE WITH THE FIRST, DO THE SECOND  
U 033, EP0B,2B 1491 NEXT/DDP,DATO,20  
1492  
1493 ;THESE CASES ARE FOR NO WRAP  
1494 ;100-----;  
1495 PRTC/DATO,CMI,STAT?, ;WAITING FOR DBBZ  
U 034, 3012,26 1496 NEXT/DDP,DATO,10  
1497  
1498 ;101-----;  
1499 PRTC/DATO,CMI,STAT?, ;WAITING FOR DBEZ  
U 035, 3012,26 1500 NEXT/DDP,DATO,10  
1501  
1502 ;110-----;  
U 036, 6F02,20 1503 NEXT/DDP,50 ;NXM STATUS  
1504  
1505 ;111-----;  
U 037, 6R02,60 1506 SSYN,NEXT/DDP,45 ;DONE  
1507
```

```
1508 ;THIS PAGE CONTINUES THE DDP DATO WRAP CASE CODE,  
1509 ;AND ALSO HAS THE WAITING FOR MSYN TO GO AWAY STUFF  
1510 #00  
1511 DDP,DATO,20:  
1512 ;00-----;  
1513 PRTC/DATO,BUT/SET,FLAG, ;BUS WON  
1514 INCR,BUFCMI/ADDR,  
U 060, E713,23 1515 NEXT/DDP,DATO,25  
1516  
1517 ;01-----;  
U 061, 0F02,20 1518 NEXT/IDLE ;MSYN WENT AWAY  
1519  
1520 ;10-----;  
1521 PRTC/DATO,BUT/SET,FLAG, ;BUS WON  
1522 INCR,BUFCMI/ADDR,  
U 062, E713,23 1523 NEXT/DDP,DATO,25  
1524  
1525 ;11-----;  
1526 REQ,XTRA?, ;TRYING TO GET THE BUS  
U 063, EP0B,2B 1527 NEXT/DDP,DATO,20  
1528  
1529 #11  
1530 DDP,DATO,25:  
1531 ;11-----;  
U 067, 3C12,26 1532 PRTC/DATO,CMI,STAT? ;THIS IS CONSTRAINED AS TARGET OF SET,FLAG  
1533  
1534 #100  
1535 DDP,DATO,30:  
1536 ;100-----;  
1537 PRTC/DATO,CMI,STAT?, ;WAITING FOR NO DBBZ  
U 03C, 3C12,26 1538 NEXT/DDP,DATO,30  
1539  
1540 ;101-----;  
1541 PRTC/DATO,CMI,STAT?, ;WAITING FOR NO DBBZ  
U 03D, 3C12,26 1542 NEXT/DDP,DATO,30  
1543  
1544 ;110-----;  
U 03E, 6F02,20 1545 NEXT/DDP,50 ;NXM  
1546  
1547 ;111-----;  
U 03F, 6R02,60 1548 SSYN,NEXT/DDP,45 ;DONE  
1549  
1550 #10  
1551 DDP,40:  
1552 ;10-----;  
1553 FIRST,FORK? ;NO MSYN, REMOVE DATA AND SSYN  
U 06A, 0002,27 1554  
1555  
1556 ;11-----;  
1557 SSYN,UB,STAT?, ;WAITING FOR MSYN OR INT TO GO AWAY  
U 06B, 6A02,64 1558 NEXT/DDP,40 ;BY SEEING IF SSYN GOT CLEARED  
1559  
1560 #10  
1561 DDP,50:  
1562 ;10-----;  
1563 FIRST,FORK? ;NO MSYN, REMOVE DATA AND SSYN  
U 06E, 0002,27 1564  
1565  
1566 ;11-----;  
U 06F, 6F02,24 1567 UB,STAT?,NEXT/DDP,47 ;WAITING FOR MSYN TO GO AWAY  
1568
```

```

1563 .TOC "THIS PAGE IS WHERE WE COME FOR DATI'S THROUGH THE DIRECT DATA PATH"
1564
1565 EQU
1566 DDP,DATI:
1567 ;100-----;
1568 CMI,STAT?, ;GOT THE BUS, WAIT FOR DATA
1569 BUFCMI/ADDR,
U 070, C002,26 1570 NEXT/DDP,DATI,10
1571
1572 ;101-----;
U 071, 0F02,20 1573 NEXT/IDLE ;LOST MSYN
1574
1575 ;110-----;
1576 CMI,STAT?, ;GOT THE BUS, WAIT FOR DATA
1577 BUFCMI/ADDR,
U 072, C002,26 1578 NEXT/DDP,DATI,10
1579
1580 ;111-----;
1581 REQ,RD?, ;TRY TO GET THAT BUS
U 073, F00A,0A 1582 NEXT/DDP,DATI
1583
1584 EQU
1585 DDP,DATI,101
1586 ;THESE FOUR ARE FOR THE WRAP CASE
1587 ;1000-----;
1588 DP_CMI,W,CMI,STAT?,REQ, ;WAITING FOR DATA
U 040, C066,0E 1589 NEXT/DDP,DATI,10
1590
1591 ;1001-----;
1592 DP_CMI,W,CMI,STAT?,REQ, ;WAITING FOR DATA
U 041, C066,0E 1593 NEXT/DDP,DATI,10
1594
1595 ;1010-----;
U 042, 6F02,20 1596 NEXT/DDP,50 ;NXM
1597
1598 ;1011-----;
1599 REQ,RD?,INCR,BDPC/DATI, ;WE GOT THE FIRST, NOW DO THE SECOND
U 043, D42B,0A 1600 NEXT/DDP,DATI,30
1601
1602 ;1100-----;
1603 DP_CMI,W,CMI,STAT?, ;WAITING FOR DATA, NO WRAP-AROUND
U 044, C066,0E 1604 NEXT/DDP,DATI,10,REQ ;*****REMOVE REQ WITH UCN-C
1605
1606 ;1101-----;
1607 DP_CMI,W,CMI,STAT?, ;WAITING FOR DATA, NO WRAP-AROUND
U 045, C066,0E 1608 NEXT/DDP,DATI,10,REQ ;*****REMOVE REQ WITH UCN-C
1609
1610 ;1110-----;
U 046, 6F02,20 1611 NEXT/DDP,50 ;NXM, WAIT THE MSYN OUT
1612
1613 ;1111-----;
1614 UB,RD_DP, ;DBBZ WENT AWAY
1615 SSYN, ;GIVE THE UBUS DATA, AND ISSUE SSYN
U 047, 5B06,5B 1616 NEXT/DDP,DATI,55,REQ ;*****REMOVE REQ WITH UCN-C
1617
    
```

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1618 .TOC "PURGE CODE"
1619 ;THIS PAGE HANDLES PURGES
1620
1621 EQU
1622 PURGE:
1623 ;110-----;
1624 REQ,PUR?, ;NOT EMPTY IF WE GET HERE
U 076, FA0D,2A 1625 NEXT/PURGE,10
1626
1627 ;11-----;
U 077, 0F02,20 1628 NEXT/IDLE ;IF PUREG WAS EMPTY WE CLEARED IT
1629
1630 EQU
1631 PURGE,101:
1632 ;110-----;
1633 PRTC/DATO,UA,CTPL/MI=2,
1634 BUFCMI/ADDR,
U 07A, CD11,20 1635 NEXT/PURGE,20
1636
1637 ;11-----;
1638 REQ,PUR?,
U 07B, FA0D,2A 1639 NEXT/PURGE,10
1640
1641 EQU
1642 PURGE,201:
1643 ;1101-----;
1644 PRTC/DATO,CMI,STAT?, ;DO WRITE AND WAIT FOR NO DBBZ
U 04D, 4D12,26 1645 NEXT/PURGE,20
1646
1647 ;1111-----;
U 04F, 0F02,20 1648 NEXT/IDLE ;ALL DONE
1649
    
```

; UBI .MCR [160,5507] Micro-2.1 1B(40) 8:52:33 18-Feb-1980
; UBI .MIC [160,5507] POWER UP CODE

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```
      1650 .TOC "POWER UP CODE"  
      1651 .REGION /80,0FF  
      1652  
U 080, 0F02,20 1653 NEXT/IDLE  
U 081, 0F02,20 1654 NEXT/IDLE  
U 082, 0F02,20 1655 NEXT/IDLE  
U 083, 0F02,20 1656 NEXT/IDLE  
U 084, 0F02,20 1657 NEXT/IDLE  
U 085, 0F02,20 1658 NEXT/IDLE  
U 086, 0F02,20 1659 NEXT/IDLE  
U 087, 0F02,20 1660 NEXT/IDLE  
U 088, 0F02,20 1661 NEXT/IDLE  
U 089, 0F02,20 1662 NEXT/IDLE  
U 08A, 0F02,20 1663 NEXT/IDLE  
U 08B, 0F02,20 1664 NEXT/IDLE  
U 08C, 0F02,20 1665 NEXT/IDLE  
U 08D, 0F02,20 1666 NEXT/IDLE  
U 08E, 0F02,20 1667 NEXT/IDLE  
U 08F, 0F02,20 1668 NEXT/IDLE  
U 090, 0F02,20 1669 NEXT/IDLE  
U 091, 0F02,20 1670 NEXT/IDLE  
U 092, 0F02,20 1671 NEXT/IDLE  
U 093, 0F02,20 1672 NEXT/IDLE  
U 094, 0F02,20 1673 NEXT/IDLE  
U 095, 0F02,20 1674 NEXT/IDLE  
U 096, 0F02,20 1675 NEXT/IDLE  
U 097, 0F02,20 1676 NEXT/IDLE  
U 098, 0F02,20 1677 NEXT/IDLE  
U 099, 0F02,20 1678 NEXT/IDLE  
U 09A, 0F02,20 1679 NEXT/IDLE  
U 09B, 0F02,20 1680 NEXT/IDLE  
U 09C, 0F02,20 1681 NEXT/IDLE  
U 09D, 0F02,20 1682 NEXT/IDLE  
U 09E, 0F02,20 1683 NEXT/IDLE  
U 09F, 0F02,20 1684 NEXT/IDLE  
      1685
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; UBI .MCR [160,5507] Micro-2.1 1B(40) 8:52:33 18-Feb-1980
; UBI .MIC [160,5507] POWER UP CODE

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```
U 0A0, 0F02,20 1686 NEXT/IDLE  
U 0A1, 0F02,20 1687 NEXT/IDLE  
U 0A2, 0F02,20 1688 NEXT/IDLE  
U 0A3, 0F02,20 1689 NEXT/IDLE  
U 0A4, 0F02,20 1690 NEXT/IDLE  
U 0A5, 0F02,20 1691 NEXT/IDLE  
U 0A6, 0F02,20 1692 NEXT/IDLE  
U 0A7, 0F02,20 1693 NEXT/IDLE  
U 0A8, 0F02,20 1694 NEXT/IDLE  
U 0A9, 0F02,20 1695 NEXT/IDLE  
U 0AA, 0F02,20 1696 NEXT/IDLE  
U 0AB, 0F02,20 1697 NEXT/IDLE  
U 0AC, 0F02,20 1698 NEXT/IDLE  
U 0AD, 0F02,20 1699 NEXT/IDLE  
U 0AE, 0F02,20 1700 NEXT/IDLE  
U 0AF, 0F02,20 1701 NEXT/IDLE  
U 0B0, 0F02,20 1702 NEXT/IDLE  
U 0B1, 0F02,20 1703 NEXT/IDLE  
U 0B2, 0F02,20 1704 NEXT/IDLE  
U 0B3, 0F02,20 1705 NEXT/IDLE  
U 0B4, 0F02,20 1706 NEXT/IDLE  
U 0B5, 0F02,20 1707 NEXT/IDLE  
U 0B6, 0F02,20 1708 NEXT/IDLE  
U 0B7, 0F02,20 1709 NEXT/IDLE  
U 0B8, 0F02,20 1710 NEXT/IDLE  
U 0B9, 0F02,20 1711 NEXT/IDLE  
U 0BA, 0F02,20 1712 NEXT/IDLE  
U 0BB, 0F02,20 1713 NEXT/IDLE  
U 0BC, 0F02,20 1714 NEXT/IDLE  
U 0BD, 0F02,20 1715 NEXT/IDLE  
U 0BE, 0F02,20 1716 NEXT/IDLE  
U 0BF, 0F02,20 1717 NEXT/IDLE  
      1718
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; UBI .MCR [160,5507] Micro-2.1 1B(40)
; UBI .MIC [160,5507] POWER UP CODE

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U 0C0, 0F02,20 1719 NEXT/IDLE
U 0C1, 0F02,20 1720 NEXT/IDLE
U 0C2, 0F02,20 1721 NEXT/IDLE
U 0C3, 0F02,20 1722 NEXT/IDLE
U 0C4, 0F02,20 1723 NEXT/IDLE
U 0C5, 0F02,20 1724 NEXT/IDLE
U 0C6, 0F02,20 1725 NEXT/IDLE
U 0C7, 0F02,20 1726 NEXT/IDLE
U 0C8, 0F02,20 1727 NEXT/IDLE
U 0C9, 0F02,20 1728 NEXT/IDLE
U 0CA, 0F02,20 1729 NEXT/IDLE
U 0CB, 0F02,20 1730 NEXT/IDLE
U 0CC, 0F02,20 1731 NEXT/IDLE
U 0CD, 0F02,20 1732 NEXT/IDLE
U 0CE, 0F02,20 1733 NEXT/IDLE
U 0CF, 0F02,20 1734 NEXT/IDLE
U 0D0, 0F02,20 1735 NEXT/IDLE
U 0D1, 0F02,20 1736 NEXT/IDLE
U 0D2, 0F02,20 1737 NEXT/IDLE
U 0D3, 0F02,20 1738 NEXT/IDLE
U 0D4, 0F02,20 1739 NEXT/IDLE
U 0D5, 0F02,20 1740 NEXT/IDLE
U 0D6, 0F02,20 1741 NEXT/IDLE
U 0D7, 0F02,20 1742 NEXT/IDLE
U 0D8, 0F02,20 1743 NEXT/IDLE
U 0D9, 0F02,20 1744 NEXT/IDLE
U 0DA, 0F02,20 1745 NEXT/IDLE
U 0DB, 0F02,20 1746 NEXT/IDLE
U 0DC, 0F02,20 1747 NEXT/IDLE
U 0DD, 0F02,20 1748 NEXT/IDLE
U 0DE, 0F02,20 1749 NEXT/IDLE
U 0DF, 0F02,20 1750 NEXT/IDLE
1751

; UBI .MCR [160,5507] Micro-2.1 1B(40)
; UBI .MIC [160,5507] POWER UP CODE


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U 0E0, 0F02,20 1752 NEXT/IDLE
U 0E1, 0F02,20 1753 NEXT/IDLE
U 0E2, 0F02,20 1754 NEXT/IDLE
U 0E3, 0F02,20 1755 NEXT/IDLE
U 0E4, 0F02,20 1756 NEXT/IDLE
U 0E5, 0F02,20 1757 NEXT/IDLE
U 0E6, 0F02,20 1758 NEXT/IDLE
U 0E7, 0F02,20 1759 NEXT/IDLE
U 0E8, 0F02,20 1760 NEXT/IDLE
U 0E9, 0F02,20 1761 NEXT/IDLE
U 0EA, 0F02,20 1762 NEXT/IDLE
U 0EB, 0F02,20 1763 NEXT/IDLE
U 0EC, 0F02,20 1764 NEXT/IDLE
U 0ED, 0F02,20 1765 NEXT/IDLE
U 0EE, 0F02,20 1766 NEXT/IDLE
U 0EF, 0F02,20 1767 NEXT/IDLE
U 0F0, 0F02,20 1768 NEXT/IDLE
U 0F1, 0F02,20 1769 NEXT/IDLE
U 0F2, 0F02,20 1770 NEXT/IDLE
U 0F3, 0F02,20 1771 NEXT/IDLE
U 0F4, 0F02,20 1772 NEXT/IDLE
U 0F5, 0F02,20 1773 NEXT/IDLE
U 0F6, 0F02,20 1774 NEXT/IDLE
U 0F7, 0F02,20 1775 NEXT/IDLE
U 0F8, 0F02,20 1776 NEXT/IDLE
U 0F9, 0F02,20 1777 NEXT/IDLE
U 0FA, 0F02,20 1778 NEXT/IDLE
U 0FB, 0F02,20 1779 NEXT/IDLE
U 0FC, 0F02,20 1780 NEXT/IDLE
U 0FD, 0F02,20 1781 NEXT/IDLE
U 0FE, 0F02,20 1782 NEXT/IDLE
U 0FF, 0F02,20 1783 NEXT/IDLE
1784

U 000	143	148	153	158	163	168	173	177
U 008	186	192	196	200	204	208	212	216
U 010	236	241	221	226	267	272	245	229
U 018	318	322	325	330	335	339	342	346
U 020	373	377	380	385	390	394	397	401
U 020	260	276	252	255	428	432	435	440
U 030	480	484	487	491	496	500	503	506
U 038	291		283	286	536	540	543	546
U 040	589	593	596	600	604	608	611	616
U 040	300	303	308	312		645		648
U 050	355	358	363	367	409	412	417	422
U 058			445	451	460		468	472
U 060	515	518	523	527				531
U 068			550	554			558	561
U 070	570	573	578	582			625	628
U 078			635	639				
U 080	653	654	655	656	657	658	659	660
U 088	661	662	663	664	665	666	667	668
U 090	669	670	671	672	673	674	675	676
U 098	677	678	679	680	681	682	683	684
U 0A0	686	687	688	689	690	691	692	693
U 0A8	694	695	696	697	698	699	700	701
U 0B0	702	703	704	705	706	707	708	709
U 0B8	710	711	712	713	714	715	716	717
U 0C0	719	720	721	722	723	724	725	726
U 0C8	727	728	729	730	731	732	733	734
U 0D0	735	736	737	738	739	740	741	742
U 0D8	743	744	745	746	747	748	749	750
U 0E0	752	753	754	755	756	757	758	759
U 0E8	760	761	762	763	764	765	766	767
U 0F0	768	769	770	771	772	773	774	775
U 0F8	776	777	778	779	780	781	782	783

Memory No. Microwords	High Addr		
U	236	255	
Total number of microwords used:	236		
Highest address(decimal):	255		
Pass 1 warnings detected:	0	Pass 2 warnings detected:	0
Pass 1 errors detected:	0	Pass 2 errors detected:	0

DRAWING NO.	NO. OF SHTS.	PART NO.	DESCRIPTION	REVISIONS																				
				C	D	E	F	H																
			MODULE REVISION	C	D	E	F	H																
B-DD-L0005-0	1		CCS DRAWING DIRECTORY	C	D	E	F	H																
E-UA-L0005-0-0	2		CCS UNIT ASSEMBLY	C	C	C	C	D																
K-PL-L0005-0-DBP	4		CCS PARTS LIST	C	D	E	F	H																
E-MD-5013516-0-0	6		CCS DRILL & ETCH DRAWINGS	C	C	C	C	D																
		5013516	ETCHED BOARD	D	D	D	D	D																
K-PC-L0005-0-DBC			CCS PC DESIGN DATA BASE	D	D	D	D	E																
E-EC-5013516-0-0	2		CCS ETCH CUT DRAWINGS	C	C	C	C	C																
K-CS-L0005-0-DBS			CCS DESIGN DATA BASE SUDS	C	C	D	D	E																
D-CS-L0005-0-1	1	*	CONTROL STORE ADDRESS DECCDE	C	C	D	D	E																
D-CS-L0005-0-2	1	*	ADDRESS BUFFERS	C	C	D	C	C																
D-CS-L0005-0-3	1	*	ADDRESS BUFFERS	C	C	C	C	C																
D-CS-L0005-0-4	1	*	PROM ARRAY	C	C	D	C	C																
D-CS-L0005-0-5	1	*	PROM ARRAY	C	C	C	C	C																
D-CS-L0005-0-6	1	*	PROM ARRAY	C	C	C	C	C																
D-CS-L0005-0-7	1	*	PROM ARRAY	C	C	C	C	C																
D-CS-L0005-0-8	1	*	PROM ARRAY	C	C	C	C	C																
D-CS-L0005-0-9	1	*	PROM ARRAY	C	C	C	C	C																
D-CS-L0005-0-10	1	*	PROM ARRAY	C	C	C	C	C																
D-CS-L0005-0-11	1	*	PROM ARRAY	C	C	C	C	C																
D-CS-L0005-0-12	1	*	PROM ARRAY	C	C	C	C	C																
D-CS-L0005-0-13	1	*	PROM ARRAY	C	C	C	C	C																
D-CS-L0005-0-14	1	*	IMC'S NAD OSCILLATORS	C	C	C	C	C																
D-CS-L0005-0-15	1	*	FORWARD REFERENCE	C	C	C	C	C																
D-CS-L0005-0-16	1	*	FORWARD REFERENCE	C	C	C	C	C																
K-MC-11750-0-0			11/750 MICROCODE	A	B	C	D	E																
NOTES: *CONTROL SOURCE IS THE SUDS DATA BASE NO CONTROLLED PAPER ORIGINALS EXIST ALL DOCUMENTATION RELEASED AT REVISION "C"				REVISIONS CHG NO. DATE		D	E	F	H															
						TW001A	TW002	TW003	TW004															
						9-80	2-81	6-81	3-82															
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								11/750								CHK'D J. CASEY								
																ENG. S. SMITH								
																PROD. J. CONSIDINE								

REVISIONS

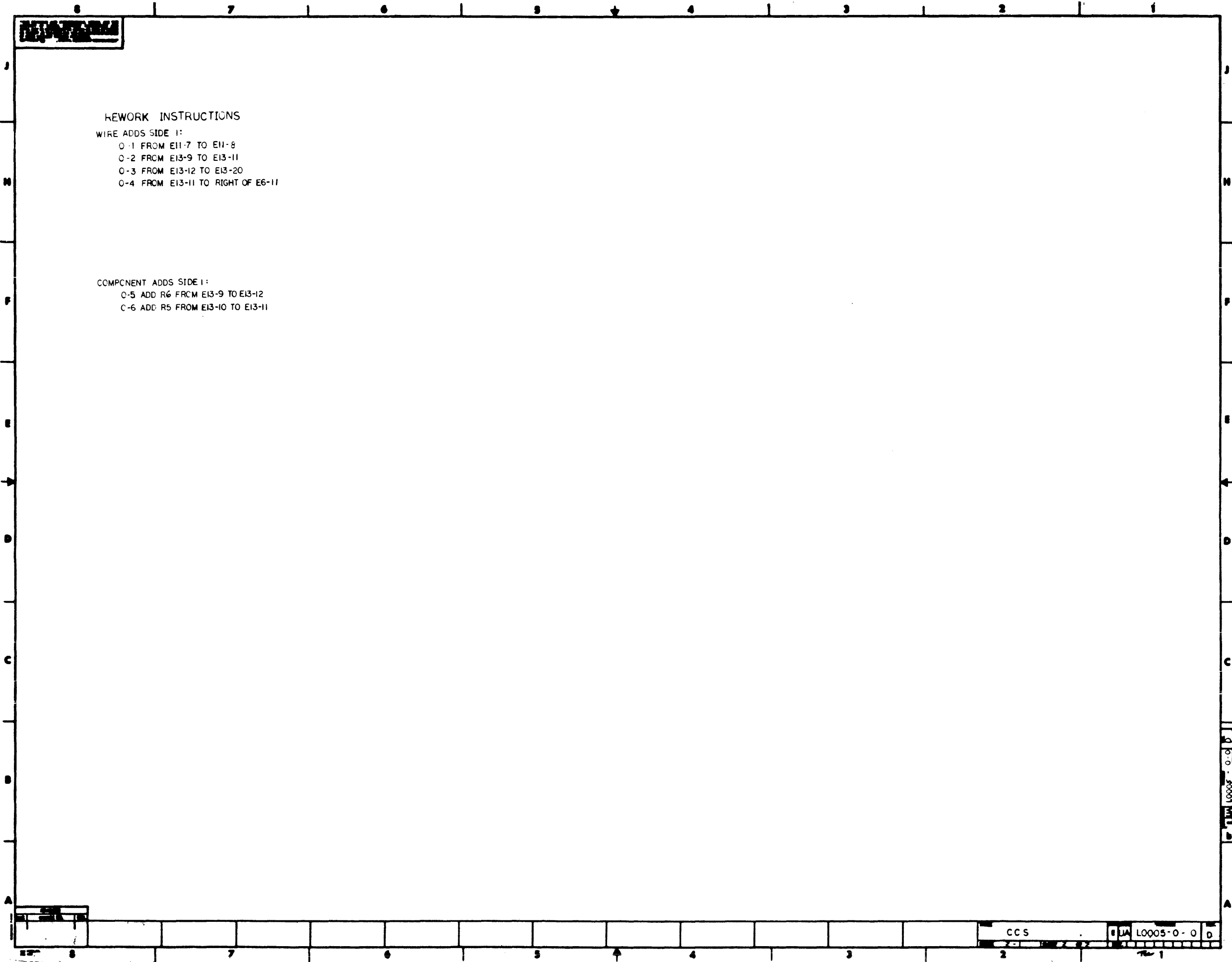
REWORK INSTRUCTIONS

WIRE ADDS SIDE 1:

- O-1 FROM E11-7 TO E11-8
- O-2 FROM E13-9 TO E13-11
- O-3 FROM E13-12 TO E13-20
- O-4 FROM E13-11 TO RIGHT OF E6-11

COMPONENT ADDS SIDE 1:

- C-5 ADD R6 FROM E13-9 TO E13-12
- C-6 ADD R5 FROM E13-10 TO E13-11



REVISIONS

CCS
LO005-0-0

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION 00	REFERENCE DESIGNATOR
1	1	E-MD-5013516-0-0	5013516-00	ETCH CIRCUIT BOARD C.S.	1
2	2	SEE NOTES	1012084-01	8 MFD 25V +75-10% AL EL	3
3	3		1012784-00	.047 MFD 50V +80-20% CER	7
4	4		1017897-00	.22 MFD 50V +80-20% CER	40
5	5		1216988-02	HANDLE,MODULE,HEX TWO EJECTORS	1
6	6		1215006-03	SKT,IC 18PIN DIP TIN PLATE	120
7	7		1300316-00	470.0 .25 W 5.0 % CC	3
8	8		1301424-00	680.0 .25 W 5.0 % CC	1
9	9		1910533-00	74S03 NAND GATE-QUAD 2IN,0	1
10	10		1910534-00	74S04 INVERTER GATE-HEX 1I	4
11	11		1910544-00	74S74 FF-D DUAL,EDGE TRIGG	1
12	12		1911573-00	74S280 PARITY GEN/CHKR,9BIT	1
13	13		1911675-00	74S138 DECODER/DEMUX 3-8 LIN	1
14	14		1912746-00	DEC 74S37 NAND GATE-QUAD 2IN	9
15	15		1913340-00	74S32 OR GATE-QUAD 2IN	1
16	16		1913462-00	74S240 OCTAL BUFFER,INVERTI	1
17	17		1913671-00	74S374 FF-D OCTAL TRISTATE	1
18	18		1217293-00	PIN,SQUARE ON NYLON STRIP	160
19	19		23821F1-00	F1-01	1
20	20		23822F1-00	F1-01	1
21	21		23823F1-00	F1-01	1
22	22		23824F1-00	F1-01	1
23	23		23825F1-00	F1-01	1
24	24		23826F1-00	F1-01	1
25	25		23827F1-00	F1-01	1
26	26		23828F1-00	F1-01	1
27	27		23829F1-00	F1-01	1
28	28		23830F1-00	F1-01	1
29	29		23831F1-00	F1-01	1
30	30		23832F1-00	F1-01	1

REVISION HISTORY		BASIC PART NO:	L0005	IRN:	K.FRIEDGEN	DATE:	17-MAY-79	DIGITAL			
ENG	ECO NUMBER	REV	SECTION A OF A	CHK'D:	E.T.GERRY	DATE:	17-MAY-79	TITLE PARTS LIST			
---	INITIAL	C	SECTION VARIATION INDEX	CHK'D:	E.T.GERRY	DATE:	17-MAY-79	TITLE PARTS LIST			
S	!TW001	D	[A] 00					CCS			
D	!TW002	E	[B]								
G	!L0005-TW003	F	[C]	DES.ENG:	S.SMITH	DATE:	17-MAY-79				
			[D]								
			[E]					DOCUMENT NUMBER			
			[F]	RESP.ENG.:	S.SMITH	DATE:	17-MAY-79	SIZE	CODE	NUMBER	REV
			[G]								
			[H]								
			[I]								
			[J]								
			[K]	MFG.ENG.:	K.O'BRIEN	DATE:	8-FEB-80	K	PL	L0005-0-DBP	F
			[L]								
			[M]	ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:		FILE NAME:		EDIT #	
			[N]	!E-UA-L0005-0-0		!#B-DD-L0005-0		! Z1260.PLS		! 29	

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LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION 00	REFERENCE DESIGNATOR
31	31		23833F1-00	F1-01	1	E39
32	32		23834F1-00	F1-01	1	E40
33	33		23835F1-00	F1-01	1	E41
34	34		23836F1-00	F1-01	1	E42
35	35		23837F1-00	F1-01	1	E43
36	36		23838F1-00	F1-01	1	E44
37	37		23839F1-00	F1-01	1	E45
38	38		23840F1-00	F1-01	1	E46
39	39		23841F1-00	F1-01	1	E47
40	40		23842F1-00	F1-01	1	E48
41	41		23718F1-00	F1-01	1	E49
42	42		23843F1-00	F1-01	1	E50
43	43		23504F1-00	F1-01	1	E51
44	44		23505F1-00	F1-01	1	E52
45	45		23506F1-00	F1-01	1	E53
46	46		23507F1-00	F1-01	1	E54
47	47		23719F1-00	F1-01	1	E55
48	48		23844F1-00	F1-01	1	E56
49	49		23845F1-00	F1-01	1	E57
50	50		23511F1-00	F1-01	1	E58
51	51		23720F1-00	F1-01	1	E59
52	52		23846F1-00	F1-01	1	E60
53	53		23721F1-00	F1-01	1	E61
54	54		23847F1-00	F1-01	1	E62
55	55		23848F1-00	F1-01	1	E63
56	56		23849F1-00	F1-01	1	E64
57	57		23786F1-00	F1-01	1	E65
58	58		23850F1-00	F1-01	1	E66
59	59		23724F1-00	F1-01	1	E67
60	60		23851F1-00	F1-01	1	E68
61	61		23852F1-00	F1-01	1	E69
62	62		23778F1-00	F1-01	1	E70
63	63		23726F1-00	F1-01	1	E71
64	64		23853F1-00	F1-01	1	E72
65	65		23727F1-00	F1-01	1	E73
66	66		23854F1-00	F1-01	1	E74
67	67		23855F1-00	F1-01	1	E75
68	68		23779F1-00	F1-01	1	E76
69	69		23856F1-00	F1-01	1	E77
70	70		23857F1-00	F1-01	1	E78
71	71		23732F1-00	F1-01	1	E79
72	72		23858F1-00	F1-01	1	E80
73	73		23859F1-00	F1-01	1	E81
74	74		23535F1-00	F1-01	1	E82
75	75		23733F1-00	F1-01	1	E83
76	76		23860F1-00	F1-01	1	E84
77	77		23734F1-00	F1-01	1	E85
78	78		23861F1-00	F1-01	1	E86

I	D	I	G	I	T	A	L	TITLE	SECTION A	OF A	SIZE	CODE	DOCUMENT NUMBER	REV
								CCS			K	PL	L0005-0-DRP	F

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION 00	REFERENCE DESIGNATOR
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80	80	23863F1-00	F1-01	1	E88
81	81	23864F1-00	F1-01	1	E89
82	82	23865F1-00	F1-01	1	E90
83	83	23866F1-00	F1-01	1	E91
84	84	23867F1-00	F1-01	1	E92
85	85	23868F1-00	F1-01	1	E93
86	86	23869F1-00	F1-01	1	E94
87	87	23740F1-00	F1-01	1	E95
88	88	23870F1-00	F1-01	1	E96
89	89	23741F1-00	F1-01	1	E97
90	90	23871F1-00	F1-01	1	E98
91	91	23872F1-00	F1-01	1	E99
92	92	23553F1-00	F1-01	1	E100
93	93	23743F1-00	F1-01	1	E101
94	94	23873F1-00	F1-01	1	E102
95	95	23744F1-00	F1-01	1	E103
96	96	23874F1-00	F1-01	1	E104
97	97	23875F1-00	F1-01	1	E105
98	98	23559F1-00	F1-01	1	E106
99	99	23745F1-00	F1-01	1	E107
100	100	23876F1-00	F1-01	1	E108
101	101	23746F1-00	F1-01	1	E109
102	102	23877F1-00	F1-01	1	E110
103	103	23878F1-00	F1-01	1	E111
104	104	23565F1-00	F1-01	1	E112
105	105	23747F1-00	F1-01	1	E113
106	106	23879F1-00	F1-01	1	E114
107	107	23748F1-00	F1-01	1	E115
108	108	23880F1-00	F1-01	1	E116
109	109	23881F1-00	F1-01	1	E117
110	110	23571F1-00	F1-01	1	E118
111	111	23882F1-00	F1-01	1	E119
112	112	23883F1-00	F1-01	1	E120
113	113	23750F1-00	F1-01	1	E121
114	114	23884F1-00	F1-01	1	E122
115	115	23885F1-00	F1-01	1	E123
116	116	23577F1-00	F1-01	1	E124
117	117	23752F1-00	F1-01	1	E125
118	118	23886F1-00	F1-01	1	E126
119	119	23753F1-00	F1-01	1	E127
120	120	23887F1-00	F1-01	1	E128
121	121	23888F1-00	F1-01	1	E129
122	122	23754F1-00	F1-01	1	E130
123	123	23755F1-00	F1-01	1	E131
124	124	23889F1-00	F1-01	1	E132
125	125	23757F1-00	F1-01	1	E133
126	126	23890F1-00	F1-01	1	E134

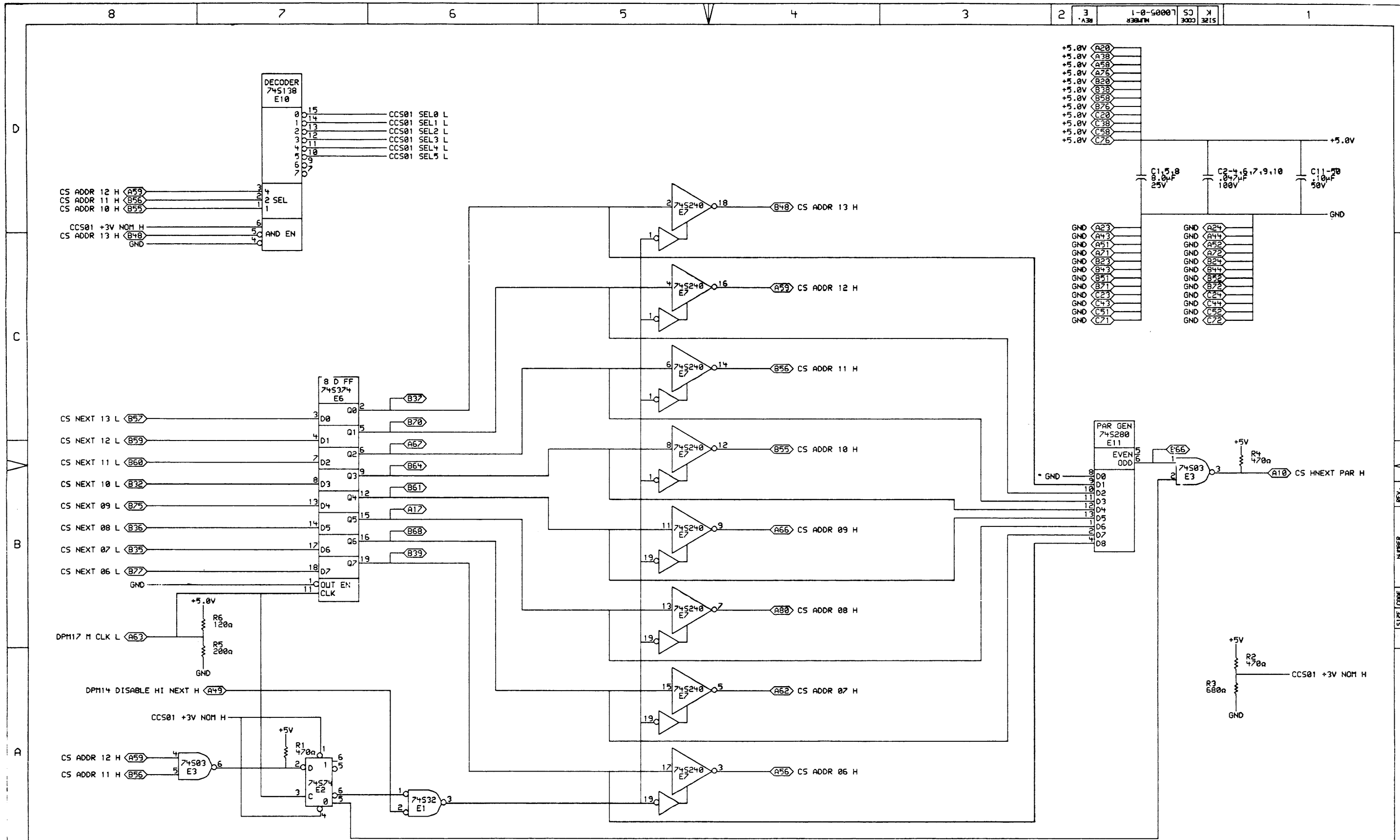
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! ! ! ! ! ! ! !	CCS	! ! ! ! ! ! ! !	! K ! PL !	L0005-0-DBP	! F !

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION 00	REFERENCE DESIGNATOR
127	127	23891F1-00	F1-01	1	E135
128	128	23892F1-00	F101	1	E136
129	129	23760F1-00	F1-01	1	E137
130	130	23893F1-00	F1-01	1	E138
131	131	23762F1-00	F1-01	1	E139
132	132	23894F1-00	F1-01	1	E140
133	133	23895F1-00	F1-01	1	E141
134	134	23896F1-00	F1-01	1	E142
135	135	23897F1-00	F1-01	1	E143
136	136	23898F1-00	F1-01	1	E144
137	137	23899F1-00	F1-01	1	E145
138	138	23900F1-00	F1-01	1	E146
139	139	9000024-01	EYELET,ROLL FLANGE .1210DX .192	12	
140	140	1912830-00	LS90 COUNTER,ASYNCH UP,DE	1	E4
141	141	1811660-01	OSCILLATOR, XTAL 10.000 MHZ	1	E5
142	142	1811660-29	OSCILLATOR, XTAL 18.750 MHZ	1	E8
143	143	1300247-00	120.0 .25 W 5.0 % CC	1	R5
144	144	1311522-00	200.0 .25 W 5.0 % CC	1	R6
145	145	1214314-00	CONN 2POS JUMPER	1	

146 NOTE: ITEM #18 IS USED ON J1-J4.

147 NOTE: SOME MODULES WILL HAVE 10-05306 INSTEAD OF 10-12084-01

D	I	G	I	T	A	L	TITLE	SECTION A OF A	SIZE	CODE	DOCUMENT NUMBER	REV
							CCS		K	PL	L0005-0-DRP	F

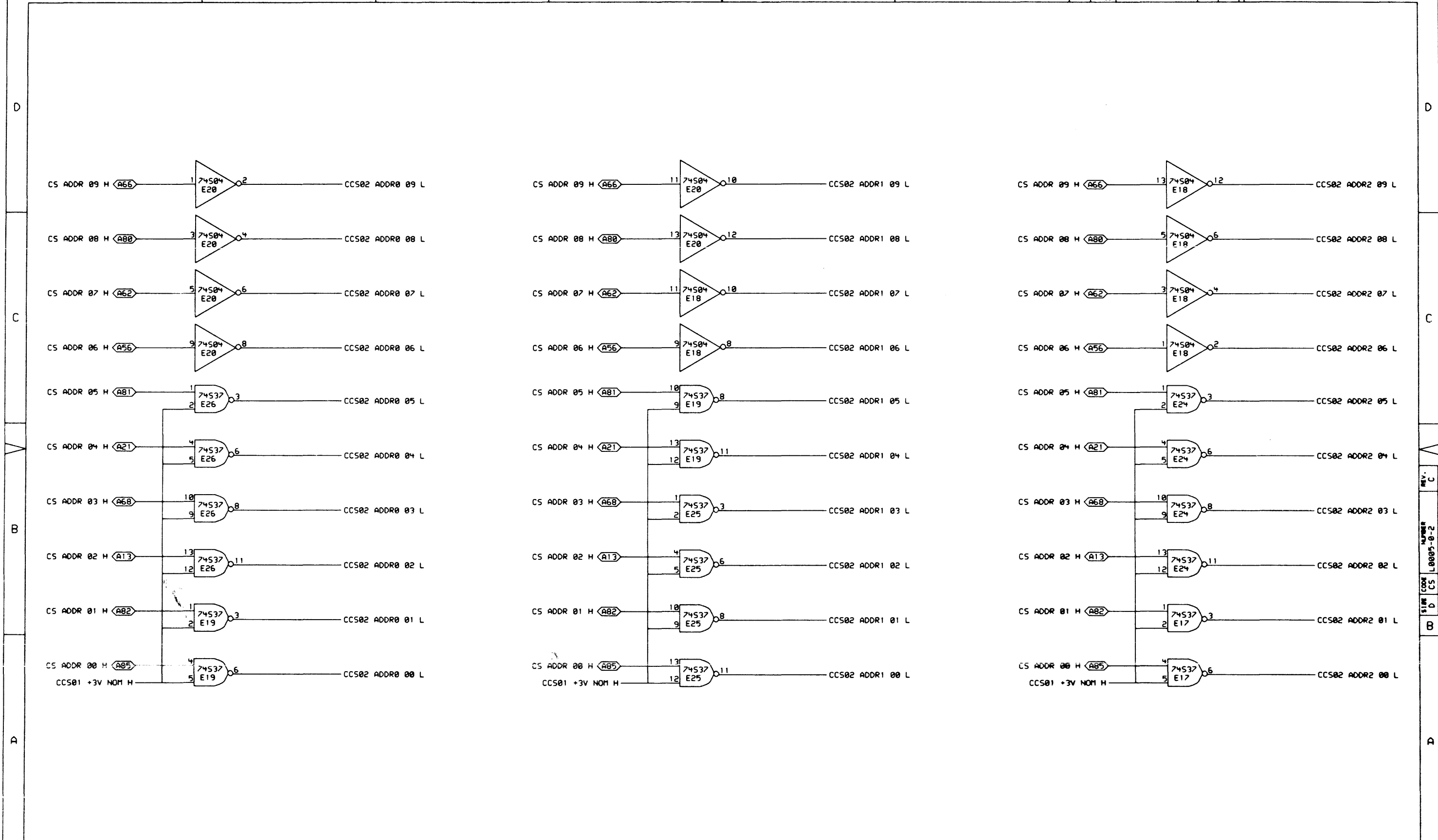


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REVISIONS	
CHK	CHANGE NO. REV

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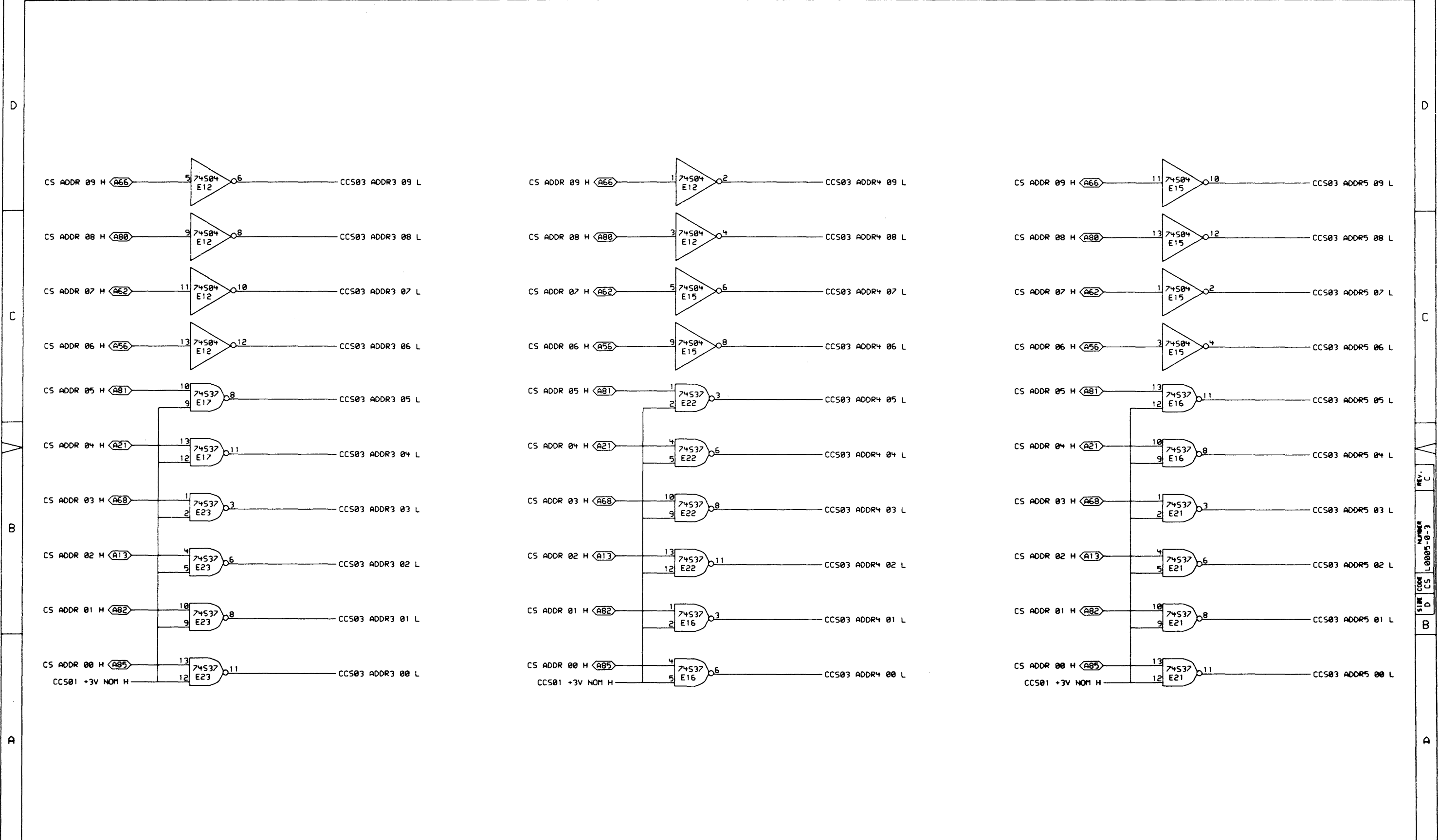
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	CHK'D.	12-NOV-82			BOARD LOCATION: AC5	SIZE	CODE	NUMBER	REV.
DSKB:[203,200]CCS01.DRW 11-NOV-82 14:59		NEXT HIGHER ASSEMBLY:		8-DD-L0005-0-0		K	CS	L0005-0-1	E
FIRST USED ON OPTION/MODEL: 11/750									



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REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN. R.C.H.	DATE	ENG.	DATE	TITLE:
	CHK'D.	DATE	BOARD LOCATION: ACS	OF	ADDRESS BUFFERS
[160,127]CCS02.DRW		17-FEB-88 17:00	NEXT HIGHER ASSEMBLY:	SIZE CODE	NUMBER
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					REV. C

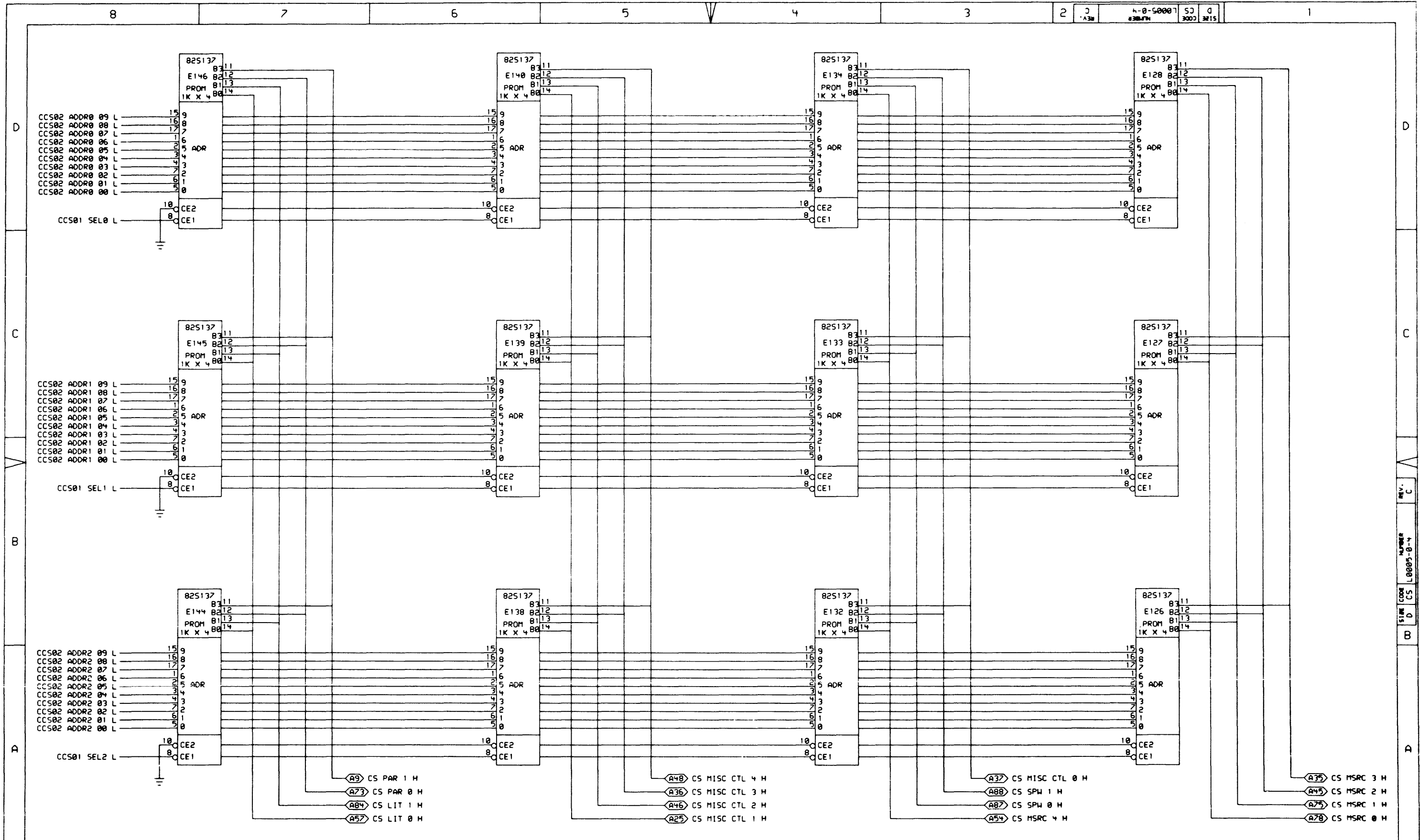


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REVISIONS	
CHK	CHANGE NO. REV

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FIRST USED ON OPTION/MODEL:	11/750	B-DD-L0005-0-0

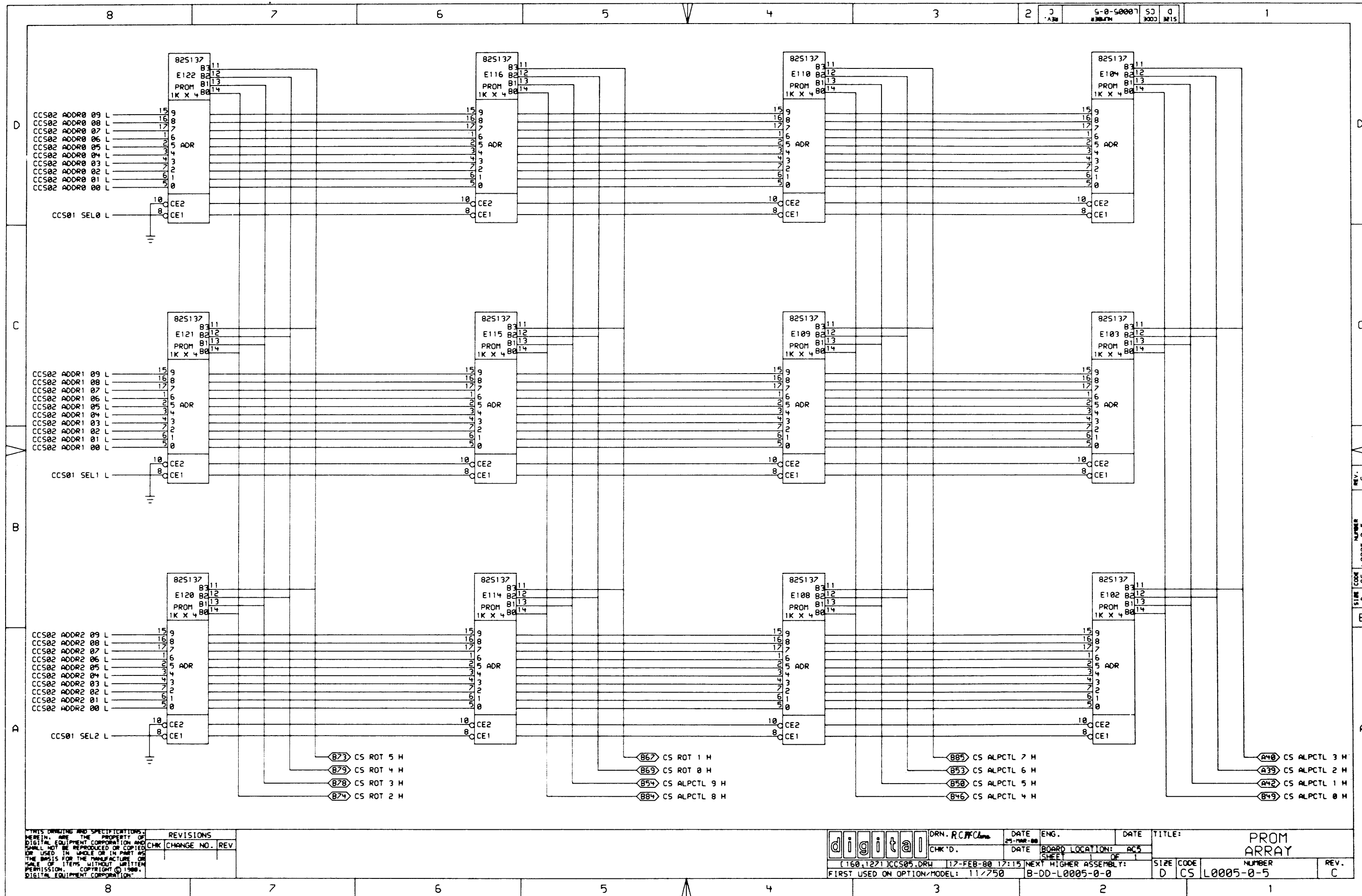
digital	DRN. R.C.F.C.	DATE	ENG.	DATE	TITLE:
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ADDRESS BUFFERS			SIZE CODE	NUMBER	REV.
D CS L0005-0-3			D CS L0005-0-3	C	



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REVISIONS		
CHK	CHANGE NO.	REV

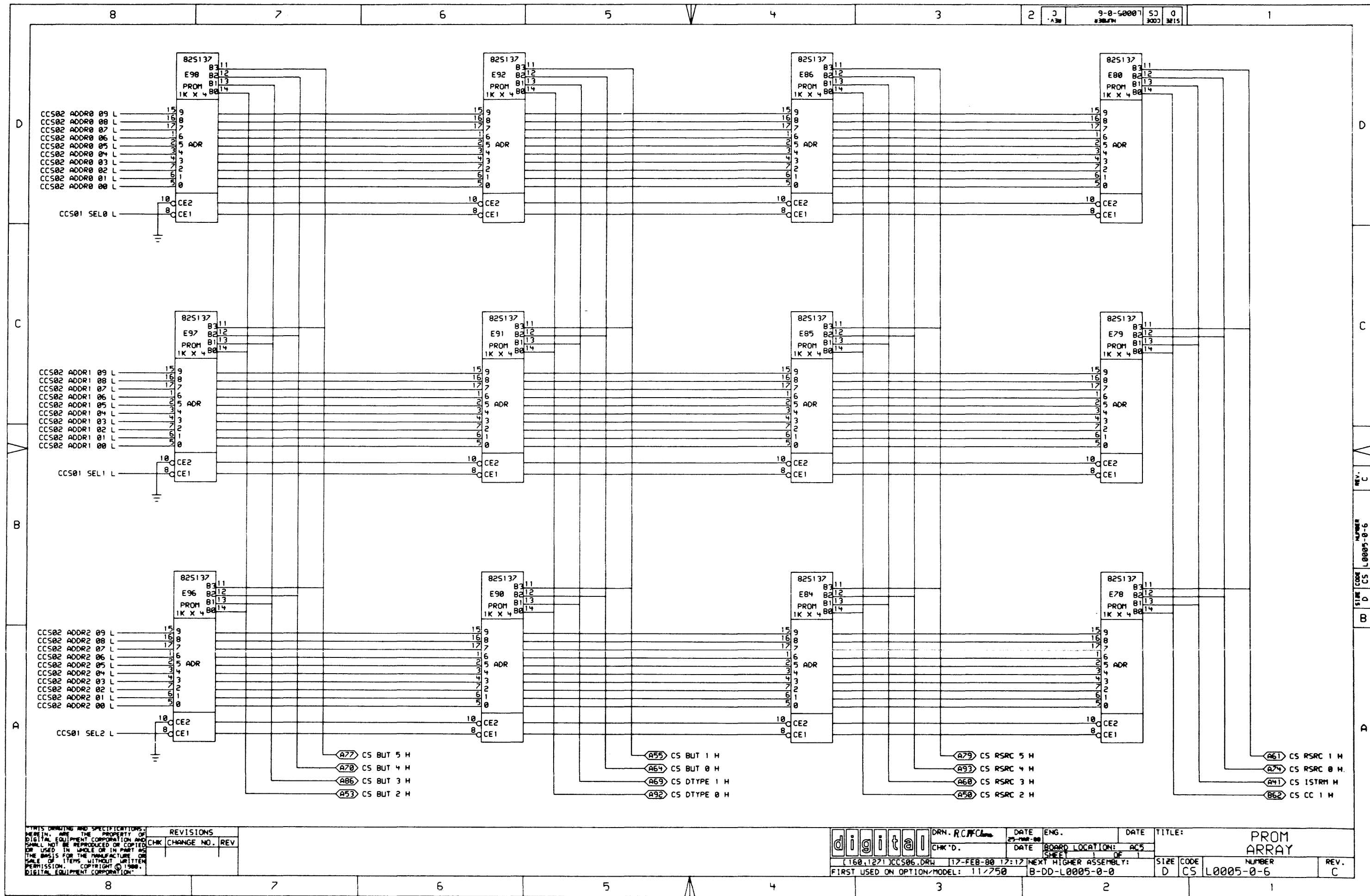
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	CHK'D.	DATE: 17-FEB-88 12:13	BOARD LOCATION: AC5	SHEET 1 OF 1	SIZE CODE: D CS
FIRST USED ON OPTION/MODEL: 11/250		NEXT HIGHER ASSEMBLY: B-DD-L0005-0-0		NUMBER: L0005-0-4	
REV. C					



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REVISIONS	
CHK	CHANGE NO. REV

digital	DRN. R.C.F.C.	DATE ENG.	DATE	TITLE:
	CHK'D.	DATE	BOARD LOCATION: AC5	PROM ARRAY
[160,127] CCS05.DRW		12-FEB-80 12:15	NEXT HIGHER ASSEMBLY:	SIZE CODE NUMBER
FIRST USED ON OPTION/MODEL: 11/750		B-DD-L0005-0-0	D CS	L0005-0-5
				REV. C

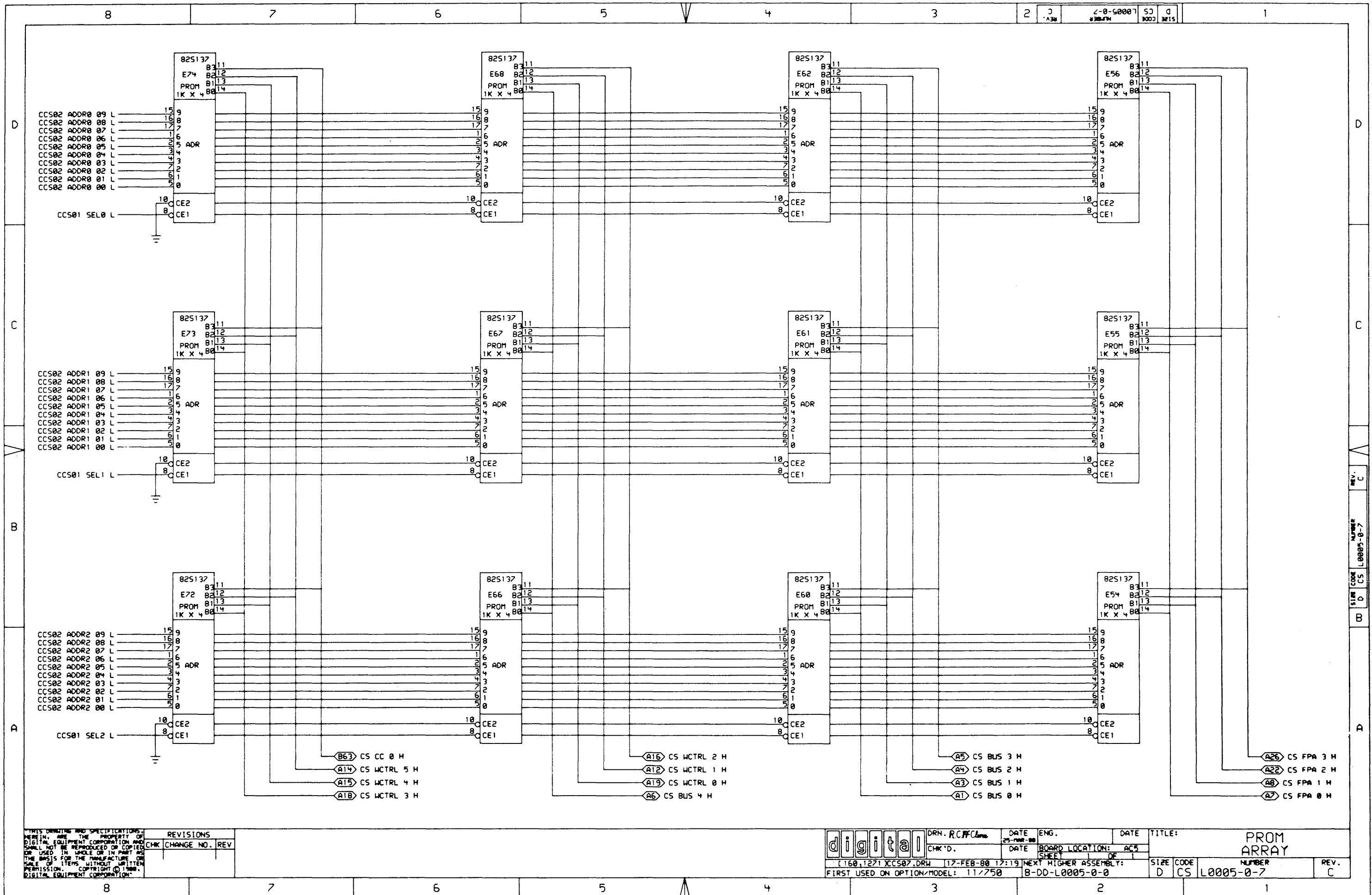


- (A77) CS BUT 5 H
- (A78) CS BUT 4 H
- (A86) CS BUT 3 H
- (A53) CS BUT 2 H
- (A55) CS BUT 1 H
- (A64) CS BUT 0 H
- (A69) CS DTYPE 1 H
- (A92) CS DTYPE 0 H
- (A79) CS RSRC 5 H
- (A93) CS RSRC 4 H
- (A68) CS RSRC 3 H
- (A58) CS RSRC 2 H
- (A61) CS RSRC 1 H
- (A74) CS RSRC 0 H
- (A41) CS ISTRM H
- (B62) CS CC 1 H

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REVISIONS		
CHK	CHANGE NO.	REV

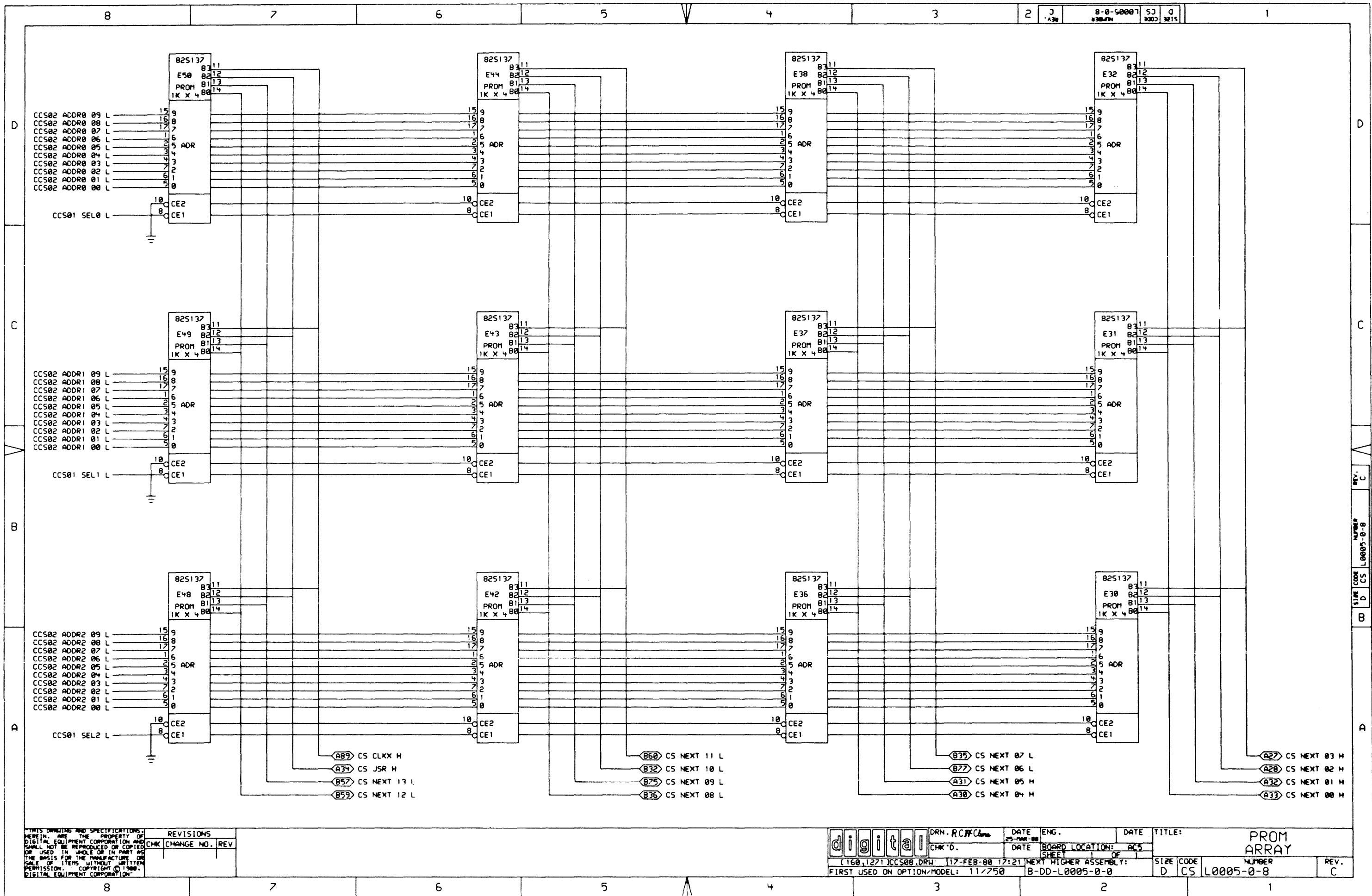
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	CHK'D.	DATE	BOARD LOCATION: AC5	SHEET 1 OF 1	PROM ARRAY
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FIRST USED ON OPTION/MODEL: 11/750		B-DD-L0005-0-0		D CS	L0005-0-6
REV. C					REV. C



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REVISIONS		
CHK	CHANGE NO.	REV

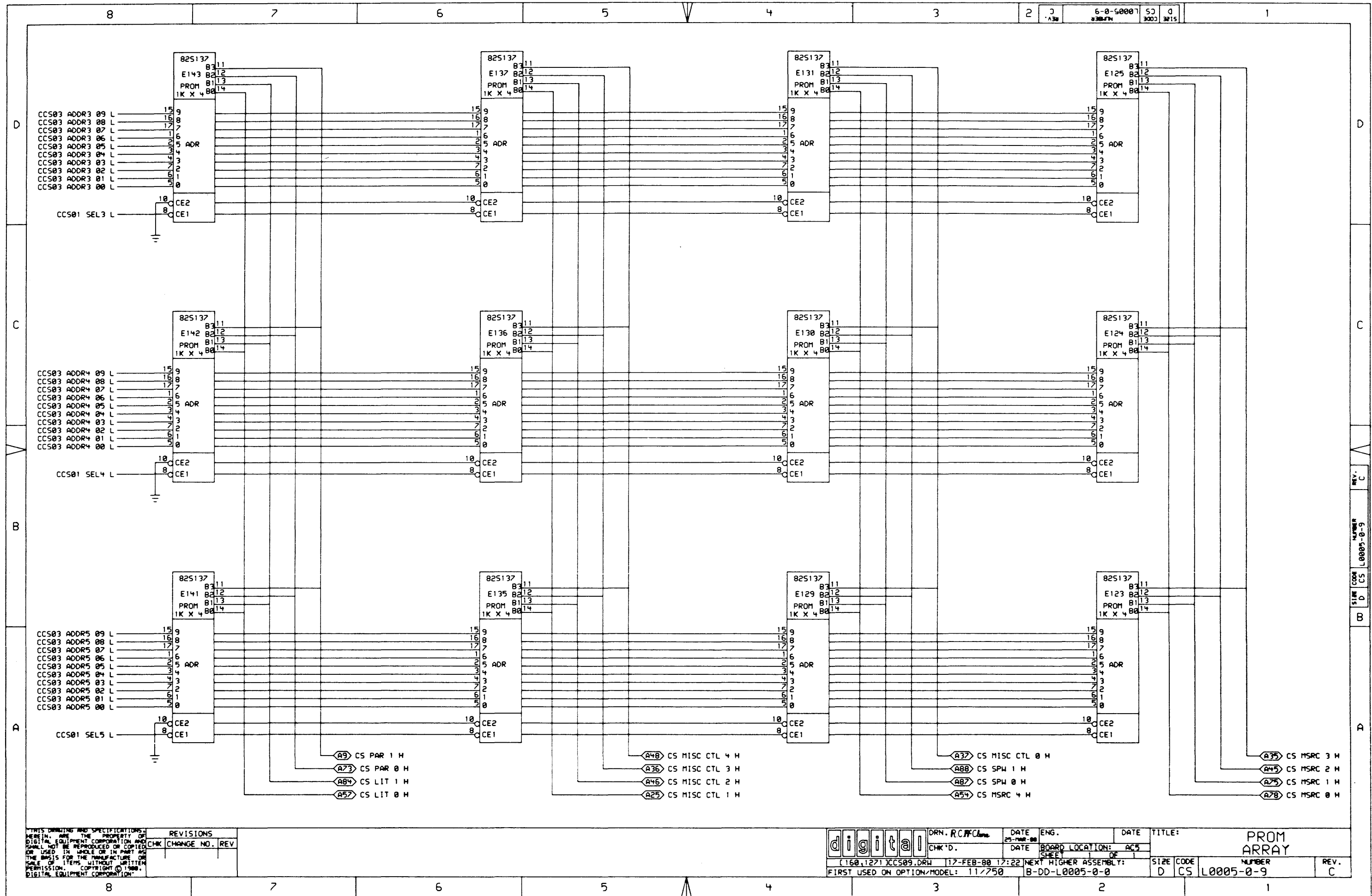
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FIRST USED ON OPTION/MODEL: 11/750		NEXT HIGHER ASSEMBLY: B-DD-L0005-0-0		BOARD LOCATION: ACS	
				SHEET	
				SIZE CODE	NUMBER
				D CS	L0005-0-7
				REV.	C



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REVISIONS	
CHK	CHANGE NO. REV.

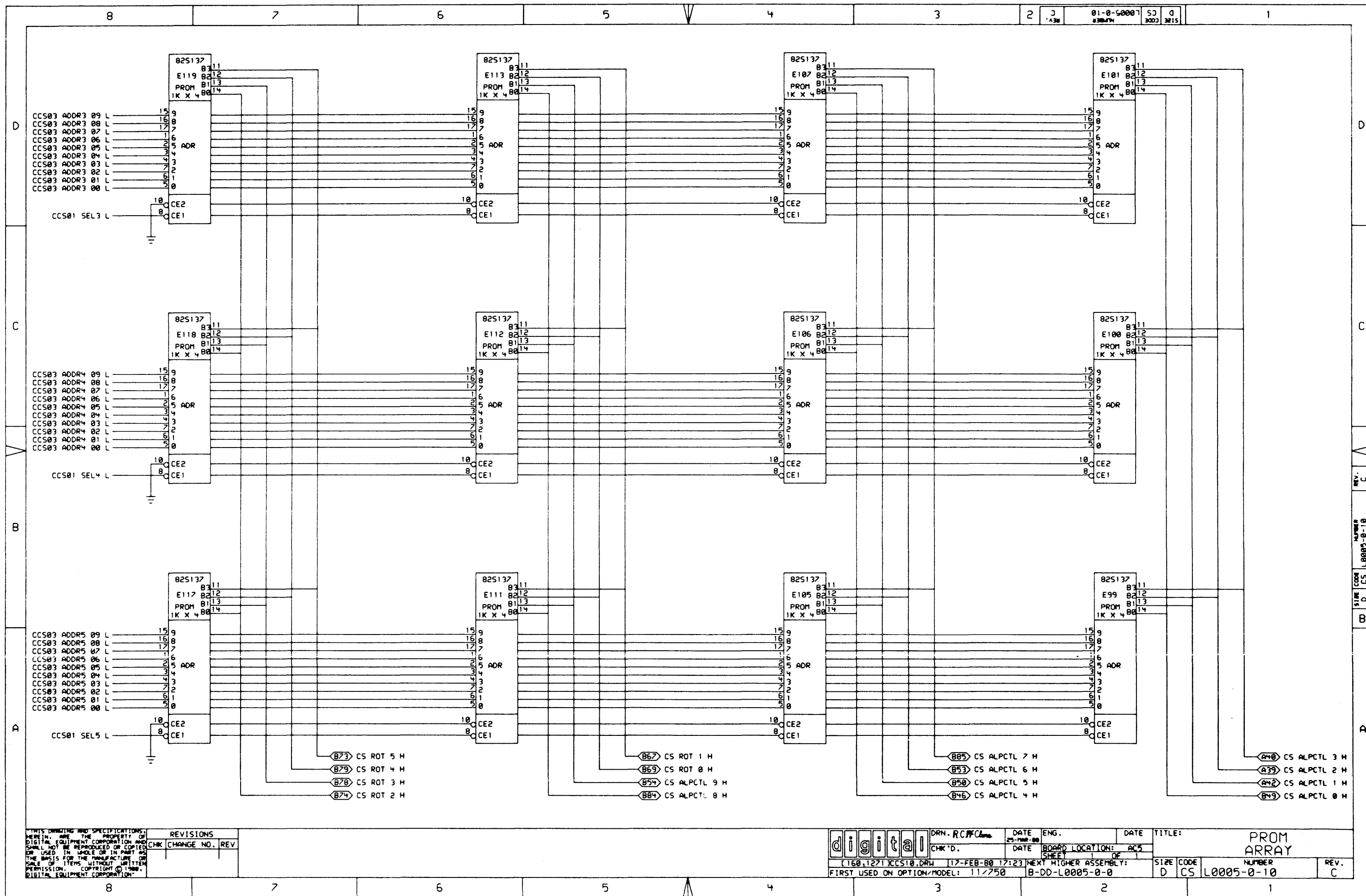
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FIRST USED ON OPTION/MODEL: 11/750		B-DD-L005-0-0		D CS	NUMBER
				L005-0-8	REV. C



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REVISIONS	
CHK	CHANGE NO. REV

digital	DRN. R.C.F.C.	DATE ENG.	DATE	TITLE:
	CHK'D.	25-MAR-80		PROM ARRAY
[160,127] CCS09.DRW		17-FEB-80 17:22	NEXT HIGHER ASSEMBLY:	SIZE CODE
FIRST USED ON OPTION/MODEL: 11/750		B-DD-L0005-0-0		NUMBER
				REV. C

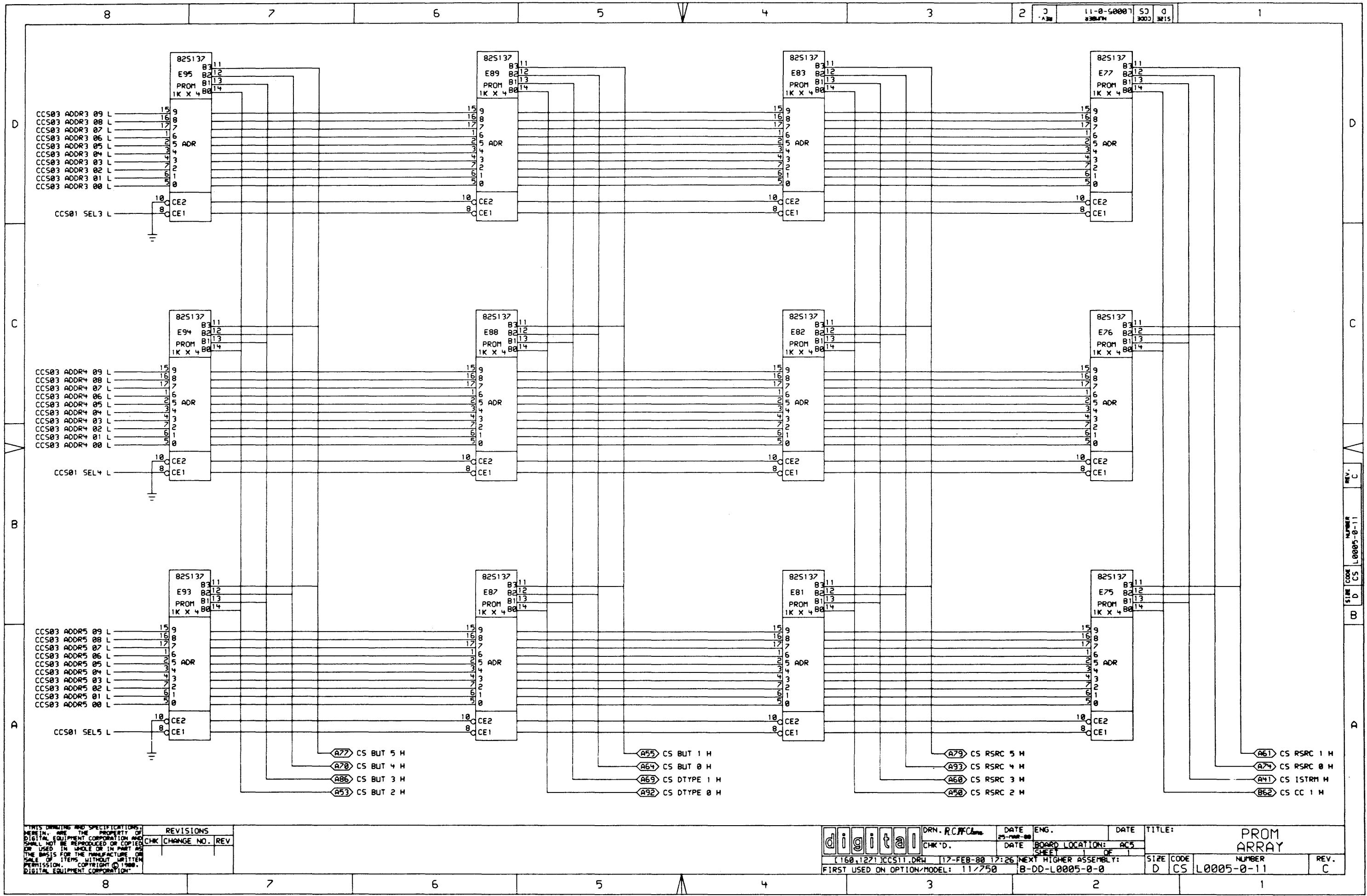


- (B73) CS ROT 5 H
- (B79) CS ROT 4 H
- (B78) CS ROT 3 H
- (B74) CS ROT 2 H
- (B67) CS ROT 1 H
- (B69) CS ROT 0 H
- (B54) CS ALPCTL 9 H
- (B84) CS ALPCTL 8 H
- (B85) CS ALPCTL 7 H
- (B53) CS ALPCTL 6 H
- (B50) CS ALPCTL 5 H
- (B46) CS ALPCTL 4 H
- (A40) CS ALPCTL 3 H
- (A39) CS ALPCTL 2 H
- (A42) CS ALPCTL 1 H
- (B49) CS ALPCTL 0 H

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REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN. R.C.M.C.	DATE 23-MAR-80	ENG.	DATE	TITLE: PROM ARRAY
	CHK'D.	DATE	BOARD LOCATION: AC5	OF	
FIRST USED ON OPTION/MODEL: 11/750			NEXT HIGHER ASSEMBLY: B-DD-L0005-0-0		
SIZE CODE	D	CS	L0005-0-10	NUMBER	REV. C



A77 CS BUT 5 H
 A78 CS BUT 4 H
 A86 CS BUT 3 H
 A53 CS BUT 2 H

A55 CS BUT 1 H
 A64 CS BUT 0 H
 A69 CS DTYPE 1 H
 A92 CS DTYPE 0 H

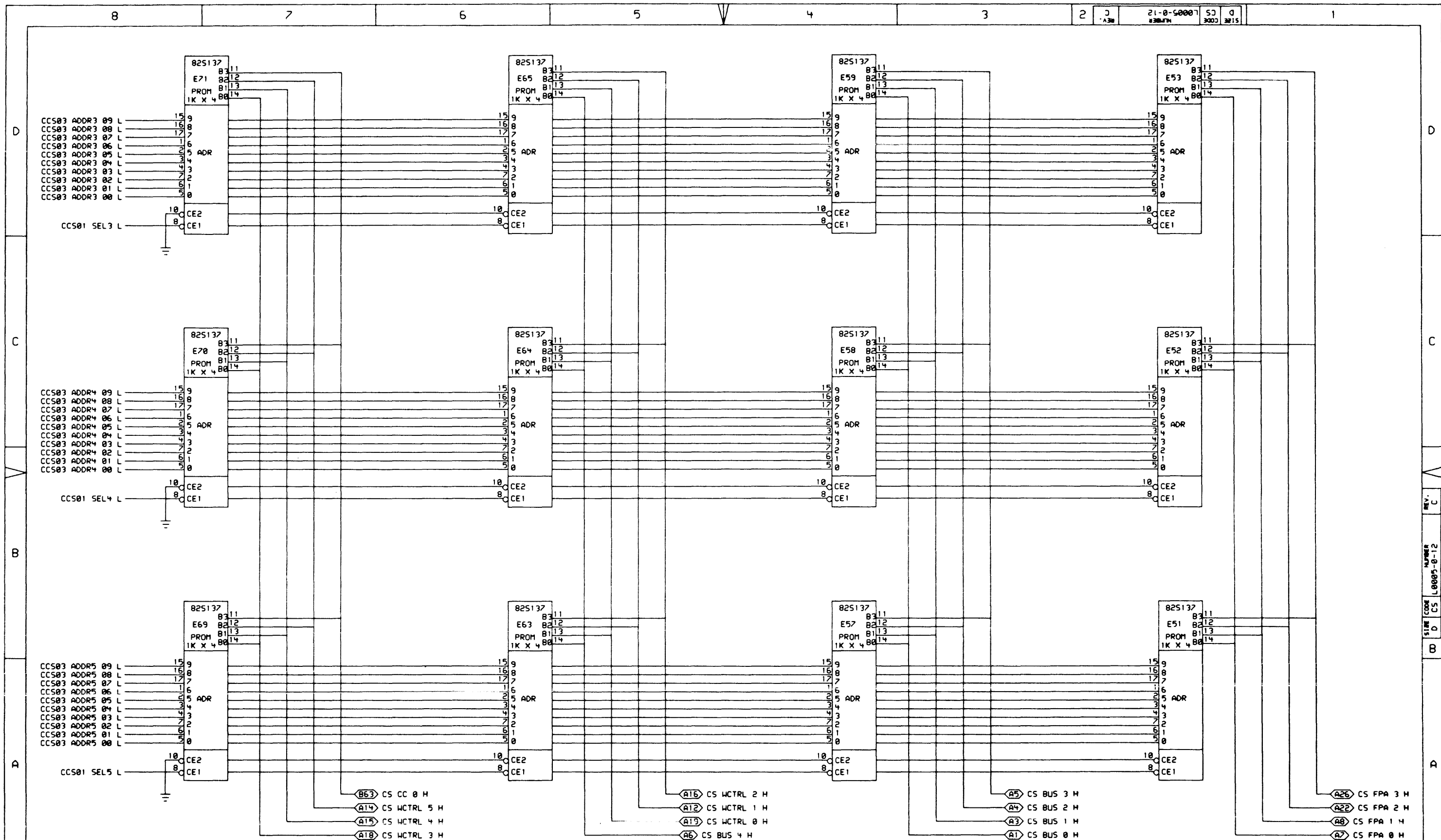
A79 CS RSRC 5 H
 A93 CS RSRC 4 H
 A60 CS RSRC 3 H
 A50 CS RSRC 2 H

A61 CS RSRC 1 H
 A74 CS RSRC 0 H
 A41 CS ISTRM H
 B62 CS CC 1 H

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REVISIONS		
CHK	CHANGE NO.	REV

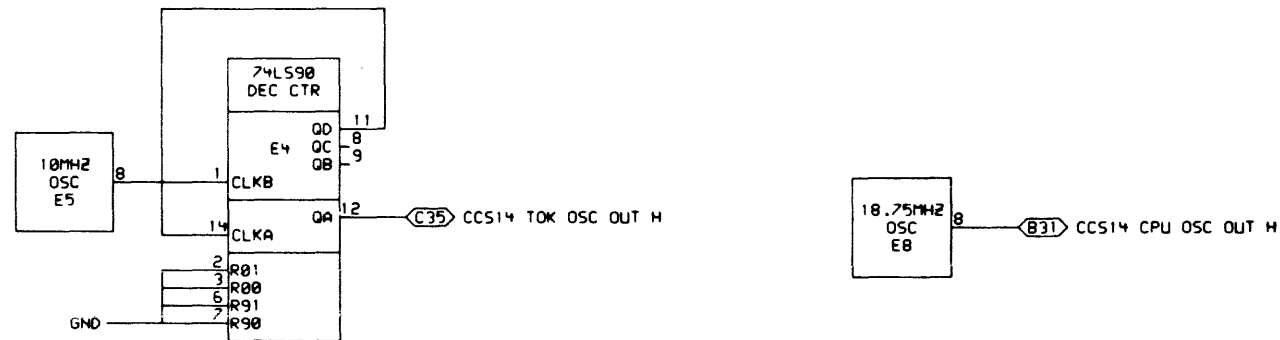
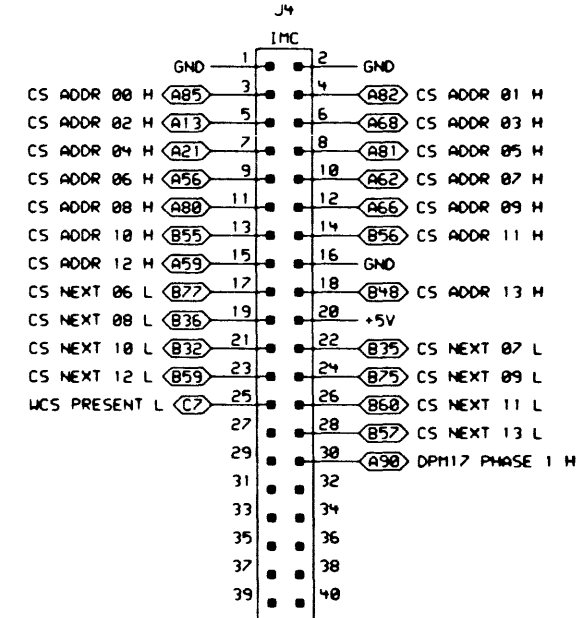
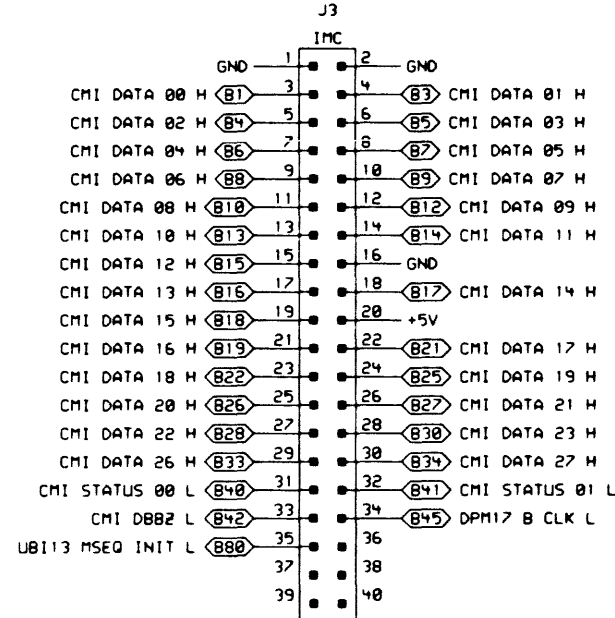
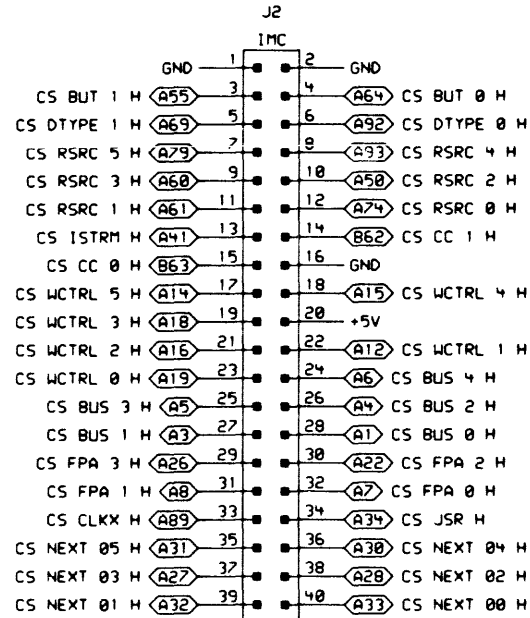
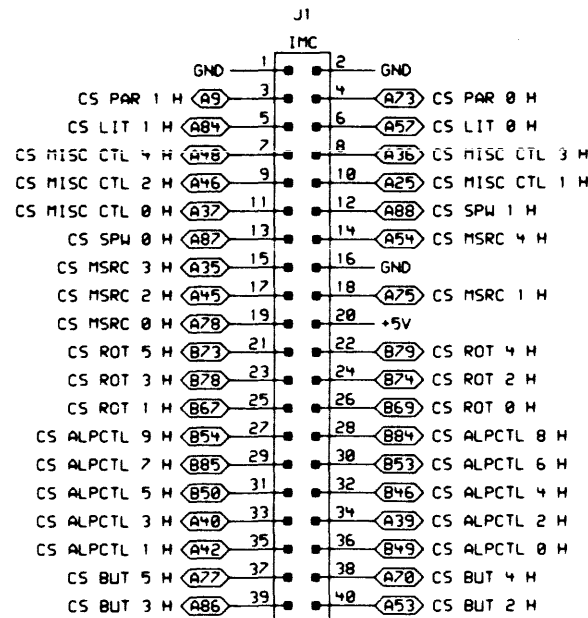
	DRN. R.C.F.C.	DATE ENG.	DATE	TITLE:
	CHK'D.	DATE	BOARD LOCATION: AC5	PROM ARRAY
[160,127] CCS11.DRW		17-FEB-80 17:26	NEXT HIGHER ASSEMBLY:	SIZE CODE
FIRST USED ON OPTION/MODEL: 11/750		B-DD-L0005-0-0	D CS	NUMBER
				L0005-0-11
				REV. C



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REVISIONS	
CHK	CHANGE NO. REV

digital	DRN. R.C.F.C.	DATE	ENG.	DATE	TITLE:
	CHK'D.	17-FEB-80			PROM ARRAY
[160.1271]JCCS12.DRW		17-FEB-80 17:27	NEXT HIGHER ASSEMBLY:	SIZE CODE	NUMBER
FIRST USED ON OPTION/MODEL: 11/750			B-DD-L0005-0-0	D CS	L0005-0-12
				REV.	C



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REV.	CHG.	NO.	REV.

digital	DRN. R.C.F.C.	DATE	ENG.	DATE	TITLE:
	CHK'D.	DATE	BOARD LOCATION: ACS		WCS CONNECTORS SYS OSCILLATORS
[160,1271]CCS14.DRW		17-FEB-80 17:32	NEXT HIGHER ASSEMBLY:	SIZE CODE	NUMBER
FIRST USED ON OPTION/MODEL: 11/750		B-DD-L0005-0-0		D CS	L0005-0-14
					REV. C

SIGNAL NAME	PAGE NUMBER(S)
CCS01 +3V NOM H	01,03,02
CCS01 SEL0 L	01,08,07,06,05,04
CCS01 SEL1 L	01,08,07,06,05,04
CCS01 SEL2 L	01,08,07,06,05,04
CCS01 SEL3 L	01,13,12,11,10,09
CCS01 SEL4 L	01,13,12,11,10,09
CCS01 SEL5 L	01,13,12,11,10,09
CCS02 ADDR0 00 L	02,08,07,06,05,04
CCS02 ADDR0 01 L	02,08,07,06,05,04
CCS02 ADDR0 02 L	02,08,07,06,05,04
CCS02 ADDR0 03 L	02,08,07,06,05,04
CCS02 ADDR0 04 L	02,08,07,06,05,04
CCS02 ADDR0 05 L	02,08,07,06,05,04
CCS02 ADDR0 06 L	02,08,07,06,05,04
CCS02 ADDR0 07 L	02,08,07,06,05,04
CCS02 ADDR0 08 L	02,08,07,06,05,04
CCS02 ADDR0 09 L	02,08,07,06,05,04
CCS02 ADDR1 00 L	02,08,07,06,05,04
CCS02 ADDR1 01 L	02,08,07,06,05,04
CCS02 ADDR1 02 L	02,08,07,06,05,04
CCS02 ADDR1 03 L	02,08,07,06,05,04
CCS02 ADDR1 04 L	02,08,07,06,05,04
CCS02 ADDR1 05 L	02,08,07,06,05,04
CCS02 ADDR1 06 L	02,08,07,06,05,04
CCS02 ADDR1 07 L	02,08,07,06,05,04
CCS02 ADDR1 08 L	02,08,07,06,05,04
CCS02 ADDR1 09 L	02,08,07,06,05,04
CCS02 ADDR2 00 L	02,08,07,06,05,04
CCS02 ADDR2 01 L	02,08,07,06,05,04
CCS02 ADDR2 02 L	02,08,07,06,05,04
CCS02 ADDR2 03 L	02,08,07,06,05,04
CCS02 ADDR2 04 L	02,08,07,06,05,04
CCS02 ADDR2 05 L	02,08,07,06,05,04
CCS02 ADDR2 06 L	02,08,07,06,05,04
CCS02 ADDR2 07 L	02,08,07,06,05,04
CCS02 ADDR2 08 L	02,08,07,06,05,04
CCS02 ADDR2 09 L	02,08,07,06,05,04
CCS03 ADDR3 00 L	03,13,12,11,10,09
CCS03 ADDR3 01 L	03,13,12,11,10,09
CCS03 ADDR3 02 L	03,13,12,11,10,09
CCS03 ADDR3 03 L	03,13,12,11,10,09
CCS03 ADDR3 04 L	03,13,12,11,10,09
CCS03 ADDR3 05 L	03,13,12,11,10,09
CCS03 ADDR3 06 L	03,13,12,11,10,09
CCS03 ADDR3 07 L	03,13,12,11,10,09
CCS03 ADDR3 08 L	03,13,12,11,10,09
CCS03 ADDR3 09 L	03,13,12,11,10,09
CCS03 ADDR4 00 L	03,13,12,11,10,09
CCS03 ADDR4 01 L	03,13,12,11,10,09
CCS03 ADDR4 02 L	03,13,12,11,10,09

SIGNAL NAME	PAGE NUMBER(S)
CCS03 ADDR4 03 L	03,13,12,11,10,09
CCS03 ADDR4 04 L	03,13,12,11,10,09
CCS03 ADDR4 05 L	03,13,12,11,10,09
CCS03 ADDR4 06 L	03,13,12,11,10,09
CCS03 ADDR4 07 L	03,13,12,11,10,09
CCS03 ADDR4 08 L	03,13,12,11,10,09
CCS03 ADDR4 09 L	03,13,12,11,10,09
CCS03 ADDR5 00 L	03,13,12,11,10,09
CCS03 ADDR5 01 L	03,13,12,11,10,09
CCS03 ADDR5 02 L	03,13,12,11,10,09
CCS03 ADDR5 03 L	03,13,12,11,10,09
CCS03 ADDR5 04 L	03,13,12,11,10,09
CCS03 ADDR5 05 L	03,13,12,11,10,09
CCS03 ADDR5 06 L	03,13,12,11,10,09
CCS03 ADDR5 07 L	03,13,12,11,10,09
CCS03 ADDR5 08 L	03,13,12,11,10,09
CCS03 ADDR5 09 L	03,13,12,11,10,09
CCS14 CPU OSC OUT H	14
CCS14 TOK OSC OUT H	14
CMI DATA 00 H	14
CMI DATA 01 H	14
CMI DATA 02 H	14
CMI DATA 03 H	14
CMI DATA 04 H	14
CMI DATA 05 H	14
CMI DATA 06 H	14
CMI DATA 07 H	14
CMI DATA 08 H	14
CMI DATA 09 H	14
CMI DATA 10 H	14
CMI DATA 11 H	14
CMI DATA 12 H	14
CMI DATA 13 H	14
CMI DATA 14 H	14
CMI DATA 15 H	14
CMI DATA 16 H	14
CMI DATA 17 H	14
CMI DATA 18 H	14
CMI DATA 19 H	14
CMI DATA 20 H	14
CMI DATA 21 H	14
CMI DATA 22 H	14
CMI DATA 23 H	14
CMI DATA 26 H	14
CMI DATA 27 H	14
CMI DBBZ L	14
CMI STATUS 00 L	14
CMI STATUS 01 L	14
CS ADDR 00 H	03,02,14
CS ADDR 01 H	03,02,14

SIGNAL NAME	PAGE NUMBER(S)
CS ADDR 02 H	03,02,14
CS ADDR 03 H	03,02,14
CS ADDR 04 H	03,02,14
CS ADDR 05 H	03,02,14
CS ADDR 06 H	01,03,02,14
CS ADDR 07 H	01,03,02,14
CS ADDR 08 H	01,03,02,14
CS ADDR 09 H	01,03,02,14
CS ADDR 10 H	01,14
CS ADDR 11 H	01,14
CS ADDR 12 H	01,14
CS ADDR 13 H	01,14
CS ALPCTL 0 H	10,05,14
CS ALPCTL 1 H	10,05,14
CS ALPCTL 2 H	10,05,14
CS ALPCTL 3 H	10,05,14
CS ALPCTL 4 H	10,05,14
CS ALPCTL 5 H	10,05,14
CS ALPCTL 6 H	10,05,14
CS ALPCTL 7 H	10,05,14
CS ALPCTL 8 H	10,05,14
CS ALPCTL 9 H	10,05,14
CS BUS 0 H	12,07,14
CS BUS 1 H	12,07,14
CS BUS 2 H	12,07,14
CS BUS 3 H	12,07,14
CS BUS 4 H	12,07,14
CS BUT 0 H	11,06,14
CS BUT 1 H	11,06,14
CS BUT 2 H	11,06,14
CS BUT 3 H	11,06,14
CS BUT 4 H	11,06,14
CS BUT 5 H	11,06,14
CS CC 0 H	12,07,14
CS CC 1 H	11,06,14
CS CLKX H	13,08,14
CS DTYPE 0 H	11,06,14
CS DTYPE 1 H	11,06,14
CS FPA 0 H	12,07,14
CS FPA 1 H	12,07,14
CS FPA 2 H	12,07,14
CS FPA 3 H	12,07,14
CS HNEXT PAR H	01
CS ISTRM H	11,06,14
CS JSR H	13,08,14
CS LIT 0 H	09,04,14
CS LIT 1 H	09,04,14
CS MISC CTL 0 H	09,04,14
CS MISC CTL 1 H	09,04,14
CS MISC CTL 2 H	09,04,14

NOTES:
1. THIS PAGE LISTS THE SCHEMATIC PAGE NUMBER(S) WHERE A SIGNAL NAME IS REFERENCED.

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REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN. <i>DeMora</i>	DATE <i>27-FEB-88</i>	ENG.	DATE	TITLE: CCS FORWARD REFERENCE
	CHK'D.	DATE	BOARD LOCATION: AC5	SHEET 1 OF 1	SIZE CODE D CS NUMBER L0005-0-15 REV. C
FIRST USED ON OPTION/MODEL: 11/750		NEXT HIGHER ASSEMBLY: B-DD-L0005-0-0			

SIGNAL NAME	PAGE NUMBER(S)
CS MISC CTL 3 H	09,04,14
CS MISC CTL 4 H	09,04,14
CS MSRC 0 H	09,04,14
CS MSRC 1 H	09,04,14
CS MSRC 2 H	09,04,14
CS MSRC 3 H	09,04,14
CS MSRC 4 H	09,04,14
CS NEXT 00 H	13,08,14
CS NEXT 01 H	13,08,14
CS NEXT 02 H	13,08,14
CS NEXT 03 H	13,08,14
CS NEXT 04 H	13,08,14
CS NEXT 05 H	13,08,14
CS NEXT 06 L	01,13,08,14
CS NEXT 07 L	01,13,08,14
CS NEXT 08 L	01,13,08,14
CS NEXT 09 L	01,13,08,14
CS NEXT 10 L	01,13,08,14
CS NEXT 11 L	01,13,08,14
CS NEXT 12 L	01,13,08,14
CS NEXT 13 L	01,13,08,14
CS PAR 0 H	09,04,14
CS PAR 1 H	09,04,14
CS ROT 0 H	10,05,14
CS ROT 1 H	10,05,14
CS ROT 2 H	10,05,14
CS ROT 3 H	10,05,14
CS ROT 4 H	10,05,14
CS ROT 5 H	10,05,14
CS RSRC 0 H	11,06,14
CS RSRC 1 H	11,06,14
CS RSRC 2 H	11,06,14
CS RSRC 3 H	11,06,14
CS RSRC 4 H	11,06,14
CS RSRC 5 H	11,06,14
CS SPW 0 H	09,04,14
CS SPW 1 H	09,04,14
CS WCTRL 0 H	12,07,14
CS WCTRL 1 H	12,07,14
CS WCTRL 2 H	12,07,14
CS WCTRL 3 H	12,07,14
CS WCTRL 4 H	12,07,14
CS WCTRL 5 H	12,07,14
DPM14 DISABLE HI NEXT H	01
DPM17 B CLK L	14
DPM17 M CLK L	01
DPM17 PHASE 1 H	14
UBI13 MSEQ INIT L	14
WCS PRESENT L	14

SIGNAL NAME PAGE NUMBER(S)

SIGNAL NAME PAGE NUMBER(S)

NOTES:
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CHK	CHANGE NO.	REV

digital
[160,1271]CCS16.DRW
FIRST USED ON OPTION/MODEL: 11/750

DRN. *DJ*
CHK'D.
DATE 25-MAR-88

ENG.
DATE
BOARD LOCATION: ACS
SHEET 1 OF 1

SIZE CODE
D CS

TITLE: CCS
FORWARD REFERENCE
NUMBER
L0005-0-16

REV.
C

DRAWING NO.	NO. OF SHTS.	PART NO.	DESCRIPTION	REVISIONS	
				A	B
		M9313	MODULE REVISION	A	B
B-DD-M9313-0	1		UET DRAWING DIRECTORY	A	B
D-UA-M9313-0-0	3		UET UNIT ASSEMBLY	A	A
K-PL-M9313-0-DBP	2		UET PARTS LIST	A	B
D-MD-5013847-0-0	5		UET DRILL & ETCH DRAWINGS	A	A
		5013847	ETCHED BOARD	B	B
K-PC-M9313-0-DBC			UET P C DESIGN DATA BASE CALDEC	B	B
K-CS-M9313-0-DBS			UET DESIGN DATA BASE SUDS	A	A
D-CS-M9313-0-1	1	*	DATA PATH (7:0)	A	A
D-CS-M9313-0-2	1	*	DATA PATH (15:8)	A	A
D-CS-M9313-0-3	1	*	ADDRESS SELECTION	A	A
D-CS-M9313-0-4	1	*	UNIBUS CONTROL	A	A
D-CS-M9313-0-5	1	*	INTERRUPT CONTROL	A	A
D-CS-M9313-0-6	1	*	UNIBUS TERMINATION	A	A
D-CS-M9313-0-7	1	*	FORWARD REFERENCE	A	A
D-CS-M9313-0-8	1	*	FORWARD REFERENCE	A	A

NOTES:

*CONTROL SOURCE IS THE SUDS DATA BASE
NO CONTROLLED PAPER ORIGINALS EXIST.

DATE	CHG NO.	REV.	REVISIONS	
			A	B
	TW001		B	

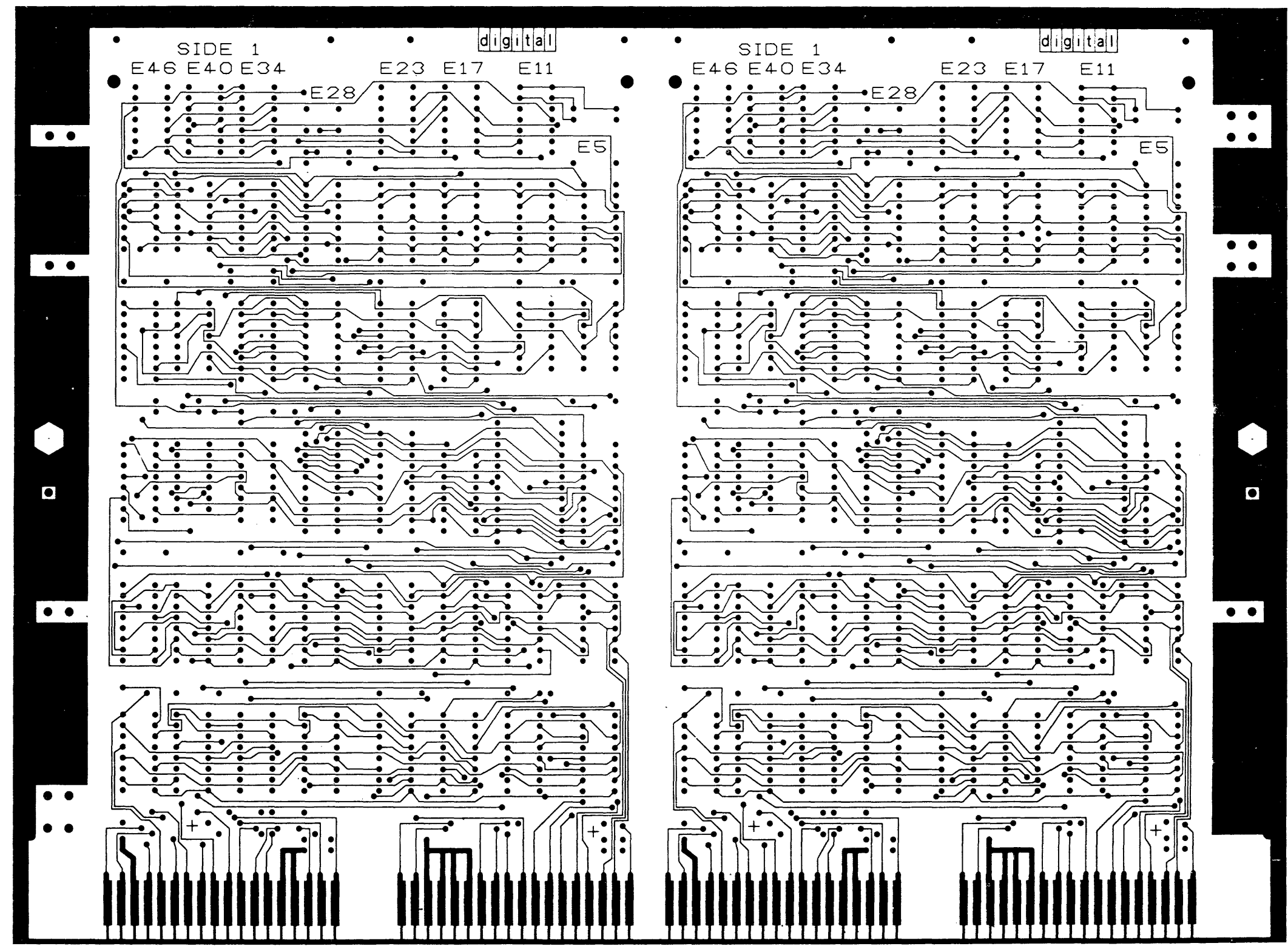
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USED ON OPTION/MODEL	DRN. M. FUNARO	TITLE
11/750	CHK'D J. CASEY	UET
	ENG. R. ARMSTRONG	SIZE B CODE DD NUMBER M9313-0 REV. B
	PROD. J. CONSIDINE	SHEET 1 OF 1

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DIGITAL EQUIPMENT CORPORATION

LAYER 1



D
C
B
A

D
C
A
B
A

REVISIONS		
CHK	CHANGE NO	REV

TITLE	UET	SIZE CODE	D U A	NUMBER	M9313 - 0 - 0	REV.	A
SCALE	2 - 1	SHEET	2	OF	3	DIST	

rw

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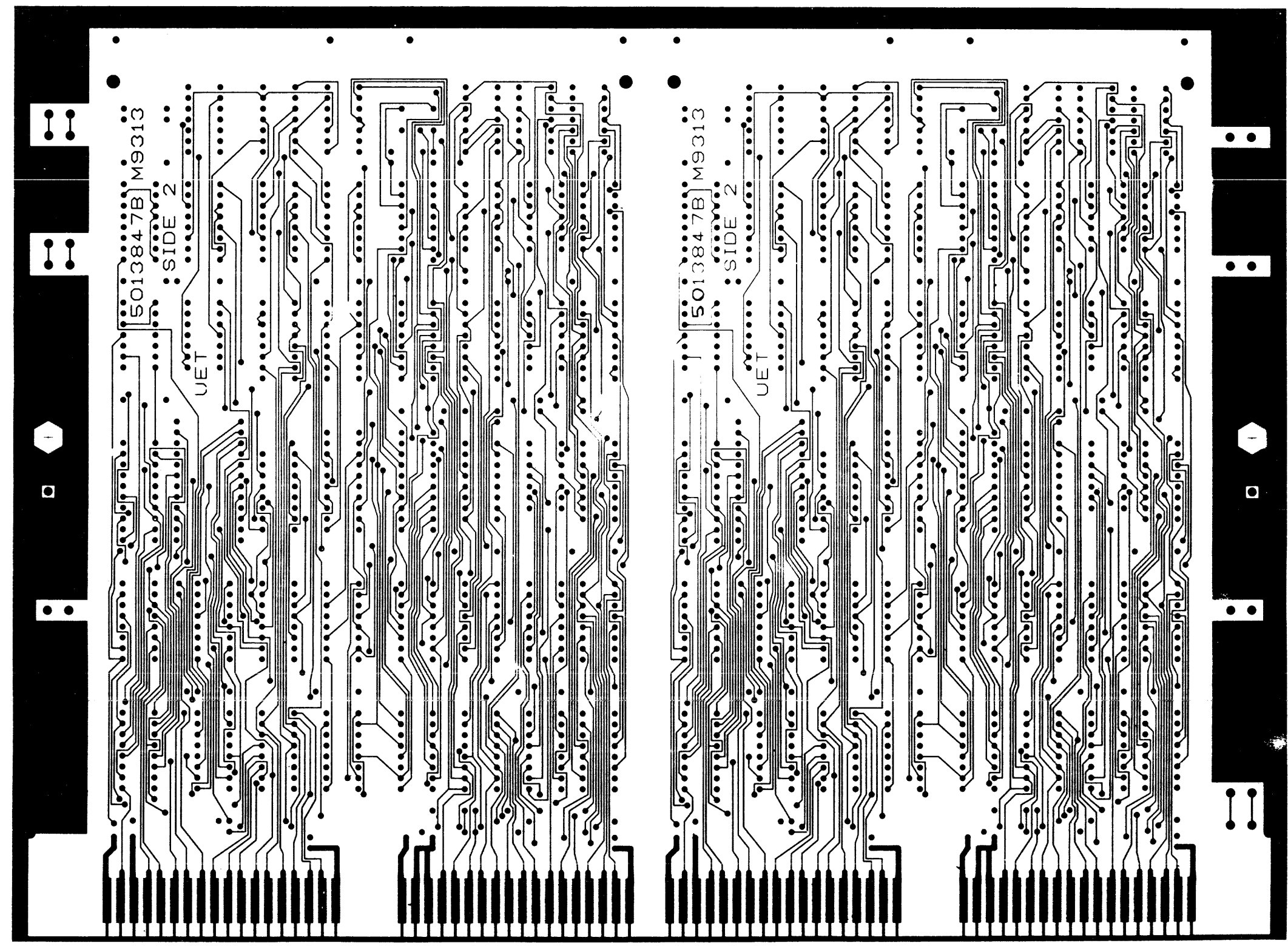
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HA 93YA J



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D
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REVISIONS		
CHK	CHANGE NO	REV

TITLE	UET	SIZE CODE	NUMBER	REV
SCALE	2 - 1	SHEET	3 OF 3	DUA M9313 - 0 - 0 A
DIST.				

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION 00	REFERENCE DESIGNATOR
11	11	5013847-00	VET	1	
11	11	1215006-06	SKT, IC 24PIN DIP TIN PLATE	1	YES
11	11	1300005-04	R NETWORK 15-470 5.0 % 16PIN	1	E27
11	11	1300202-00	47.0 .25 W 5.0 % CC	1	R1, R2
11	11	1300229-00	100.0 .25 W 5.0 % CC	1	R3, R4
11	11	1312628-00	R NETWORK 14-176.5 14-375 16PIN	1	E2, E42
11	11	1312628-01	R NETWORK 14-176.5 14-375 16PIN	2	E12, E29
11	11	1909704-00	DEC 314A NOR GATE-SINGLE 7IN.	1	E37
11	11	1909705-00	DEC 8881 NAND GATE-QUAD 2IN 0	1	E39
11	11	1910436-00	DEC 74123 ONE SHOT-DUAL, RETRIG	1	E28
11	11	1911469-00	DEC 8640 RECEIVER, BUS, QUAD, U	1	E4
11	11	1911579-00	8641 TRANSCEIVER, BUS, QUA	10	E1, E7, E18, E19, E30, E31, E35, E36, E41, E44
13	13	1911983-00	74S133 NAND GATE-POSITIVE 1	1	CONT E43
13	13	1912799-00	LS00 NAND-GATE-QUAD 2IN, P	1	E40
13	13	1912803-00	74LS04 INVERTER GATE, HEX	2	E17, E39
13	13	1910535-00	74S05 INVERTER GATE-HEX 1	1	E33
13	13	1912805-00	LS08 AND GATE-QUAD 2IN, PO	1	E46
13	13	1912808-00	LS11 AND GATE-TRIPLE 3IN	1	E34
13	13	1912810-00	LS20 NAND GATE-DUAL 4IN	1	E23
13	13	1912824-00	LS74 FF-D DUAL, EDGE TRIGG	2	E38, E45
13	13	1912853-00	LS175 FF-D QUAD	1	E21
13	13	1914214-00	LS374 FF-D OCTAL EDGE TRIG	1	E3, E26
13	13	1914438-00	DC 013 UNIBUS INTERRUPT-BIP	2	E5, E10, E15, E16, E22
13	13	1914845-00	2918 FF-D QUAD TRI-STATE	5	E6, E13, E14, E24, E25
13	13	2327291-00	A1-03, A1-04, A1-05	1	E32
13	13	1000055-00	2200.0 MMF 250V 20% Y5S DISC	2	C2, C4
13	13	1000043-00	1000.0 MMF 250V 20% Y5F DISC	2	C5, C6
13	13	1001610-01	.01 MFD50/100V +80-20% DISC	1	C7
13	13	1611243-00	DELAY=25-250NS, 10TAPS RCL#L-183	1	E11

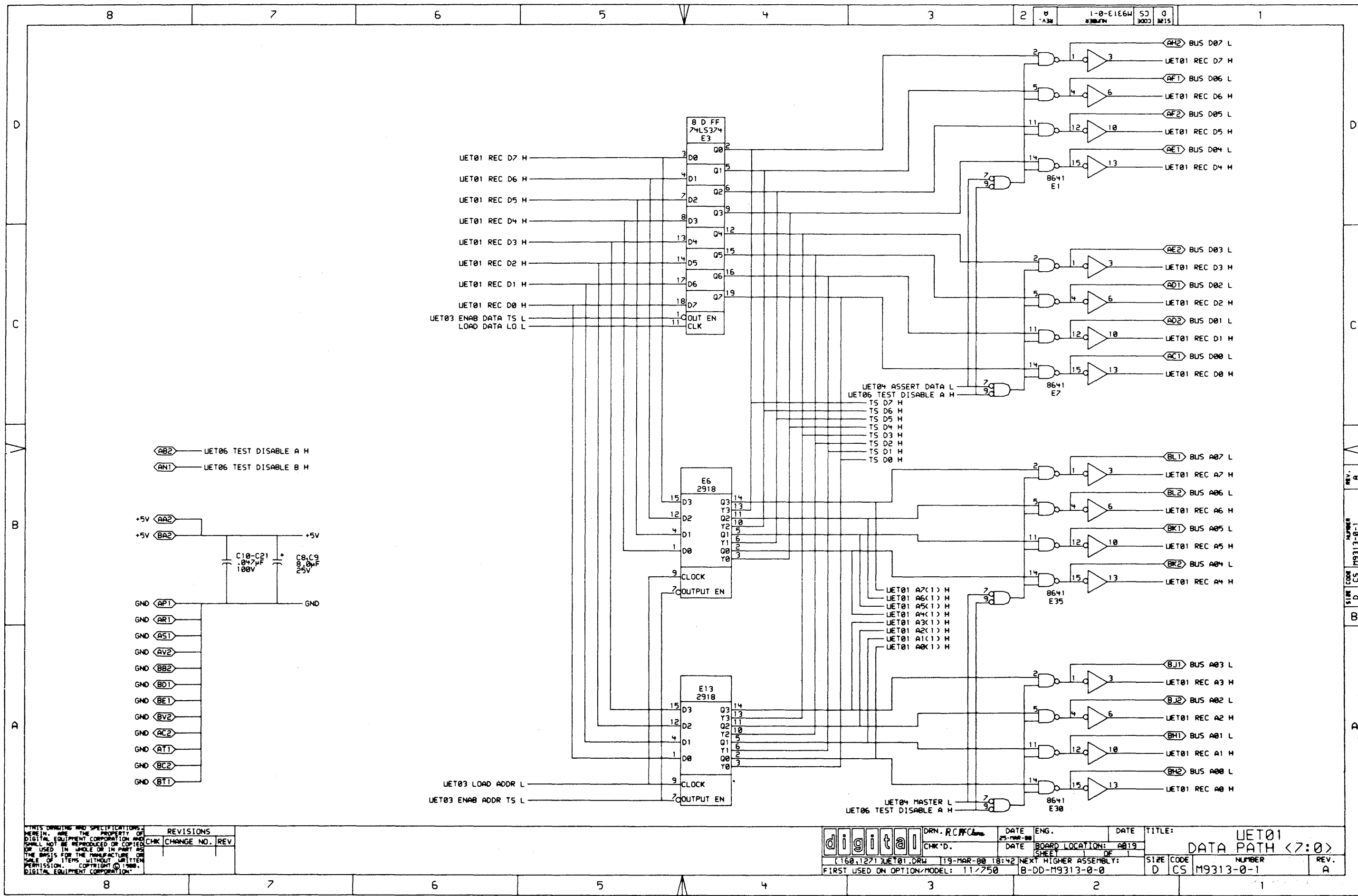
REVISION HISTORY		BASIC PART NO: M9313		DRN:	M.FUNARO	DATE:	22-MAY-79	D I G I T A L	
ENG:	ECO NUMBER	REV	SECTION A OF A	CHK'D:	F.GAROFALO	DATE:	22-MAY-79	TITLE	PARTS LIST
SB	M9312-TW001	A	SECTION VARIATION INDEX	DES. ENG:	B.ARMSTRONG	DATE:	22-MAY-79	UET	
		B	[A] 00	RESP. ENG.:	B.ARMSTRONG	DATE:	22-MAY-79	DOCUMENT NUMBER	
			[B]	MFG. ENG.:	K.O'BRIEN	DATE:	13-FEB-80	SIZE! CODE! NUMBER	REV
			[C]	ASSEMBLY NUMBER:	D-UA-M9313-0-0	TOP DOCUMENT NUMBER:	B-DD-M9313-0-0	K PL M9313-0-DBP	B
			[D]			FILE NAME:	Z1259B.PLS		EDIT #
			[E]						15
			[F]						
			[G]						
			[H]						
			[I]						
			[J]						
			[K]						
			[L]						
			[M]						
			[N]						

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LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION	REFERENCE DESIGNATOR
30	30		1913777-00	LS240 DRIVER LINE, OCTAL, T	1	E20
31	31		1302394-00	30.0 K .25 W 5.0% CC	1	R5, R6
32	32		1305125-00	383.0 .25 W 1.0% RN55D-F10	1	R7, R8
33	33		1012784-00	.047 MFD 50V +80-20% CER	1	C10-C21
34	34	SEE NOTES	1012084-01	8 MFD 25V +75-10% AL EL	1	C8, C9
35	35		1000024-00	470.0 MMF 100V 5%200PPM MICA	1	C1, C3
36	36		9008337-06	HANDLE, FLIP CHIP, MAGENTA	1	
37	37		9000024-01	EYELET, ROLL FLANGE .1210DX .192	4	

38 NOTE: SOME MODULES WILL HAVE 10-05306 INSTEAD OF 10-12084-01

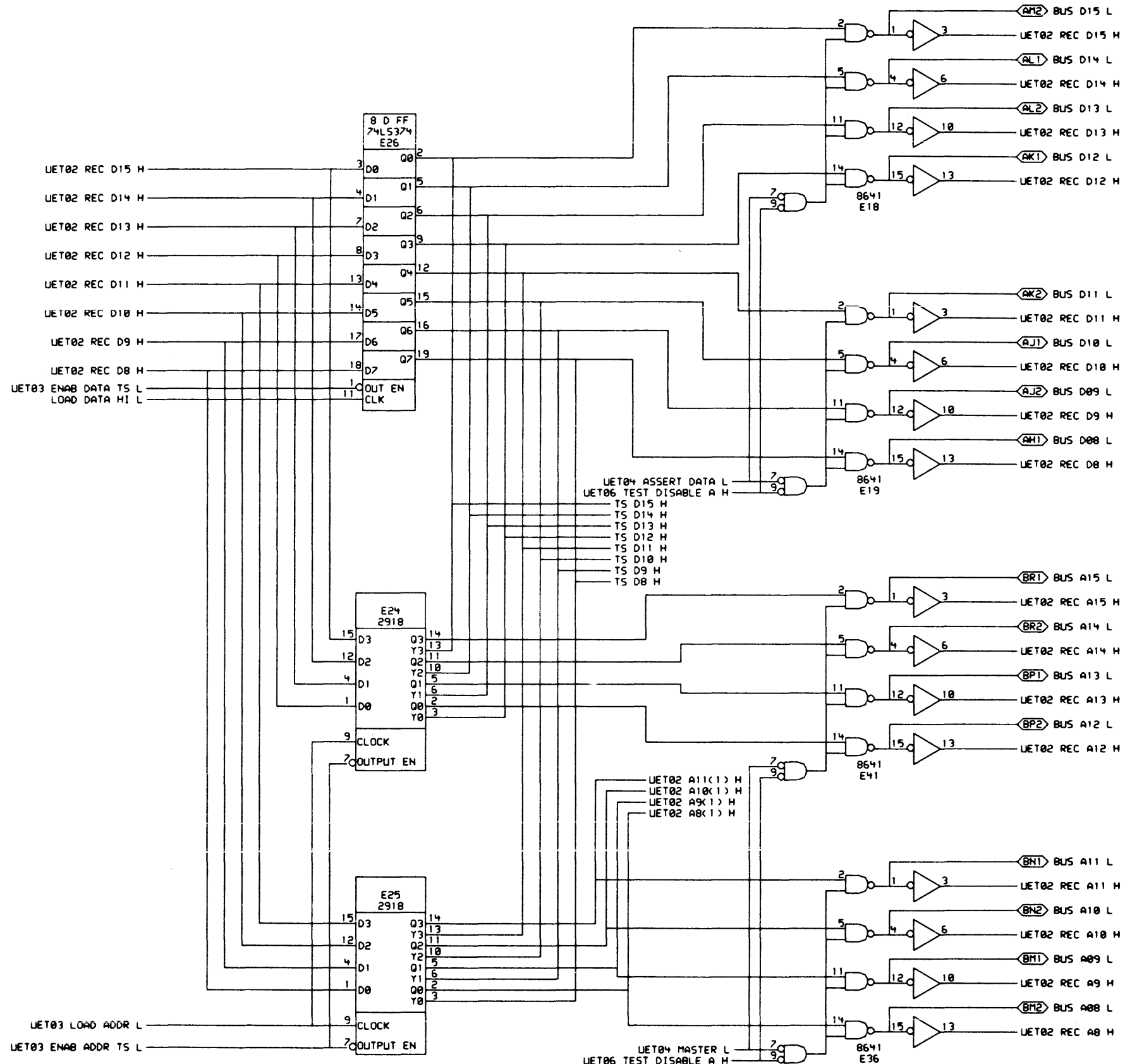
D	I	G	I	T	A	L	TITLE	SECTION A OF A	SIZE	CODE	DOCUMENT NUMBER	REV
							UET		K	PL	M9313-0-DBP	B



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REVISIONS	
CHK	CHANGE NO. REV

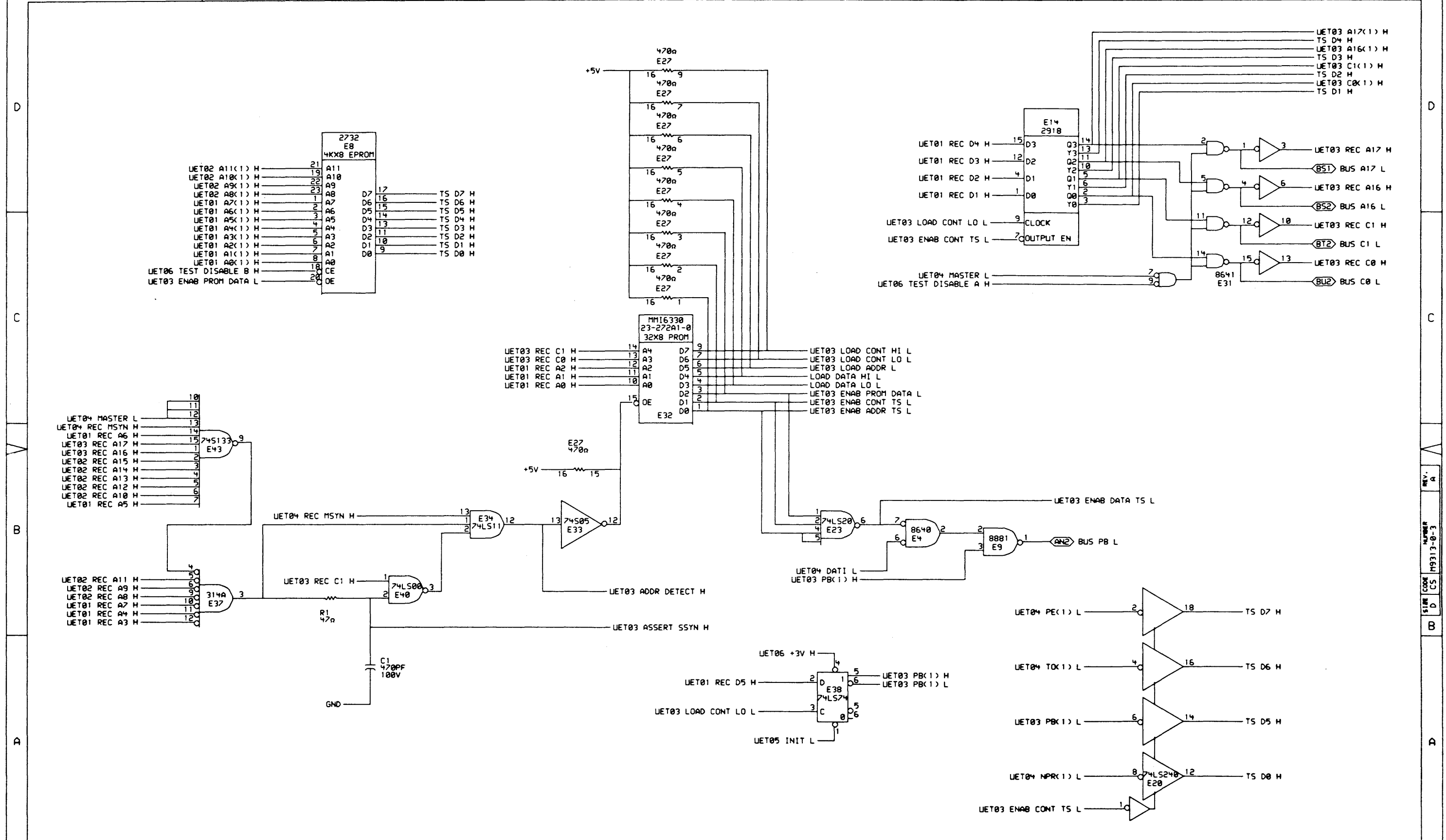
	DRN. R.C.F.C.	DATE	ENG.	DATE	TITLE:
	CHK'D.	25-MAR-88			UET01 DATA PATH <7:0>
(168,1271) UET01.DRW 119-MAR-88 18:42 NEXT HIGHER ASSEMBLY:		BOARD LOCATION: A019		SIZE	CODE
FIRST USED ON OPTION/MODEL: 11/750		B-DD-M9313-0-0		D	CS
NUMBER				M9313-0-1	REV.
A					



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REVISIONS	
CHK	CHANGE NO. REV

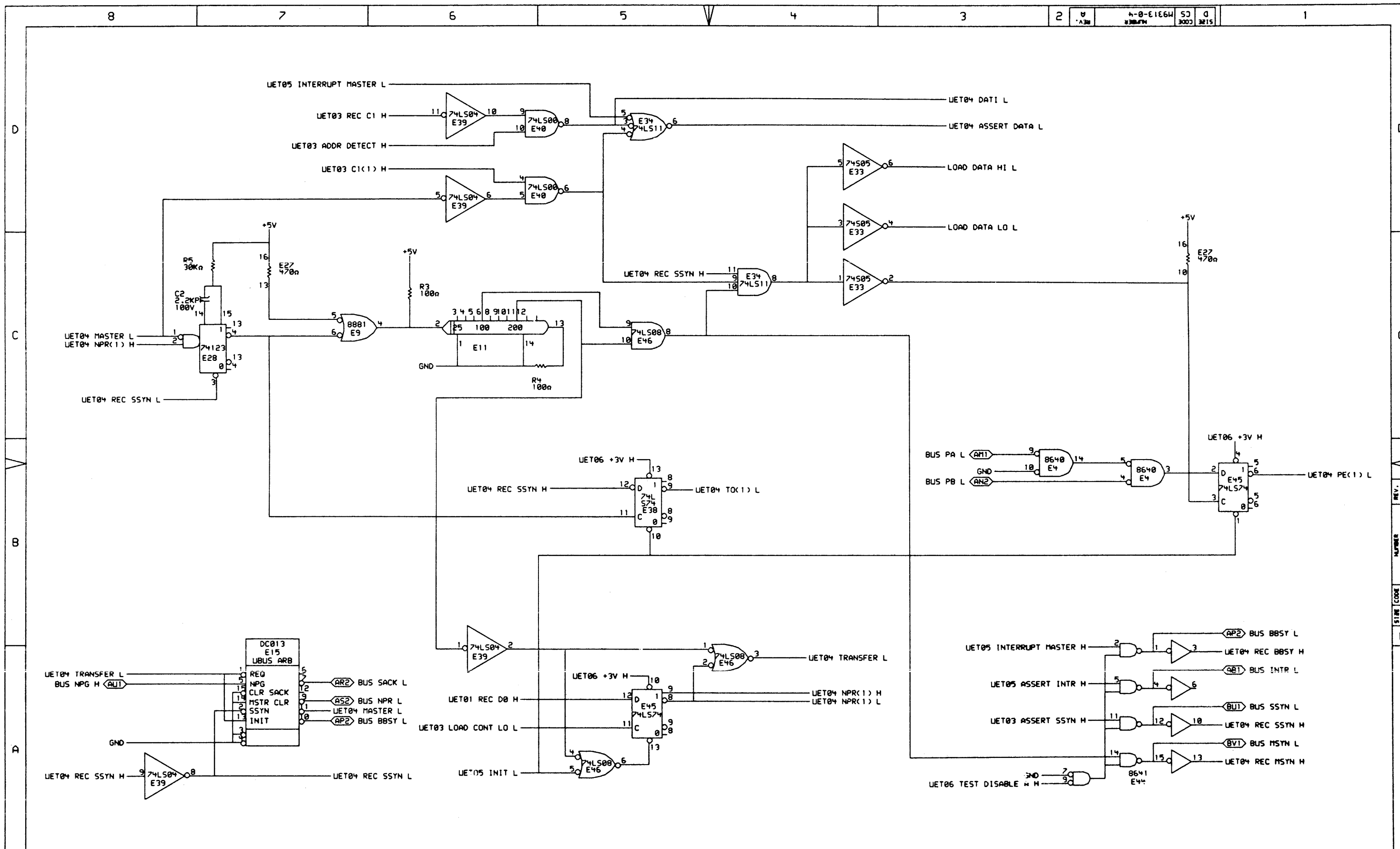
digital	DRN. R.C.F.C.	DATE	ENG.	DATE	TITLE:
	CHK'D.	DATE	BOARD LOCATION: AB19		UET02 DATA PATH <15:8>
[160,1271] UET02.DRW		17-FEB-80 13:17	NEXT HIGHER ASSEMBLY:	SIZE CODE	NUMBER
FIRST USED ON OPTION/MODEL: 11/750		B-DD-M9313-0-0		D CS	M9313-0-2
					REV. A



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REVISIONS		
CHK	CHANGE NO.	REV

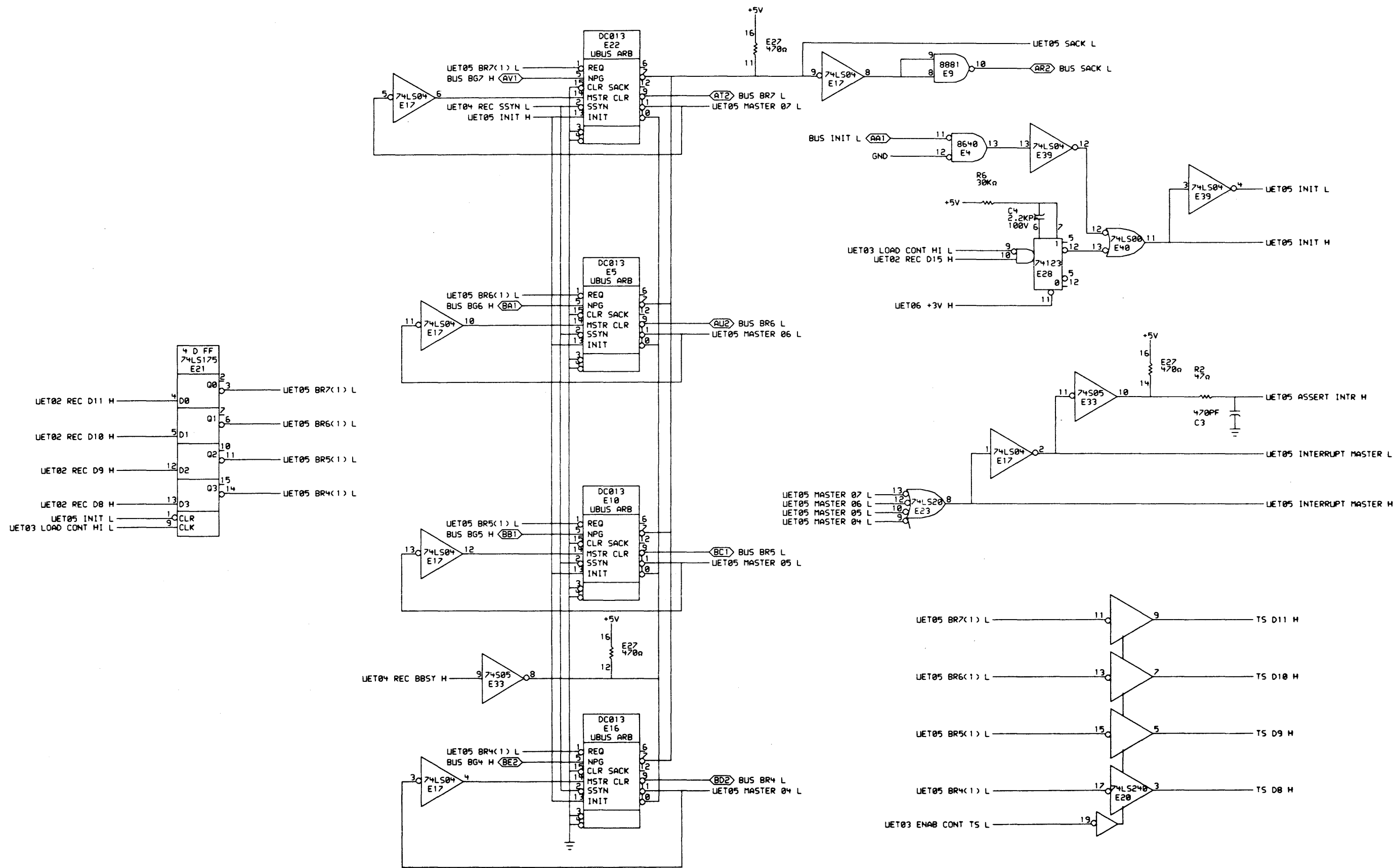
digital	DRN. R.C.F.C.	DATE	ENG.	DATE	TITLE:
	CHK'D.	21-MAR-80			UET03 ADDRESS SELECTION
[160,127] UET03.DRW		19-MAR-80 18:50	NEXT HIGHER ASSEMBLY:		SIZE CODE
FIRST USED ON OPTION/MODEL: 11/750		B-DD-M9313-0-0		D CS	NUMBER
				M9313-0-3	REV. A



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REVISIONS	
CHK	CHANGE NO. REV

digital	DRN. RCAF/CS	DATE 21-MAR-80	ENG.	DATE	TITLE: UET04 UNIBUS CONTROL
	CHK'D.	DATE	BOARD LOCATION: A819	SHEET OF	SIZE CODE D CS M9313-0-4
FIRST USED ON OPTION/MODEL: 11/250			NEXT HIGHER ASSEMBLY: B-DD-M9313-0-0		NUMBER REV. A

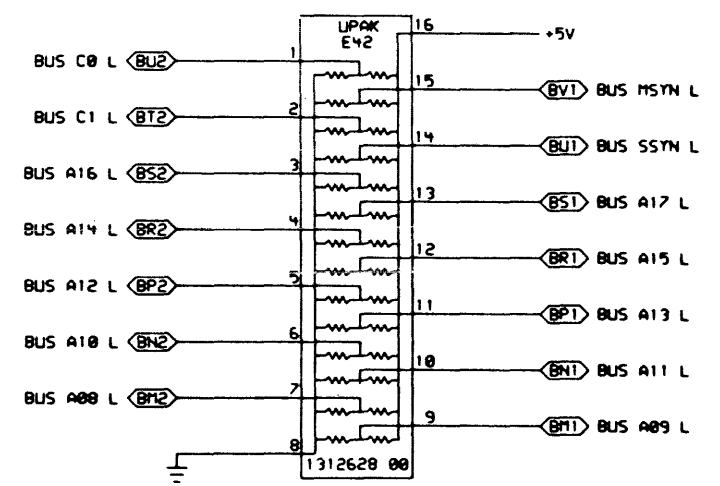
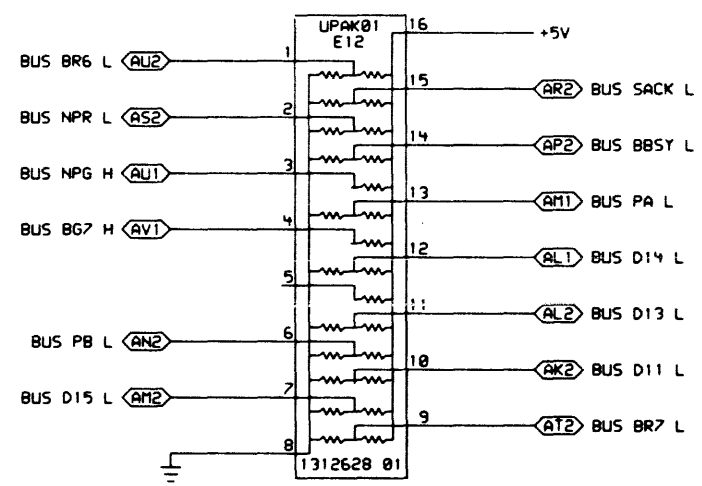
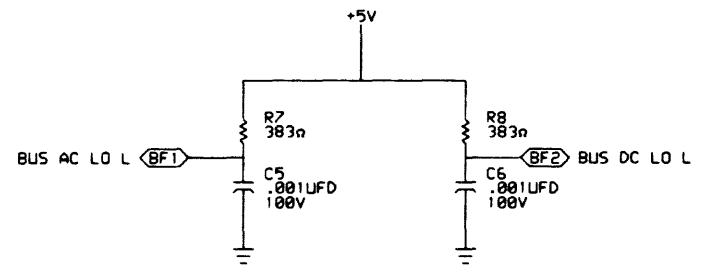
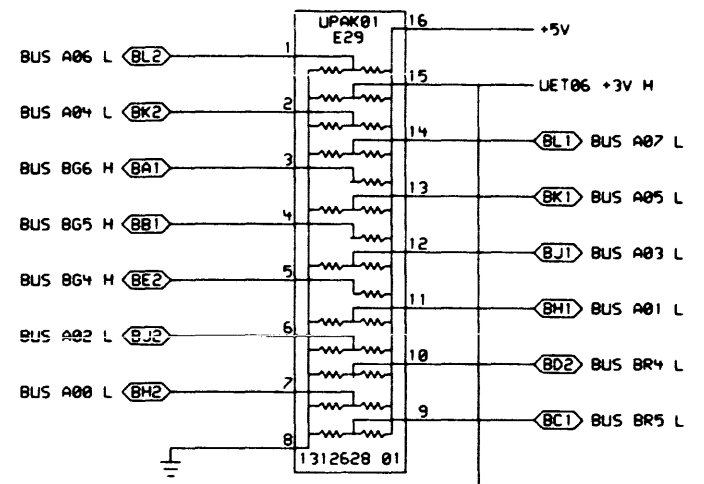
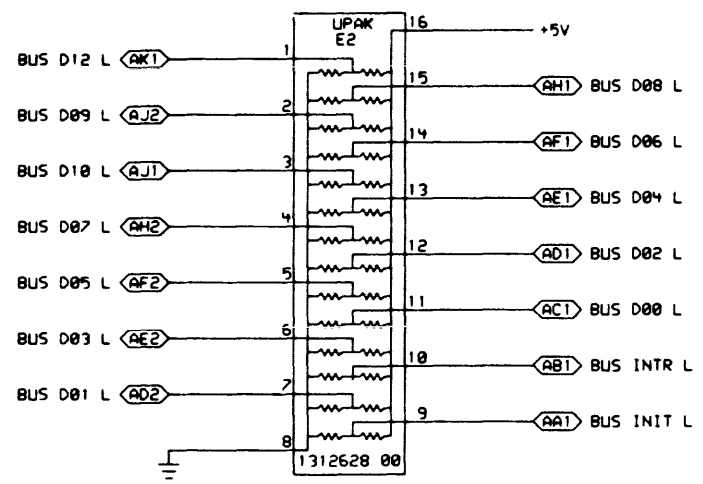


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REVISIONS		
CHK	CHANGE NO.	REV

--	--	--	--	--	--	--	--	--	--

digital	DRN. R.C.F.C.	DATE	ENG.	DATE	TITLE:	UET05 INTERRUPT CONTROL			
	CHK'D.	DATE	BOARD LOCATION: AB19	SHEET	OF	SIZE	CODE	NUMBER	REV.
[160,1271] UET05.DRW		17-FEB-80 13:22	NEXT HIGHER ASSEMBLY:	B-DD-M9313-0-0		D	CS	M9313-0-5	A
FIRST USED ON OPTION/MODEL: 11/750									



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REVISIONS	
CHK	CHANGE NO. REV

digital	DRN. R.C. #	DATE	ENG.	DATE	TITLE:
	CHK'D.	DATE	BOARD LOCATION:		UET06 UNIBUS TERMINATION
[160,1271] UET06.DRW 17-FEB-80 13:25 NEXT HIGHER ASSEMBLY:					SIZE
FIRST USED ON OPTION/MODEL: 11/750					D CS
B-DD-M9313-0-0					NUMBER
					M9313-0-6
					REV.
					A

SIGNAL NAME	PAGE NUMBER(S)	SIGNAL NAME	PAGE NUMBER(S)	SIGNAL NAME	PAGE NUMBER(S)
UET05 BR4 (0)H	05				
UET05 BR5 (0)H	05				
UET05 BR6 (0)H	05				
UET05 BR7 (0)H	05				
UET05 INIT H	05				
UET05 INIT L	05,03,04				
UET05 INTERRUPT MASTER H	05,04				
UET05 INTERRUPT MASTER L	05,04				
UET05 MASTER 04 L	05				
UET05 MASTER 05 L	05				
UET05 MASTER 06 L	05				
UET05 MASTER 07 L	05				
UET05 SACK L	05				
UET06 +3V H	06,05,03,04				
UET06 TEST DISABLE A H	01,02,03,04				
UET06 TEST DISABLE B H	03				

NOTES:
1. THIS PAGE LISTS THE SCHEMATIC PAGE NUMBER(S) WHERE A SIGNAL NAME IS REFERENCED.

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REVISIONS		
CHK	CHANGE NO.	REV

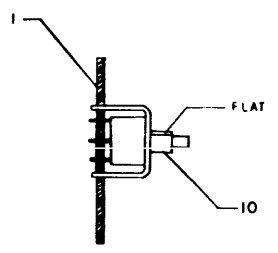
digital	DRN. <i>DE</i>	DATE 25-MAR-88	ENG.	DATE	TITLE: UET08 FORWARD REFERENCE
	CHK'D.	DATE	BOARD LOCATION: AB19	SHEET 1 OF 1	
[160,127] UET08.DRW		17-FEB-88 13:30	NEXT HIGHER ASSEMBLY:	SIZE CODE NUMBER REV.	
FIRST USED ON OPTION/MODEL: 11/750		B-DD-M9313-0-0		D CS M9313-0-8	A

REV. A
NUMBER M9313-0-8
CODE CS
SIZE D

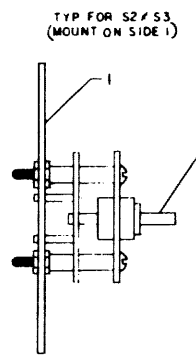
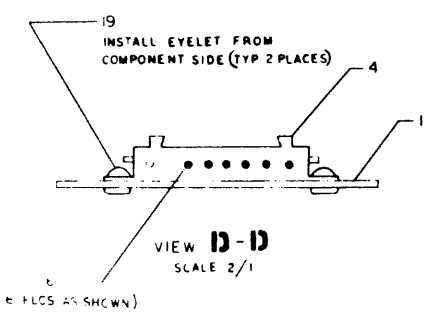
ECO REWORK INSTRUCTIONS
 WIRE ADDS SIDE 1
 1-1 ADD 18 AWG 9107560-03 WIRE AND TUBING
 9107274-06 BETWEEN BOTTOM LEAD OF C8
 AND J2-1.
 WIRE ADDS SIDE 2
 1-2 BETWEEN J1-14 AND J1-20

COMPONENT SIDE VIEW

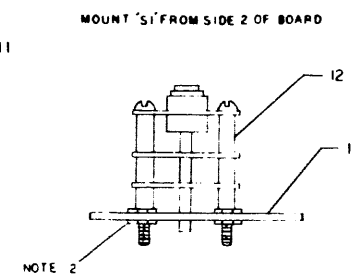
DESIGN	DATE	BY	CHK'D	DATE	BY
117750	11/750				
CONTROL PANEL					
ELECT. REV. 5					



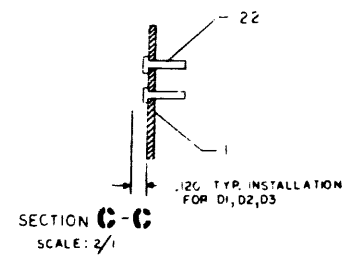
SECTION E-E
SCALE 2/1



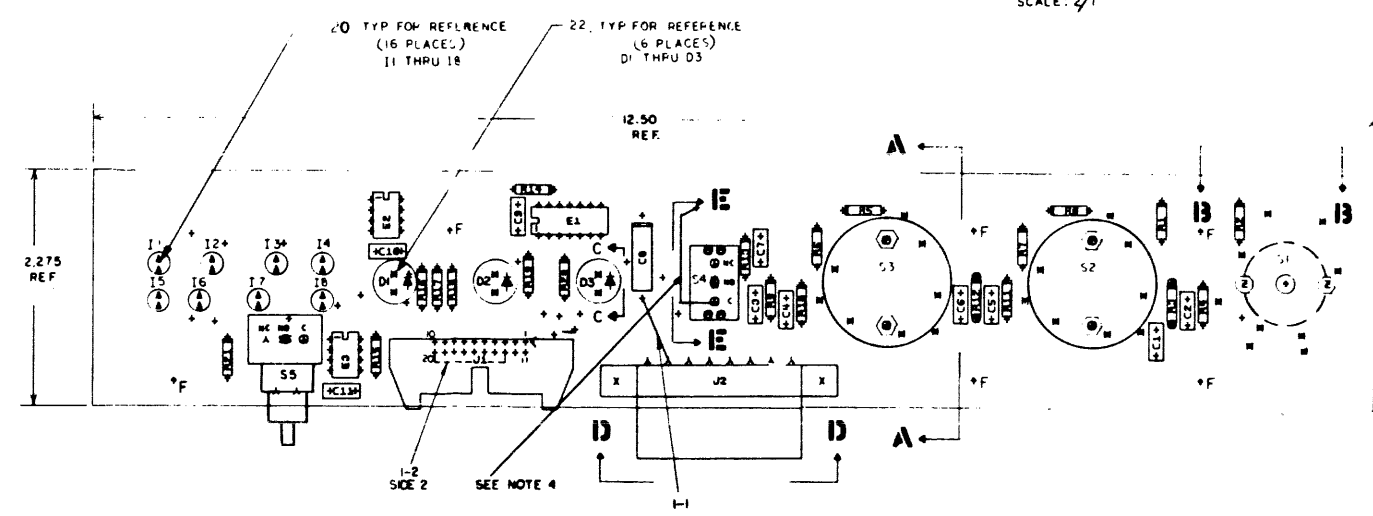
SECTION A-A
SCALE 2/1



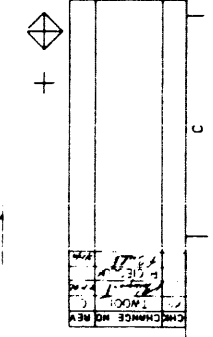
SECTION B-B
SCALE 2/1



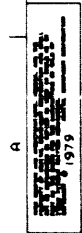
SECTION C-C
SCALE 2/1



NOTES:
 1-18 WIRE FROM L-1-NR FAC OF S4 TO
 FEET THRU 48-VE S4



NOTES:	1-18 WIRE FROM L-1-NR FAC OF S4 TO FEET THRU 48-VE S4
1-18 WIRE FROM L-1-NR FAC OF S4 TO FEET THRU 48-VE S4	
1-18 WIRE FROM L-1-NR FAC OF S4 TO FEET THRU 48-VE S4	
1-18 WIRE FROM L-1-NR FAC OF S4 TO FEET THRU 48-VE S4	



AUTOMATED BY PRTLST.4Q(50)

PARTS LIST

SHEET A1 OF A2

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION	REFERENCE DESIGNATOR
					00	
1	1	D-MD-5013794-0-0	5413795-00	11750 CONTROL PANEL	1	
2	2		1001610-00	.01 MFD 50V +80-20% Z5U CER	10	C1-C7,C9-C11
3	3	SEE NOTE	1012084-01	8 MFD 25V +75-10% AL EL	1	C8
4	4		1209340-00	MATE-N-LOK 08SKT(1X08). HSG	1	J2
5	5		1209456-01	MATE-N-LOK 01SKT 20-18AWG .0870D	6	
6	6		1209941-06	PCB,HEADER 20PIN(2X20).100CC 90D	1	J1
7	7		1211068-00	LAMP,T-1 5VDC,.115A	8	I1-I8
8	8		1212749-00	LIGHT,PNL INDICATOR,LED,RED	2	D2,D3
9	9		1212749-01	LIGHT,PNL INDICATOR,LED,GRN	1	D1
10	10		1216167-00	SW,PB 1PDT NO MOM-NC 0.4A	1	S4
11	11		1216181-00	SW,ROT 2P 4POS 110VAC/0.5A	2	S2,S3
12	12		1216182-00	SW,ROT 2P 5POS 28VDC/0.1A 1	1	S1
13	13		1300229-00	100.0 .25 W 5.0 * CF	6	R1,R2,R5-R8
14	14		1300295-00	330.0 .25 W 5.0 * CF	2	R19,R20
15	15		1300365-00	1.0 K .25 W 5.0 * CF	6	R13,R15-R18,R21
16	16		1303177-00	2.40 K .25 W 5.0 * CF	7	R3,R4,R9-R12,R14
17	17		1910011-00	DEC 7486 X-OR GATE-QUAD 2INPU	1	E1
18	18		1910406-00	75451 DRIVER,PERIPH,DUAL,	2	E2,E3
19	19		9000024-01	EYELET,ROLLED 0.1210DX0.192	2	
20	20		9007812-00	SKT,SPRING 01POS GOLD PC M	16	
21	21		1216523-00	SW,PB 1PDT NO MOM-NC 1A	1	S5
22	22		1211449-01	SKT,SPRING 01SKT TIN KNOC	6	
23	23		9009963-00	PLUNGER, 1/4 DIA	6	
24	24		9009966-00	GROMMET,SNAP-IN POLYCARBONAT	6	
25	25		1209941-04	PCB,HEADER LATCH	1	
26	26		1209941-03	PCB,HEADER LATCH	1	
27	27		1216524-00	CAP,PB SW WHITE	1	C12
28	28		9107560-03	WIRE,BUSS 18AWG TIN BARE	A/R	
29	29		9107278-00	TUBING,TEFLON .042ID	A/R	

REVISION HISTORY			BASIC PART NO: 5413795		D I G I T A L			
ENG	ECO NUMBER	REV	SECTION A OF A	DRN:	P.TELLIER	DATE:	17-MAY-83	
	INITIAL	B	SECTION VARIATION INDEX	CHK'D:	F.GAROFALO	DATE:	17-MAY-83	TITLE
RC	5413795-TW01A	C	[A] 00					PARTS LIST
RC	5413795-TW002	D	[B]					11/750 CONTROL PANEL
SB	5413795-TW003	E	[C]	DES.ENG:	R.CIESLUK	DATE:	17-MAY-83	
			[D]					DOCUMENT NUMBER
			[E]	RESP.ENG.:	D.CANE	DATE:	17-MAY-83	
			[F]					SIZE
			[H]	MFG.ENG.:	K.O'BRIEN	DATE:	17-MAY-83	CODE
			[J]					NUMBER
			[K]	ASSEMBLY NUMBER:	D-UA-5413795-0-0	TOP DOCUMENT NUMBER:	B-DD-5413795-0-0	REV
			[L]					FILE NAME:
			[M]					Z1255E.PLS
			[N]					EDIT #
								24

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P A R T S L I S T

SHEET A2 OF A2

LINE ITEM DOCUMENT NUMBER

PART NUMBER

DESCRIPTION

QTY PER VARIATION
00

REFERENCE DESIGNATOR

30 NOTE: SOME MODULES WILL HAVE 10-05306 INSTEAD OF 1012084-01

D	I	G	I	T	A	L	TITLE	SECTION A OF A	SIZE	CODE	DOCUMENT NUMBER	REV
							11/750 CONTROL PANEL		K	PL	5413795-0-DBP	E

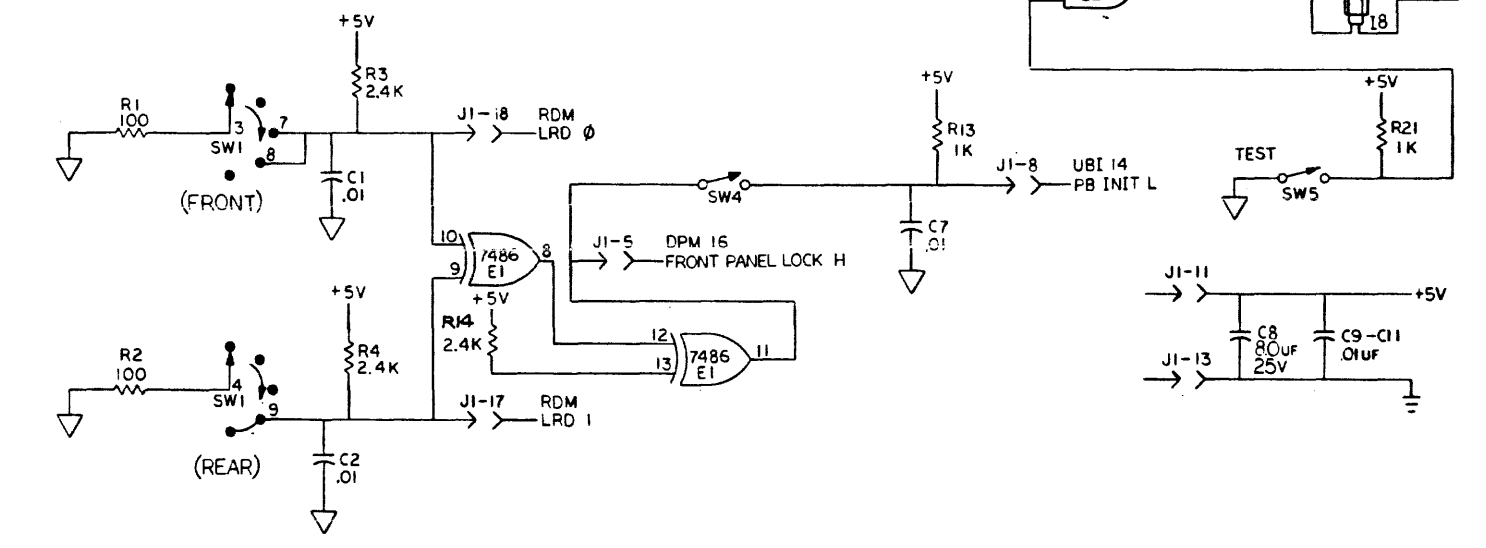
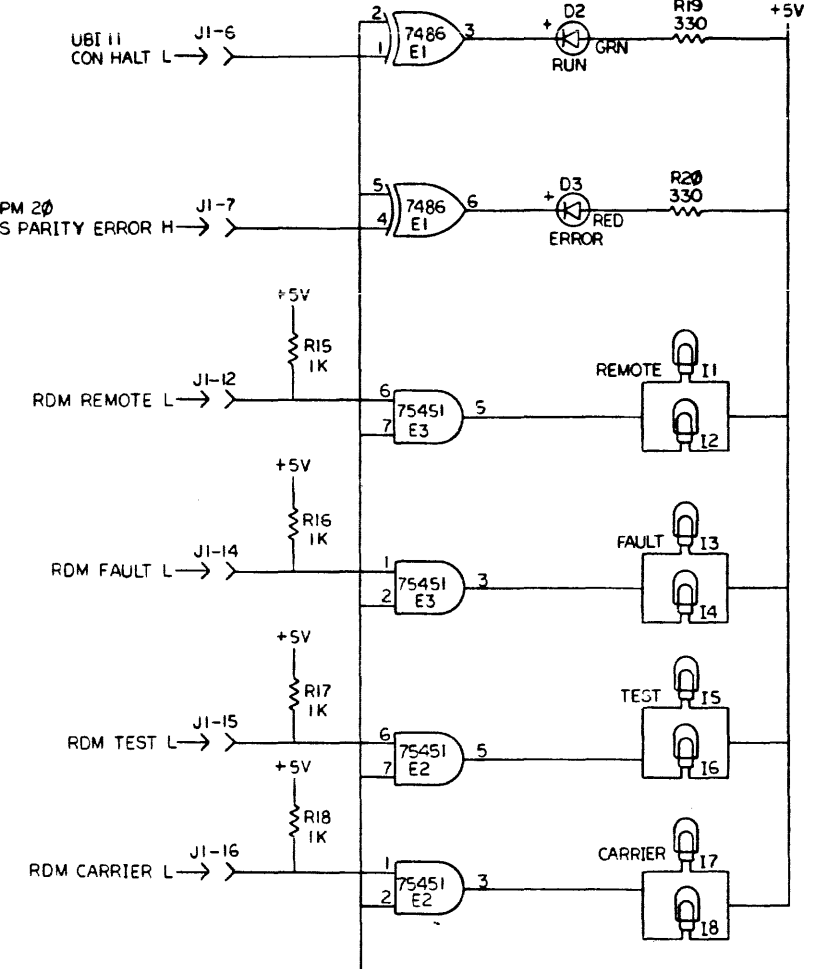
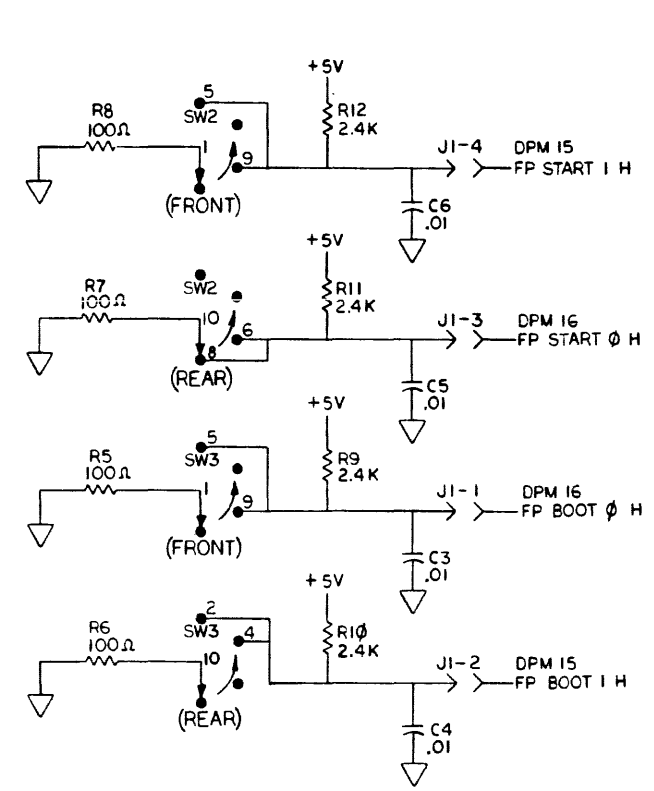
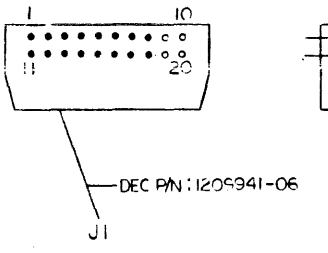
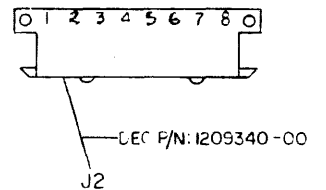
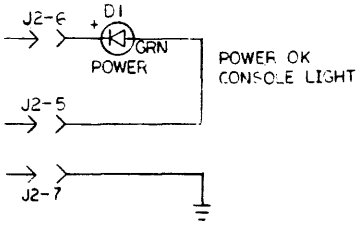
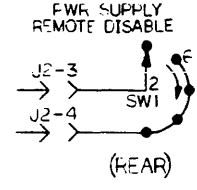
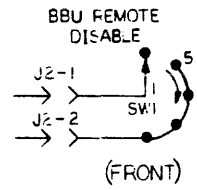
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SWITCH 1 (SHOWN IN POSITION ONE)		
SWITCH POS.	FUNCTION	CONNECTION
1	OFF	---
2	SECURE	1-5, 2-6
3	LOCAL	1-5, 2-6, 3-7
4	REMOTE SECURE	1-5, 2-6, 3-8, 4-9
5	REMOTE	1-5, 2-6, 4-9

SWITCH 2 (SHOWN IN POSITION ONE)		
SWITCH POS.	FUNCTION	CONNECTION
1	BOOT	8-10
2	RESTART	1-9, 6-10
3	HALT	---
4	RESTART	1-5

SWITCH 3		
SWITCH POS.	FUNCTION	CONNECTION
1	A	---
2	B	1-9
3	C	4-10
4	D	1-5, 2-10

NOTES:
1. SWITCHES 2 AND 3 ARE IN POSITION ONE WHEN FULLY CLOCKWISE.

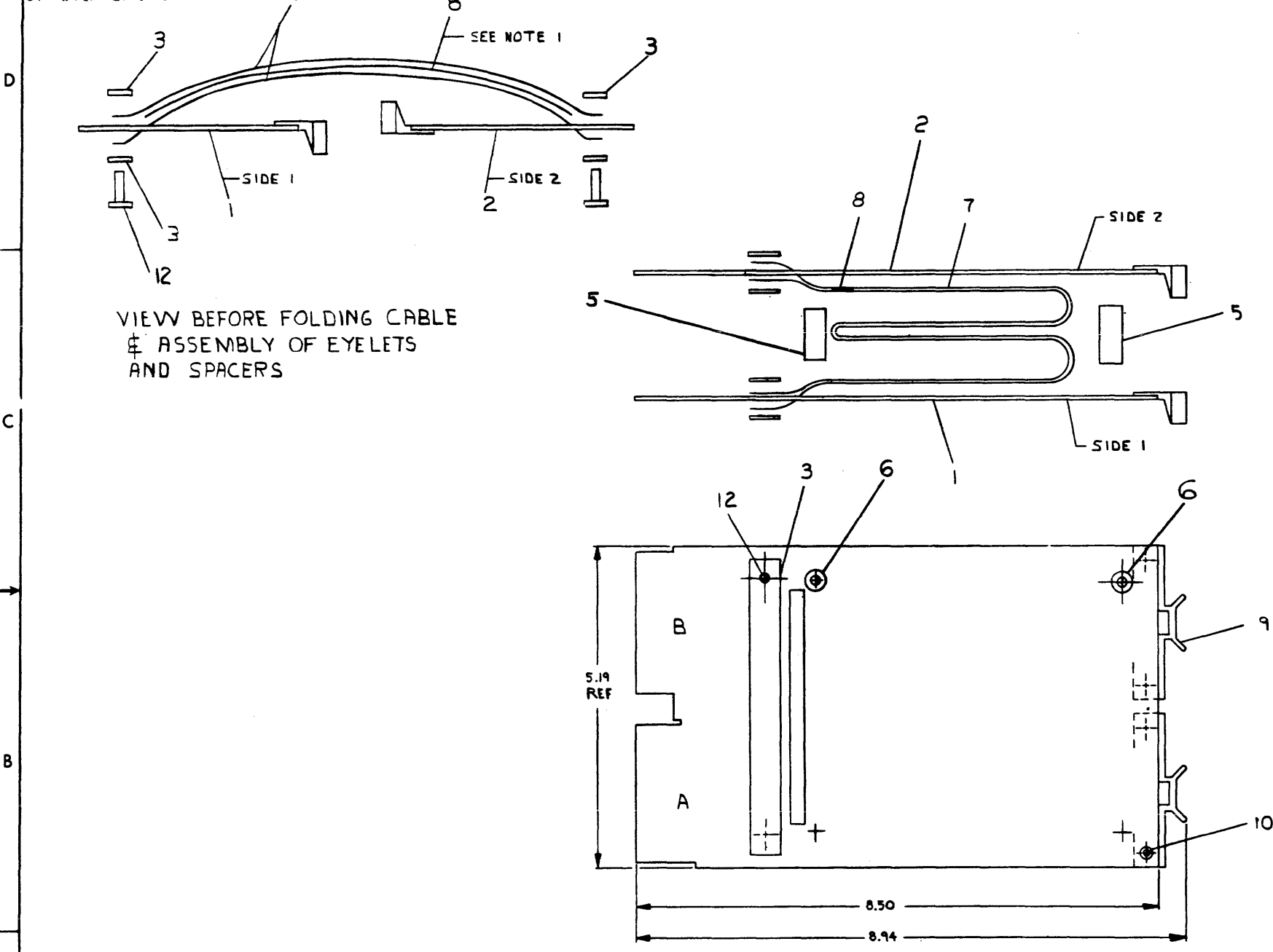


REV.	DATE	BY	CHK'D
1	5/13/75	WJ/ST	C
2	5/21/75	R. CIESLOK	

DRN. B. Chisner	30 July 75	FIRST USED ON	
CHK'D		TITLE	11/750 CONTROL PANEL
ENGR. R. Chisner	2/21/80	SIZE	D CS
PROD. ENGR. D. Cane	3/21/80	NUMBER	5413795-0-1
PROD. ENGR. J. Condit	3/21/80	REV.	C
NEXT HIGHER ASBY.		SCALE	
DD-5413795-00		SHEET	1 OF 1

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- NOTES:
1. FOAM (ITEM#8) TO BE PLACED BETWEEN CABLES (ITEM#7) AND ALL FOLDS.
 2. FLEX PRINTS TO BE BOUND TOGETHER BY WRAPPING WITH BLACK ELECTRICAL TAPE (ITEM#4) EVERY 8 INCHES.



CAUTION: OFF SHEET PARTS LIST EXISTS SEE K-PL-M9202-0-DBP

QTY.	DESCRIPTION	PART NO.	ITEM NO.
12	EYELET	9006745	12
A/R	TAPE, ADHESIVE TRANSFER	9009347-01	11
8	EYELET	9006752	10
4	HANDLE, FINE-CHIP MAGENTA	9009337-06	9
A/R	FOAM, GRAY	9008881	8
2	CABLE, FLEXPRINT (2' LG)	1700002-01	7
4	EYELET	9008223	6
4	SPACER	B-MD-7413488-0-0	5
A/R	BLACK ELECTRICAL TAPE	9009507	4
4	CLAMP, CABLE	B-MD-7413489-0-0	3
1	ETCHED CIRCUIT BOARD	D-CS-5411579-0-1	2
1	ETCHED CIRCUIT BOARD	D-CS-5411577-0-1	1

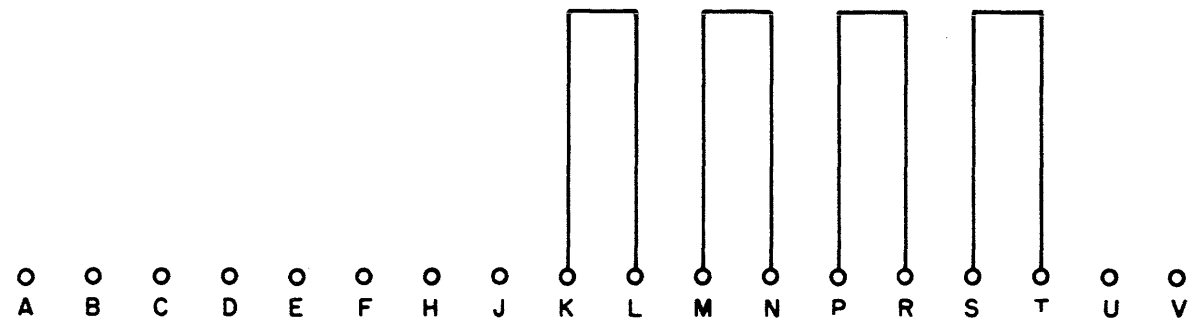
REVISIONS

REV.	CHANGE NO.	DATE	BY	CHK'D.
A	00001			
B	00002			
C	00003			
D	00004			
E	00005			
F	00006			
G	00007			
H	00008			
I	00009			
J	00010			
K	00011			
L	00012			
M	00013			
N	00014			
O	00015			
P	00016			
Q	00017			
R	00018			
S	00019			
T	00020			
U	00021			
V	00022			
W	00023			
X	00024			
Y	00025			
Z	00026			

FIRST USED ON OPTION/MODEL PDP/11	QTY.	DESCRIPTION	PART NO.	ITEM NO.
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES. TOLERANCES				
DECIMALS	ANGLES	PARTS LIST		
XXX - .005	± 0° 30'	digital EQUIPMENT CORPORATION		
XX - .002		MAYNARD MASSACHUSETTS		
X - .001		TITLE		
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY		CABLE, UNIBUS (JUMPER)		
MATERIAL	NEXT HIGHER ASSY.	SIZE CODE	NUMBER	REV
SEE PARTS LIST		DUA	M9202-0-0	C
FINISH	SCALE NONE	SHEET	1 OF 1	

REV. NUMBER 6727-0-1 SIZE CODE B CS

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REVISIONS CHK CHG NO. REV.	DRN. <i>BUTLER</i> DATE <i>11-19-69</i>	TRANSISTOR & DIODE CONVERSION CHART				digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS	TITLE GRANT CONTINUITY G727			
	CHK'D <i>P. J. Smith</i> DATE <i>6-FC-1370</i>	DEC	EIA	DEC	EIA		SIZE B	CODE CS	NUMBER G727-0-1	REV.
	ENG. <i>P. E. Johnson</i> DATE <i>1/28/70</i>						PRINTED CIRCUIT REV. <i>A</i>			
	PROD. DATE									