

The two-board design of Data General's Nova® 4/X computer incorporates CPU, MMPU, async interface, real-time clock, automatic program load, and power fail/auto restart on the CPU board, and up to 256K bytes of memory on a single memory board.

MANAGEMENT SUMMARY

Introduced in December 1978 as the eventual replacement for the Nova 3, Data General's Nova 4 series computers continue to be a significant entry in the traditional 16-bit minicomputer arena. The Nova 4 series consists of a family of three computers that feature "up to 50 percent faster speed than the Nova 3, improved reliability/maintainability, and greater economy than ever available in the Nova line."

Nova 4 models include the compact Nova 4/C, featuring a complete 64K-byte computer on a single circuit board; the standard Nova 4/S, a higher-performance model with up to 64K bytes of memory implemented on two boards; and the extended-memory Nova 4/X, which features a standard memory management and protection capability, allowing a 256K-byte computer to reside on two boards.

All three Nova 4 computers offer significant enhancements over the Nova 3 line in performance, function, and economy. The Nova 4/C packs the speed of a Nova 3 computer on a single board, and the Nova 4/S and 4/X models feature 50 percent higher speed than the Nova 3. Increased performance also results from instruction set enhancements, which add new byte

The Nova 4 series of computers consists of the compact, single board Nova 4/C; the higher-performance Nova 4/S; and the extended-memory Nova 4/X computer. There are also five packaged system models based on the Nova 4/C. Prices range from \$2,940 for the Nova 4/C, to \$15,015 for the Nova 4/X

MAIN MEMORY: 32K to 256K bytes DISK CAPACITY: 96M to 400M bytes

WORKSTATIONS: Up to 16 PRINTERS: 180 cps to 900 lpm

OTHER I/O: Magnetic tape, card readers,

paper tape reader/punch, plotters

CHARACTERISTICS

MANUFACTURER: Data General Corporation, Route 9, Westboro, Massachusetts 01581. Telephone (617) 366-8911.

Data General is a leading manufacturer of minicomputers, peripherals, and associated equipment. The company maintains sales offices in most major North American cities and in South America, Europe, and Australia. Manufacturing operations are located in Southboro, Massachusetts; Westbrook, Maine; Portsmouth, New Hampshire; Clayton, North Carolina; and Sunnyvale, California. Assembly operations are also performed in Hong Kong and in Thailand.

MODELS: Nova 4/C, 4/S, and 4/X.

DATE ANNOUNCED: December 1978.

AVAILABILITY: 90 days for all three models.

DATA FORMATS

BASIC UNIT: 16-bit word or 8-bit byte.

FIXED-POINT OPERANDS: 16-bit words can be interpreted as signed or unsigned binary numbers, logical words, memory addresses, or portions of decimal character strings.

Decimal numbers can be either character decimal or packed decimal. In character decimal format, each digit is an 8-bit ASCII character, and the sign is either carried separately as an extra character at the beginning or end of the decimal string or by modifying either the first or last digit in the string. The packed decimal format places each digit in 4-bit hexadecimal code with a separate sign character at one end of the string.

FLOATING-POINT OPERANDS: All Nova processors can implement single- and double-precision floating-point arithmetic through software subroutines. With the optional Floating-Point (FPU) on the 4/S and 4/X, single-and double-precision arithmetic can be handled by the hardware.

INSTRUCTIONS: One-word instructions. There are four basic instruction types; each with a different format: Jump and Modify Memory, Move Data, I/O, and Arithmetic and Logic. The instruction repertoire includes the Nova 3

CHARACTERISTICS OF THE DATA GENERAL NOVA 4 FAMILY

	Nova 4/C	Nova 4/S	Nova 4/X
Maximum number of terminals	4	8	16
Standard disk capacity, megabytes	96	200	400
Main memory capacity, bytes: Minimum Maximum	32K 64K	32K 64K	128K 256K

manipulation and integer arithmetic capabilities to the Nova 3 instruction set.

The Nova 4/X can support dual operations under the RDOS operating system. This capability allows it to run a multi-terminal data entry program in the foreground while processing a remote job entry or local batch program in the background.

The Nova 4/C is a 15-inch single board Nova-series computer with asynchronous interface, auto program load, power fail/auto restart and up to 64K bytes of MOS memory. It features a choice of 16K, 32K, or 64K bytes of MOS memory on a CPU board with 400-nanosecond memory cycle time; multiply/divide and real-time clock options; enhanced microprogrammed architecture with Nova-series instruction set; virtual console with power-up diagnostics and debugging; and a battery backup unit option that supports memory for up to 90 minutes.

The Nova 4/S is a 15-inch single board processor with onboard asynchronous interface, real-time clock, auto program load, and power fail/auto restart. The 4/S features a high-performance Nova-series computer including prefetching processor for instruction look-ahead; enhanced microprogrammed architecture with Novaseries instruction set; four-way interleaved 32K byte and 64K byte MOS memory modules with 400-nanosecond cycle time; virtual console with power-up diagnostics; 16-slot chassis providing maximum configurability or optional 5-slot chassis for small configurations; and multiply/divide (on CPU board), floating-point unit (FPU) and battery backup unit options.

The extended-memory Nova 4/X is a single 15-inch board with onboard asynchronous interface, real-time clock, auto program load and power fail/auto restart. The 4/X features a high-performance Nova-series computer including prefetching processor instruction lookahead; standard memory management and protection unit, implemented on the CPU board, for utilization of up to 256K bytes of memory; enhanced microprogrammed architecture with Nova-series instruction set; four-way interleaved 64K, 128K, and 256K byte MOS memory modules with 400-nanosecond cycle time; virtual console with extensive power-up diagnostics; 16-slot chassis providing maximum configurability or optional 5slot chassis for small configurations; and hardware multiply/divide (on CPU board), floating-point unit and battery backup options.

instruction set plus local byte, store byte, and signed multiply and divide.

In the Jump and Modify instructions, bits 3 and 4 identify the specific function (op code), and the rest of the word contains information used to calculate the effective address. This information consists of an 8-bit displacement, a 2-bit index register specification, and a 1-bit indicator to specify direct or indirect addressing. In Move Data instructions, bits 3 and 4 address an accumulator, the op code is in bits 1 and 2, and the rest of the word is identical in structure to the Jump and Modify type above. For I/O instructions, bits 5-9 specify the function (indication of transfer direction, selection of an I/O device register, and/or specification of an operation). Bits 3 and 4 select an accumulator for transfer, and bits 10-15 indicate a specific device. Arithmetic and Logic instructions use bits 1 and 2 to identify an accumulator containing a second operand (if present), bits 5-7 to specify primary function, and the rest of the word to specify secondary functions, if any (such as shift and skip test).

For all memory reference instructions, bits 5-15 are used for addressing, using bits 8-15 as the displacement or direct address. Each instruction can address 256 words directly, or can use either relative or base register addressing.

INTERNAL CODE: ASCII and binary.

MAIN STORAGE

TYPE: 4K-chip MOS RAM for 64K-byte memory increments, 16K-chip MOS RAM for 128K- and 256K-byte memory increments; both chips employ N-channel, silicongate technology.

CYCLE TIME: 400 nanoseconds, with four-way memory interleaving on the 4/S and 4/X.

CAPACITY: 16,384 to 65,536 bytes for the Nova 4/C; 32,768 to 65,536 bytes for the Nova 4/S; and 131,072 to 262,144 bytes for the Nova 4/X with MMPU (memory management and protection unit).

CHECKING: None.

STORAGE PROTECTION: A memory management and protection unit (MMPU) is available on the Nova 4/X for expanding the memory capacity to 256K bytes, protecting memory, and restricting physical-level I/O device access from user programs. The MMPU divides main memory into 2K-byte pages, and can protect individual pages through software support under the Real-Time Disc Operating System (RDOS).

In addition to the storage protection, the Nova MMPU also provides three other forms of system protection: I/O protection, validity protection, and runaway defer protection. I/O protection prevents the use of any specified I/O device; any instruction attempting to address the protected device causes a system interrupt. Validity protection is used in conjunction with memory mapping and can be extended to any 1024-word page in memory by mapping it to page 127;

PERIPHERALS/TERMINALS

DEVICE	DESCRIPTION
MAGNETIC TAPE EQUIPMENT	
6026	Subsystem includes Transport, 9-track, 75 ips, 800/1600 bpi, switch-selectable, industry compatible; includes control for light transport
6026-A 6027	Transport; 9-track, 75 ips, 800/1600 bpi, switch-selectable, industry compatible Subsystem includes 9-track transport, 75 ips, 800 bpi, switch-selectable, industry compatible; includes control for eight transports
6021	Subsystem includes transport, 9-track, 75 ips, 800 bpi, switch-slectable, industry compatible; includes control for eight transports
6023	Transport; 9-track, 800 bpi, switch-selectable, industry compatible
PRINTERS	
6073	DASHER LP2 line printer; 180 cps, logic-seeking, 7 x 9 impact dot matrix, 132 cpl, selectable 6/8 lines per inch, top of form control, horizontal and vertical tabs, and expanded printing
6074 6086	Same as 6073 except with condensed and expanded/condensed printing DASHER LP2 line printer subsystem; 180 cps logic-seeking, bidirectional 7 x 9 impact dot matrix, 132 cpl, selectable 6/8 lines per inch, top-of-form control, horizontal and vertical tabs and expanded printing; includes programmed I/O controller
6087	Same as 6086 except with condensed and expanded/condensed printing
6088	DASHER LP2 data channel printer subsystem; 180 cps logic-seeking bidirectional, 7 x 9 impact dot matrix, 132 cpl, selectable 6/8 lines per inch, top-of-form control, horizontal and vertical tabs, and expanded printing; includes line printer and data channel controller
6089	Same as 6088 except with condensed and expanded/condensed printing
4323	300 lpm DG/band printer, 64 upper case character set; includes DAVFU and forms length selector switch
4324	230 lpm DG/band printer; 96 upper/lower case character set; includes DAVFU and forms length selector switch
4325	300 lpm programmed I/O DG/band printer subsystem; 64 upper case character set; includes DAVFU and forms length selector switch; controller and cables included
4326	230 lpm programmed I/O DG/band printer subsystem; same as 4325 except includes 96 upper/lower character set
4327	300 lpm data channel DG/band printer subsystem; 64 upper case character set; includes DAVFU and forms length selector switch; D/CH controller and cables includes
4328	230 Ipm data channel DG/band printer subsystem; same as 4327 except includes 96 upper/lower case character set
4244	900 ipm line printer subsystem; 136 columns, 64 ASCII character set, 6-part form capability, 6 or 8 lines per inch; includes line printer, data channel controller, static eliminator, direct-access vertical format unit, active ribbon control, paper receptacle and cable
4245	Same as 4244 except 660 lpm printer and 96 ASCII character set
4315	Same as 4244 except includes 600 lpm printer
4216	Same as 4244 except 436 lpm and 96 ASCII character set
4218	Same as 4244 except includes 300 lpm printer
4219 4217	Same as 4244 except 240 lpm and 96 ASCII character set Optional programmable interval timer (PIT)
4034	Line printer control for 4034C, DASHER LP2 or 4034C, 4034D printers
4034C	Serial matrix printer; 165 cps, 10 cpi, up to 132 cpl; character formation is with a 4 x 7 matrix
4034D	Same as 4034C except character formation is with 7 x 9 matrix
4193	Line printer control for 4034G, 4034H printers
4034G	300 lpm line printer; 136 columns, 64 ASCII character set, 6-part form capability, 6 or 8 lines per inch (switch-selectable) static eliminator standard
4034H	240 lpm line printer; 136 columns, 96 ASCII character set, 6-part form capability, 6 or 8 lines per inch (switch-selectable) static eliminator standard
CARD READERS	
4306	Buffered card reader controller for 4016D-4016J card reader
4016D	Medium-speed punched card reader, 285 cpm
4016F 4016I	High-speed punched card reader, 600 cpm Medium-speed mark sense card reader, 285 cpm
PAPER TAPE READER/PUNCH	
4007	I/O interface subassembly; must be ordered with real-time clock, asynchronous line controller, paper tape reader control and/or paper tape punch control
4011	Paper tape reader control for 6013 reader
6013	High-speed paper tape reader; 400 cps, fanfold, 8-channel tape, rack mountable Paper tape punch control for 4012A and 4012B paper tape punch
4012 4012A	High-speed paper tape punch; 63.3 cps fanfold, 8-channel paper tape, slide mountable in a 19-inch rack
4013	Remote-operation modification to punch
4012B	High-speed paper tape punch for use with 4013 remote operations modification
PLOTTERS	
4014	I/O interface subassembly; must be ordered with incremental plotter control, and/or line printer control
4017	Incremental plotter control for all 4017 series plotters
4017E 4017E-A	Incremental plotter (Z-fold paper); 11-inch paper, 0.005-inch step size (450 steps per second) Incremental plotter (Z-fold paper); 11-inch paper, .010-inch step size (400 steps per second)
4017E-B	Incremental plotter (Z-fold paper); 11-inch paper, .010-inch step size (450 steps per second)
4017E-C	Incremental plotter (Z-fold paper); 11-inch paper, .25mm step size (400 steps per second)

PERIPHERALS/TERMINALS

DEVICE	DESCRIPTION
TERMINALS	
6106	DASHER D100; detached keyboard, 9600 bps character upper, lower case characters, and 24 lines x 80 character screen with 7 x 11 dot matrix format
6107	Same as 6106 except includes EIA printer interface for attaching DASHER TP1 or TP2 printer for hardcopy of screen contents
6108	DASHER D200; detached keyboard, 9600 bps, 96 character upper/lower case characters, and 24 line x 80 character screen with 7 x 11 dot matrix format
6109 6052	Same as 6108 except includes EIA printer interface for attaching DASHER TP1 or TP2 printer for hardcopy of screen formats DASHER D1 alphanumeric video (CRT) display terminal; detachable keyboard, 64 character set (ASCII upper case), and 24 line x 80 character screen with 5 x 7 dot matrix characters
6053	Same as 6052 except includes 96 character set
6093	DASHER D3 display; CRT with detachable typewriter keyboard, 19.2K bps, 15 key data entry pad, 18 function keys, 96 character set, direct cursor positioning and sensing, programmable intensity plus blink, underscore, reverse video, block file and field protect
6083	DASHER D2 display subsystem; 9600 baud, typewriter style keyboard with real-time clock, console interface and cable
6084-A	Same as 6083-A except includes foreground console
6085-A 6042	DASHER D1 display subsystem; 9600 baud teletype style keyboard with real-time clock, console interface and cable DASHER TP1, 30 cps, keyboard send/receive terminal, 128 character ASCII upper and lower case, and buffered carriage return for continuous 30 cps operation
6043	Same as 6042 except receive only without keyboard
6040	DASHER TP1, 60 cps terminal printer, 132 column dot matrix, typewriter style keyboard, and 128 character ASCII upper and lower case
6041	Same as 6040 except receive only without keyboard
6080-A	DASHER TP1 printer subsystem; 60 cps, with real-time clock, console interface, and cable
6081-A	Same as 6080-A except includes foreground console
6082-A	Same 6080-A except 30 cps
6075	DASHER TP2 receive-only terminal printer; 180 cps logic-seeking, bidirectional, 7 x 9 impact dot matrix, 132 cpl
6076	Same as 6075 except with condensed and expanded/condensed printing
6077 6078	DASHER TP2 keyboard send/receive terminal printer; 180 cps logic-seeking, bidirectional 7 x 9 impact dot matrix, 132 cpl Same as 6077 except with condensed and expanded/condensed printing

Although processor options and configuration rules are not the same among various members of the Nova family, all employ the same basic 16-bit architecture, with four accumulators for computational use (two of which can be used for index registers), an I/O Bus, either a standard or high-speed Direct Memory Access (DMA) data channel, common 15-inch square PC board packaging design, and strong communications capabilities. The actual number of devices that can be configured with any Nova system depends upon the number of available plug-in circuit board "slots" in the chassis.

The microprogrammed Nova 4 line incorporates most of the architectural features of the Nova 3 computers, including hardware stack and frame pointer, high-speed direct memory access (DMA) channel, and 16-level priority interrupt structure. The Nova 4/S and 4/Xmodels have a number of advanced performance features. A pre-fetch processor boosts performance by storing instructions in a high-speed buffer in the CPU. eliminating the memory fetch cycle in most cases. Highspeed 400-nanosecond memories are accelerated with standard four-way interleaving, allowing the pre-fetch processor to load instructions at a 20-megabyte/second rate and reducing memory conflicts on memory modification instructions. These features result in typical instruction execution times of 400 nanoseconds for a store and 200 nanoseconds for an add. Performance can also be increased with the optional Floating-Point Unit (FPU), which operates in parallel with the CPU and is considerably faster than previous Nova FPU's. For example, the FPU executes a doubleprecision store in 200 nanoseconds and a doubleprecision add in 1.6 microseconds.

➤ this indicates that the page is protected and, if addressed by any means, causes a system interrupt. Runaway defer protection prevents infinite indirect loops by counting the number of consecutive times a particular address is referenced through a defer (indirect) cycle; if addressed 17 times consecutively, a system interrupt is generated.

The MMPU holds two program maps and two data channel maps at the same time. Each map consists of 32 1K pages. Although both data channel maps can be enabled at the same time, only one program map may be enabled.

RESERVED STORAGE: Certain low-end memory locations are reserved for use during interrupt servicing, MMPU protection processing, stack fault processing, and power failure. Sixteen locations are reserved for increment and decrement registers.

CENTRAL PROCESSORS

GENERAL: The Nova 4 high-density architecture allows a 64K-byte Nova 4/C to be configured on one board, and a 64K-byte Nova 4/S or 256K Nova 4/X to be configured on two boards. The Nova 4s feature Nova 3 stack capabilities, similar to those of the Eclipse series. The operation of the push-down stack is controlled through two hardware registers, the stack pointer and the frame pointer.

The Nova 4/C is housed in a 5-slot chassis, and the Nova 4/S and 4/X are housed in a 16-slot chassis. The Nova 4's use a horizontally microcoded CPU implementation for overlap, and a look-ahead prefetch processor also increases performance.

Processor options for all Nova 4 models include hardware multiply/divide and a real-time clock. The latter attaches and operates like a peripheral device. An optional hardware floating-point arithmetic unit (FPU) is available for the 4/S and 4/X. The Nova 4 FPU is faster than previous Nova FPU's, executing a double-precision store in 200 nanoseconds and a double-precision add in 1.6 microseconds. An optional battery back-up system can support a single

➤ Memory boards are available in 32K- and 64K-byte increments for the Nova 4/S and in 32K-, 64K-, 128K-, and 256K-byte increments for the 4/X. The Nova 4/C is available with 32K or 64K bytes of memory on the CPU board. Memory management and protection hardware in the Nova 4/X performs logical-to-physical address translation, providing user programs with access to 256K bytes of main memory through four address extension tables, or maps: two program maps and two data channel maps. The Memory Management and Protection Unit (MMPU) also supports privileged instructions, I/O device protection, and main memory write and validity protection. This allows multi-user activities to occur in a hardware-protected environment.

All Nova 4 instructions are one 16-bit word in length. The basic instruction set contains instructions that perform fixed-point arithmetic and logical operations between accumulators, transfer of operands between accumulators and main memory, transfer of program control, and I/O operations.

A push-down stack provides a convenient method for saving return information and passing arguments between subroutines. The stack also provides an expandable area for the temporary storage of variables and intermediate results.

The floating-point feature allows the manipulation of both single-precision (32 bits) and double-precision (64 bits) floating-point numbers. Floating-point calculations can take place between two 64-bit accumulators or between one of the accumulators and operands in main memory.

The optional Memory Allocation and Protection (MAP) feature performs logical to physical address translation using two user maps and two data channel maps. Only one user map can be enabled at any one time, but both data channel maps are enabled at the same time. In addition to translating addresses, the feature also performs various protection functions. A user is allowed to access only those blocks of memory allocated to him. The MAP also detects and inhibits indirect chains that go deeper than 16 levels, and provides I/O protection which allows all I/O devices to be declared accessible or inaccessible to a user.

The Nova 4 I/O bus is made up of a 6-line device selection network, interrupt circuitry, command circuitry, and 16 data lines. Each I/O device is connected to the device selection network in such a way that it will only respond to commands that contain its own device code. There are 54 device codes available. The interrupt circuitry of the I/O bus contains the capability to implement up to 16 levels of priority interrupts through a 16-bit priority mask.

Data General offers a comprehensive array of I/O devices for the Nova 4 computers, ranging from teletypewriters to line printers to video displays for man-machine interaction; and from paper tape to board of memory for 30 minutes on the 4/C and for 90 minutes on the 4/S and 4/X. Recharge time from the fully depleted state is 24 hours during normal CPU operation. One to two milliseconds are provided for the execution of a save routine during a power failure.

CONTROL STORAGE: None.

REGISTERS: All Nova 4 processors have four 16-bit accumulators and a 15-bit program counter. Two accumulators can be used for address indexing. The Nova 4 computers also have a last-in/first-out (LIFO) push-down stack implemented in any 256 consecutive memory locations and two additional hardware registers, the stack pointer and the frame pointer. The stack pointer identifies the first memory location designated as the stack, and the frame pointer marks intra-stack boundaries to permit several "register saves" to be accumulated in the stack.

The MMPU dual program maps are composed of 32 registers, each register controlling a 1K word space. The same is true for the 64 data channel map registers.

Beyond these hardware registers, Nova processors also have 16 reserved memory locations which function as autoincrement or auto-decrement registers when addressed indirectly.

ADDRESSING MODES: Direct addressing of 1024 words via absolute, relative, and indexed modes; multilevel indirect addressing of 32,768 words; stack addressing on a last-in, first-out basis and on a random-indexed basis.

When the MMPU is implemented, the 15-bit logical address coming from the CPU or data channel is translated to a 17bit physical address. Memory access cycle time is unchanged.

The mapping information needed to service a CPU or data channel request is given to the address translation hardware by the operating system through I/O instructions that reference the address translation hardware. This information is transmitted before the supervisor enables either the user map or the data channel map.

All addresses can be mapped, including those acquired from DMA controllers.

INSTRUCTION REPERTOIRE: All Nova 4's have the same basic complement of 4 Jump and Modify Memory instructions, 2 Move Data instructions, 2 byte manipulation instructions, 7 stack processing instructions, 16 I/O instructions, and 8 arithmetic and logic instructions. (There are 256 variations on each of the arithmetic and logic instructions.) Hardware multiply/divide instructions and a floatingpoint unit are available as options.

The optional Floating-Point Unit adds 31 I/O-format instructions to the basic set; this option is not available for the Nova 4/C. The 4/X MMPU adds 10 I/O-format instructions.

INSTRUCTION TIMINGS: The following times are the averages of the maximum and minimum values, in microseconds, for Nova 4 processors with the standard 400nanosecond NMOS memory.

Load/Store:	0.8/0.4
Add/Subtract:	0.2/0.2
Multiply/Divide	4.4/5.4
Compare and Branch:	0.2/1.2

INTERRUPTS: Each I/O device is wired to one of 16 bus positions, and can be either authorized or denied authorization to interrupt particular service routines by an Interrupt



magnetic tape to fixed- and moving-head disks for data storage. Also available are various multiplexers and telecommunications adapters, including an IBM 360/370 interface.

The Nova series features four operating systems—DOS, RTOS, RDOS/Mapped RDOS, and MP/OS. DOS, the Disc Operating System, is diskette-based, while RTOS, the Real-Time Operating System, is memory-based. Both DOS and RTOS are compatible subsets of RDOS, the Real-Time Disc Operating System, which is hard discbased. Four high-level languages are available for the Nova Series: single- and multi-user Extended BASIC, Business BASIC, FORTRAN IV (with ISA real-time extensions), and optimizing FORTRAN 5. An extended assembler, macro assembler, symbolic debugger, communications package, and a library of utilities—sort/ merge, plotting routines, commercial subroutines, and text editor—are also available. MP/OS supports the MP/Pascal and MP/FORTRAN IV programming languages.

In the area of data communications, Data General provides RJE80, the Remote Job Entry Control Program; HASP II, the remote batch terminal emulator; SAM, the Sensor Access Manager; and CAM, the Communications Access Manager. RJE80 and HASP II allow for remote job entry and communications between Nova processors and IBM 360/370 systems (in IBM 2780/3780 mode), or between Nova processors and other Data General computers. CAM supports all types of communications and is designed to work with such hardware units as the DCU/50 Data Control Unit and various asynchronous and synchronous multiplexers.

Data General provides its own maintenance and field support service through about 93 field service centers distributed nationwide, which employ more than 500 service personnel. Five depot locations—Framingham, Massachusetts; El Segundo, California; Toronto, Canada; Frankfurt, Germany; and London, England—provide comprehensive repair facilities.

With the growing popularity of small business computer systems, many turnkey systems vendors now offer custom solutions to business problems based on Nova series minicomputers.

Data General feels that in computational environments such as semiconductor testing, the economy and packaging of the Nova 4/C makes it well suited for incorporation into automated test equipment. The Nova 4/S is aimed at higher-performance applications, such as front-end processing in image processing systems. A processing system of this type could be expanded by adding a Multiprocessor Communications Adapter (MCA) for linking with other Data General Nova or Eclipse computers. The Nova 4/X supports dual operations under RDOS in areas such as retail or commercial applications. The 4/X can run a multi-

➤ Disable Mask Bit that corresponds to the bus position of the device. I/O devices that operate at similar speeds are normally connected to the same mask bit. User-written routines can recognize and process up to 16 levels of priority interrupts.

PROCESSOR MODES: The Nova 4/X recognizes either a supervisor or user mode of program execution for use with the memory allocation and protection options. The executive program runs in the supervisor mode, and can write-protect portions of each user's memory area. With this option and operating in user mode, no user can write in a protected area, use more than 16 levels of indirect addressing, or issue I/O instructions.

The Nova 4/X provides four memory maps: two program maps and two data channel maps, each consisting of 32 2K-byte pages. All addresses can be mapped, including those acquired from DMA controllers.

PHYSICAL SPECIFICATIONS: All Nova 4 processors are housed in either 5.25-inch (5-slot) or 10.5-inch (16-slot) high chassis which are 19 inches wide and approximately 28 inches deep. Weights vary from 40 to 130 pounds fully loaded. The expansion chassis weighs 130 pounds fully loaded. Four AC line voltages are available: 100v, 120v, 220v, or 240v, all operable within the range of 47 to 63 Hz at maximum load. Power consumption is 1200 watts for a fully loaded 16-slot chassis. Recommended operating environment for the Nova 4 computer systems is between 68° F (20° C) and 86° F (30° C), but temperatures between 32° F (0° C) and 130° F (55° C) can be tolerated. Non-condensing humidity of up to 99% can be tolerated.

INPUT/OUTPUT CONTROL

One (4/C) or two (4/S and 4/X) memory buses and a Direct Memory Access (DMA) channel are standard on all Nova 4's. Various high-speed options are available. The DMA data channel provides a multiplexer-like capability and can be seized by any device through a data channel request to handle 16-bit data transfers to and from main memory. In high-speed mode, the maximum word transfer rates per second are: Nova 4/C—625K in, 500K out; Nova 4/S and 4/X—1000K in, 710K out.

CONFIGURATION RULES

The actual number of devices that can be attached to a particular Nova depends upon the available slots in the basic chassis and any available chassis extensions.

All processors occupy one slot. Any size memory module also occupies one slot. The price list at the end of this report states the number of slots remaining after the processor and memory requirements for each particular configuration are satisfied. The multiply/divide feature and parity option share the same board as the CPU. The floating-point option requires one slot.

In general, all peripheral I/O interface subassemblies/controllers require one slot. These include applicable units for reel-to-reel magnetic tape drives, cassette drives, printers, punched card equipment, paper tape units, terminals, plotters, and disc units. Also requiring one slot each are the I/O interface subassembly for the bus control card, various communications controllers, and the DCU/50 and DCU/200 Data Control Units. The programmable interface to an IBM 360/370 requires two slots.

WORKSTATIONS: The maximum number of terminals configurable on the Nova computers are 4 on the 4/C, 8 on the 4/S, and 16 on the 4/X.

DISK STORAGE: See above.



terminal data entry program in the foreground while processing a remote job entry or local batch program in the background with full hardware and software protection.

USER REACTION

Seventeen users representing over 29 Nova 4 systems responded to Datapro's 1981 User Survey. The majority of the systems were Nova 4/Xs with an average life span of approximately 10.3 months; fifteen of those polled had purchased their systems and two leased from third parties. Types of industries responding to the survey included an educational institution, EDP consulting firm, manufacturing company, service bureau, insurance company, banking and finance corporation, and accounting firm. While nine of the respondents were first time users, four had converted to the Nova 4 from Nova 3 systems, two converted from the Basic/4, one from a Nixdorf system, and one from a Control Data Corporation system. A variety of applications were performed on these Nova 4 systems. The most frequently mentioned were accounting/ billing, order processing/inventory control, sales/distribution, payroll/personnel, and manufacturing. Their application programs came mostly from in-house personnel and proprietary software. The majority of systems were located in a central processing installation as opposed to a distributed processing site.

Memory capacities ranged from between 32K bytes to as high as 2M bytes. Total disk storage ranged from 1M bytes to less than 600M bytes. The operating system used most often was RDOS, and only two users had installed data base management systems. Four users had communications monitors, and of those four, three used the manufacturer's package, and one used their own "homegrown" system. Seven of the respondents had integrated word processing functions on their computers. The primary programming language used was BASIC, followed by COBOL and FORTRAN. Of the seventeen users questioned, fifteen said they did not expect to replace their systems in 1981, and two said yes but with the same manufacturer. These users rated their Nova 4 computers as shown in the table below.

	Excellent	Good	Fair	Poor	WA*
Ease of operation	9	6	2	0	3.4
Reliability of mainframe	7	10	0	0	3.4
Reliability of peripherals	3	11	3	0	3.0
Maintenance service:					
Responsiveness	4	8	5	0	2.9
Effectiveness	3	9	5	0	2.8
Technical support:					
Trouble-shooting	3	8	5	1	2.7
Education	2	8	3	2	2.6
Documentation	3	6	5	3	2.5
Manufacturer's Software:					
Operating system	4	11	2	0	3.1
Compilers and Assemblers	3	9	2	0	3.0
Applications programs	2	5	0	0	3.2
Ease of programming	6	7	2	0	3.2
Ease of conversion	8	2	2	0	3.5
Overall satisfaction	5	10	2	0	3.1

*Weighted Average on a scale of 4.0 for Excellent.

► MAGNETIC TAPE: See above.

PRINTERS: See above.

MASS STORAGE

All Data General disc subsystems can be accessed by two controllers to allow dual-processor, shared-disc systems.

6030 FLOPPY DISC SUBSYSTEM: Consists of a four-drive controller and either a 6030 single drive or a 6031 dual drive. Each floppy disc stores up to 315K bytes on 77 tracks. Maximum storage capacity is 1.26 million bytes on a four-drive subsystem. Average head positioning time is 260 milliseconds, and average rotational delay is 83 milliseconds. Data transfer rate is 31K bytes/second. The 6030 drives feature IBM 3740 compatibility and are supported by Data General's RDOS operating system. The controller occupies one slot. The 6030 drives are manufactured by Data General.

6063 AND 6064 FIXED-HEAD DG/DISC SUBSYS-TEMS: Consist of a four-drive controller plus up to four 1-megabyte 6063-A disc drives or up to four 2-megabyte 6064-A drives. Each sector contains 512 bytes. There are 32 sectors per track and either 32 tracks per surface (6063) or 64 tracks per surface (6064). Either subsystem can be expanded with 1- or 2-megabyte drives for a total of from 4 to 8 megabytes.

Average rotational delay is 10.22 milliseconds, and the data transfer rate is 910K bytes per second. The pack requires 10 seconds to come up to speed initially and 6 seconds to stop for removal.

6060, 6061, AND 6067 DG/DISC STORAGE SUBSYS-TEMS: Consist of a 96-, 190-, or 50-megabyte disc pack drive and a controller for up to four drives. Thus, the same controller can handle from 50 to 760 megabytes of on-line storage. The drives are 3330-type units designed and manufactured by Data General at its Westbrook, Maine, facility. Model 6060 is a 96-megabyte drive, Model 6061 is capable of storing 190 megabytes, and Model 6067 is a 50-megabyte drive. Data density is 4040 bits per inch for all three drives, although their track densities are different. The drives employ a servo track-following technique that allows 192 tracks per inch for a total of 411 tracks per surface on the Model 6060, and 370 tracks per inch for a total of 815 tracks per surface on the Model 6061 and 6067. There is a total of 411 or 815 cylinders, each containing 19 tracks on the 6060 and 6061, or 5 tracks on the 6067.

The Model 6060 employs an IBM 3336-type pack, while the Model 6061 and 6067 utilize an IBM 3336-11-type pack. The 6060 and 6061 disc packs contain 10 platters with 19 usable surfaces, while the 6067 contains 3 platters with 5 usable surfaces. There are 24 sectors per track and 512 bytes per sector, yielding 12,288 bytes per track. Total surface capacity is either 5,050,368 or 10,014,720 bytes per surface, depending on the model. Total formatted drive capacity is 95,956,992 bytes for Model 6060, 190,279,680 bytes for Model 6061, and 50,073,600 bytes for Model 6067.

Disc rotational speed is 3600 rpm, and average rotational delay is 8.3 milliseconds. Track-to-track head positioning time is 6 milliseconds, average head positioning time is 35 milliseconds, and average access time is 43.3 milliseconds.

The controller employed with these drives can handle up to four Model 6060, 6061, or 6067 drives in any combination. Software limitations restrict the number of controllers per system to two. The controller features independent command and read/write channels and reserve-and-trespass capabilities for users to transfer data. In multiple shared-disc environments, privileged file structures are allowed. An error correction feature makes it possible to detect and correct all error bursts of 11 bits or less.

➤ When asked to state the significant advantages of system, the most common answer was the system is easy to expand/reconfigure, followed by users are happy with response times, terminals/peripherals carried over from other systems are compatible as the vendor promised, and programs/data carried over from other systems are compatible as the vendor promised. While four users stated they had no significant problems with their computers, common negative statements from the others included the vendor did not provide all promised software or support, the computer proposed by the vendor was too small, the installation of equipment was late, and system costs (for hardware, vendor-supplied software, support) exceeded the expected total. All the users except two stated that the system did what it was expected to do and that they would recommend the Nova 4 to another

These drives replace the 92-megabyte 4231A and B units that were manufactured by Control Data Corporation and utilized by Data General in the past. Due to formatting differences, the 4231A and B drives may not be intermixed with the 6060 or 6061 drives on the same controller. The new drives can be configured into any Eclipse system supported by the RDOS or AOS operating system. The drives feature a transfer rate of 806K bytes/second.

6045/46/47/48 CARTRIDGE DISC SUBSYSTEMS: Each subsystem consists of a controller and up to four 10-megabyte, top-loading cartridge disc drives. The four systems are being manufactured at Data General's Westbrook, Maine, facility. These subsystems, configured with one, two, three, and four cartridge disc drives, are respectively designated the Model 6045, 6046, 6047, and 6048.

Each drive employs two platters, one fixed and the other an IBM 5540-type removable cartridge, both mounted on a common spindle. Each platter is capable of storing 5,013,504 bytes, or 2,506,762 bytes per surface. There are 200 tracks per inch, 408 tracks per surface, 408 cylinders per drive, and 4 surfaces per drive. Recording density is 2200 bits per inch. All tracks are divided into 12 sectors of 512 bytes each, yielding a formatted track capacity of 6144 bytes. Each cylinder consists of four tracks, giving a formatted cylinder capacity of 24,576 bytes. Total drive capacity is 10,027,008 bytes.

Drive rotational speed is 2400 rpm. Track-to-track, average, and full-stroke head positioning times are 8, 38, and 70 milli-seconds, respectively. The data transfer rate is 312,500 bytes per second. Drive start-up to full operating speed takes 30 seconds, and the drive requires 25 seconds to come to a full stop. All four subsystems are supported under the RDOS and RTOS operating systems.

An add-on drive, the Model 6050, allows field upgrading of already installed cartridge disc subsystems. Dual porting can be implemented by adding the Model 6051 option. The Model 6050-F is an add-on drive for an existing floppy disc subsystem. Any mix of floppy and cartridge disc drives up to a maximum of four is allowable. Likewise, floppy drives can be added to a previously installed cartridge disc subsystem. Diskette drives offer advantages in diagnostic program loading and compact file backup.

6070 CARTRIDGE DISC SUBSYSTEM: Consists of essentially the same components and specifications as the 6045 subsystem, except for a doubled recording density resulting in twice the storage capacity and a doubled transfer rate

6099 DG/DISC STORAGE SUBSYSTEM: Consists of a Winchester-type 12.5-megabyte drive and controller. Two read/write heads access both surfaces of the single disc. Track density is 166 per inch for a total of 348 tracks per surface, and data density is 5760 bpi. The access mechanism is stepper motor driven, controlled by a microprocessor velocity determination algorithm that optimizes head acceleration and deceleration. The 6099 disc controller is located in the Nova 4 CPU. The rotational speed is 2964 rpm, with a data transfer rate of 910K bytes per second. The 6099 allows up to 16 logically contiguous sectors to be transferred with one I/O command. It is fully supported by RDOS.

INPUT/OUTPUT UNITS

See Peripherals/Terminals table.

Date General is also an OEM peripherals supplier. Its OEM products are covered in Section M13 of *DATAPRO REPORTS ON MINICOMPUTERS*. Data General also provides a broad array of data acquisition, process control, and analog/digital I/O equipment.

COMMUNICATIONS CONTROL

The DG/CS Communications Subsystem is a hardware and software system that is modular in nature. It is composed of the ALM-16 and ALM-8 Series asynchronous multiplexers, the SLM-2 Series asynchronous multiplexers, the 4251 Communications Chassis, and the DCU/50 or DCU/200 Data Control Unit. The CAM Communications Access Manager provides software support for teletypewriter terminals and Bisync line protocol.

Low-to-medium-performance applications use the appropriate multiplexers in the 4251 chassis, which is connected directly to the host computer I/O bus. For high-performance applications, the DCU/50 is inserted as a "peripheral processor" between the host CPU and the communications subsystem.

The DG/CS supports both full- and half-duplex operation. Line interface support is provided for EIA RS-232C/CCITT V.24, 20-ma current loop, and 23-ma current loop.

The DG/CS is supported by the RDOS, mapped RDOS, and RTOS operating systems. Other software support includes Data General's HASP Work Station Emulator, DCU-resident physical I/O routines, and the aforementioned CAM.

DCU/50 DATA CONTROL UNIT: Provides a dedicated communications controller consisting of a Nova processor with a local 1K words of memory on a single card that plugs into the host computer chassis. A DMA interface to the host allows any of the host memory to be "windowed" into the DCU/50 address space above 4K to give the DCU access to the host processor's communications buffers without the need to interrupt the host. Communications with the various asynchronous and synchronous line multiplexers is via the DCU/50's I/O bus (separate from that of the host). Control of these multiplexers is in parallel with the main processor. The DCU/50 can be programmed for composite throughputs of up to 48K characters per second over up to 256 asynchronous or synchronous lines. Programming of the DCU/ 50 can also handle unique protocols and character processing functions. Multiple DCU/50's can be connected to a single Eclipse processor for increased communications

A single DCU/50 can provide 10,000 to 12,000 characters per second of communications throughput with an average of 10 percent computer overhead. The unit is normally used to perform all character-level interrupt processing, control



 character processing, code translation, error checking, and buffer maintenance.

DCU/200 DATA CONTROL UNIT: This unit has essentially the same capabilities as the DCU/50, except that it has 4K words of local memory compared to the DCU/50's 1K words, and a 400-nanosecond rather than a 300-nanosecond memory cycle. The DCU/200 acts as an intelligent front end to the processor for communications and sensor I/O applications, off-loading interrupt processing, line protocol, and error-control chores for greater system throughput.

4263/4264 SLM-2 SYNCHRONOUS LINE MULTI-PLEXER: Provides one or two lines with speeds of up to 9600 bps as a stand-alone unit or up to 56,000 bps in conjunction with the DCU/50 and an optional high-speed interface for Bell 303 Series modems. Line operation may be either full- or half-duplex. Full character buffering is offered.

Programmable line characteristics include parity type (even, odd, or none); 6-, 7-, or 8-bit code level; SYN (synchronous) and DLE (delete) characters; transmitter/receiver on or off; and either CRC-16 or CCITT-16 cyclic redundancy check (CRC) polynominals, if the optional CRC Generator/Checker is installed. The internal clock is jumper-selectable to one of eight frequencies from 300 bps to 56,000 bps. Modem control is offered for Bell 201, 203, 208, 209, and 303 or equivalent data sets. Modem control signals include carrier detect, data set ready, ring indicator, request to send, data terminals ready, and clear to send. The RS-232C/CCITT V.24 interface is standard, while an interface for a Bell 303 is optional.

Up to 32 synchronous lines can be supported with the DCU/50 or DCU/200 using 4263 dual-line SLM-2's. Off-line error detection and diagnostics are a standard feature.

4255/4256 ALM-8 ASYNCHRONOUS LINE MULTI-PLEXER: Offers a line speed of 5 to 9600 bps on a program-selectable basis. The 4255 offers eight lines, while the 4256 offers four. Full-character buffering is standard. Line operation may be full- or half-duplex. Full modem control, including automatic answer capabilities for Bell 103, 202, or equivalent data sets, is standard. Modem control signals include carrier detect, ring indicator, data set ready, request to send, data terminal ready, and clear to send.

Programmable line characteristics include parity type (odd, even, or none); 5-, 6-, 7-, or 8-bit code level; 1 or 2 stop bits; and the aforementioned line speed. Diagnostics and error detection on an off-line basis are standard. Up to 128 asynchronous lines can be supported with the DCU/50 or DCU/200 and the 4255's. The EIA RS-232C/CCITT V.24 interface is standard.

4257/4258 ALM-16 ASYNCHRONOUS LINE MULTI-PLEXER: Provides a line speed of 5 to 9600 bps on a program-selectable basis. The 4257 handles 16 lines, while the 4258 handles 8. Full-character buffering is standard. Line operation may be full- or half-duplex. Programmable line characteristics include parity type (odd, even, or none); 5-, 6-, 7-, or 8-bit code level; 1 or 2 stop bits; and the aforementioned line speed. Diagnostics and error detection on an offline basis are standard. Up to 256 asynchronous lines can be supported with either the DCU/50 or DCU/200 and any combination of the 4257 or 4258. Interfacing is by the 4260 4-line, 20-ma current loop module or the 4261 4-line, EIA RS-232C/CCITT V.24 module. Both modules can be intermixed on the same ALM-16 board. Interfacing is also available to a Bell 103 data set for manual answer-only operations.

4010 ASYNCHRONOUS LINE CONTROLLER: Provides a full-duplex current-loop interface for a local 33 ASR, 33

KSR, 35 ASR, or 35 KSR teletypewriter, a local 6012 video display, or a Bell 103 or equivalent data set through the 4023 EIA interface. The 4029 interface provides control for Bell 103, 202, or equivalent data sets with automatic or manual answer. Clock signals required for the 4023 interface are jumper-selectable. Ten standard frequencies between 75 and 9600 bps are available.

4023 ASYNCHRONOUS SINGLE-LINE CONTROLLER: Provides a full-duplex interface for a single Teletype Model 37ASR or 37KSR; 6012 CRT display; or Bell System 103, or equivalent, with manual answer. Standard rate is 150 bps for 10-or 11-unit codes. Other rates are optionally available. The 4029 option adds modem control features for Bell System 202 units with Automatic Answer.

4025 IBM 360/370 PROGRAMMABLE INTERFACE: Attaches to an IBM selector or multiplexer channel, and can simulate the IBM 2803, 2700 series, etc. Operates in multiplexed or burst mode at up to 150,000 bytes per second. A 4025 IBM Software Driver is provided for I/O programming. The 4025 can operate with RDOS or RTOS in a real-time environment. On-line and off-line diagnostics are provided.

4061 ASYNCHRONOUS MULTIPLEXER: Provides interfaces for up to 64 lines (4 lines per subassembly) at speeds from 45 to 9600 bits per second with 5- to 8-level codes. The 4061 is wired with junction panels to provide system expansion capabilities.

4206 MULTIPROCESSOR COMMUNICATIONS ADAPTER: Interconnects up to 15 Nova central processors into multiprocessor rings. The 4206 features simultaneous program-controlled data transmission and reception. Two jumper-selectable modes of operation are available: normal and fast. In normal mode, data is transferred at 625,000 bytes per second. In fast mode, up to 1 million bytes of data are transferred per second. Up to 15 computers are supported in normal mode, while a maximum of four can be sustained in fast mode. The 4206 is fully supported by RTOS and RDOS.

COMMUNICATIONS SOFTWARE

COMMUNICATIONS ACCESS MANAGER (CAM): Supports all types of communications, with or without the DCU/50 or DCU/200 Data Control Unit. SLM-2, ALM-8, and ALM-16 line multiplexers are supported under CAM, as well as the DCU units. CAM is modular and can be generated by the Communications System Generation Program (COMGEN) to include only those program segments required for each individual system. It operates under RTOS, RDOS, or mapped RDOS, and since it uses the operating system's runtime-defined interrupt service, it is brought into main memory from disc only as needed. This frees large segments of memory in a real-time communications system for other processing tasks. Portions of CAM can also reside in the memory of the DCU/50 or DCU/200, if either is present, further reducing main memory overhead.

CAM software can support both standard and special userdefined protocols, including Bisync (BSC) and an asynchronous terminal line procedure. Synchronous and asynchronous protocols can be intermixed. Multi-drop lines are supported through polling and selection sequences. Modem control support for auto answer/auto disconnect is a standard feature. CAM provides a queue for I/O completions that permits a single user task to control several asynchronous lines.

CAM also features a FORTRAN IV or FORTRAN 5 interface, permitting communications I/O in FORTRAN programs.



CAM operates on any Nova processor with 32K bytes of main memory, a real-time clock, and a communications chassis which includes at least one SLM-2, ALM-8, or ALM-16.

SENSOR ACCESS MANAGER (SAM): Data General's Sensor Access Manager provides a library of device handlers and subroutines that control input/output transfers between user programs and analog and digital sensor devices.

REMOTE JOB ENTRY CONTROL PROGRAM (RJE80): Allows for remote job entry and communications between Nova processors and IBM 360/370 systems, or between Nova processors and other Data General computers. Support is provided for four types of RJE systems:

- Point-to-point communications between a Nova or Eclipse emulating an IBM 2780/3780 and an IBM 360/ 370 host.
- Point-to-point communications between two Data General systems running RJE80.
- Multi-drop Data General systems emulating IBM 3780 slave terminals, communicating with an IBM 360/370 host.
- Multi-drop Data General Systems emulating IBM 3780 slave terminals, communicating with a Nova or Eclipse master system also running RJE80.

RJE80 running under mapped RDOS also allows a user to run one application in either the foreground or background mode while RJE80 runs independently in either mode.

RJE80 is supported by RTOS, RDOS, and mapped RDOS, as well as CAM. Features include horizontal and vertical printer format control; error detection on transmission and reception; and disc, tape, or card transmission to remote systems. Transmission between host systems may be to unattended RJE80 systems, and because of device-independent I/O capabilities, any combination of I/O devices can be utilized without additional software.

Under RTOS, RJE80 requires a 32K-byte Nova with a card reader, line printer, console terminal, real-time clock, 4251 or 4252 communications chassis, and an SLM-2 Synchronous Line Multiplexer.

Under RDOS, RJE80 requires a 48K-byte Nova with 512K bytes of disc storage, a line printer, a console terminal, a real-time clock, a 4251 or 4252 communications chassis, and an SLM-2 Synchronous Line Multiplexer. RJE80 can handle line speeds up to 9600 bps with the SLM-2 or up to 56K bps with the DCU/50 or DCU/200 Data Control Unit and SLM-2 combination. Any modem of the Bell 201, 203, 208, 209, or 303 type, or equivalent, can be utilized.

A library of communications software is available for operation under RDOS, Mapped RDOS, or RTOS. The library includes a Binary Synchronous Communications package callable from FORTRAN programs, device-independent drivers for intercomputer communications, particular multiprocessor Data General configurations, and direct channel interfacing to an IBM 360 or 370 system.

SOFTWARE

OPERATING SYSTEMS: Five levels of system control programming are available for various configurations of the Nova systems. Each of these includes an appropriate level of language processors and utility programs.

MP/OS is a single-user, multitasking, disk-based, real-time operating system. MP/OS supports MP/Pascal and MP/

FORTRAN IV. It provides data management capabilities such as permanent file protection, device-independent I/O access, and hierarchical file directories. MP/OS utilities include a command line interpreter, text editor, macro assembler, binder (compiles object files into relocatable program files), debugger, and library editor.

Real-Time Disc Operating System (RDOS) is a full-scale operating system that supports multi-tasking. It can schedule and allocate program resources to many different subprogram tasks. It is a comprehensive, modular system with a system generation procedure allowing the user to tailor the operating system to his hardware configuration and his application.

RDOS can be used either interactively from a console keyboard or in batch mode from job streams entered via card readers, disc files, cassette files, or magnetic tape files. RDOS can simultaneously support both foreground and background tasks, so that users can run two jobs at the same time. The higher-priority job, which is normally a real-time or response-dependent application program, is run in the foreground, while the lower-priority job is run in the background. Data from a background job is typically processed while waiting for an event or for data from the foreground job. Background mode can also be used to develop new programs without interrupting ongoing jobs. Foreground and background programs can be hardware-protected from each other and from the operating system.

RDOS includes a multi-partitioning system that gives users flexibility in overlaying programs from disc into main memory. Large user programs can be segmented into discresident overlays to allow efficient use of main memory and to make the programs more manageable. Tasks stored on the disc occupy main memory only when they are ready for execution. The dual-processor, shared-disc feature allows RDOS users to share peripherals and to access common data and programs on disc.

Also available to RDOS users is the Batch command interpreter and job supervisor. Batch calls in and controls execution of user and system programs. Any program that an online user can execute interactively from the console can be called. The Batch processor is not an integral part of RDOS and occupies no main memory when it is not being run.

RDOS supports Business BASIC, Extended BASIC, and Extended FORTRAN IV and 5.

RDOS operates on any Data General minicomputer with 16K words of main memory, a teletypewriter, and a disk. In addition, RDOS can support additional memory (up to 32K words), 8 disk cartridges or disk pack drives, and 8 magnetic tape transports (either 7- or 9-track). Card readers, line printers, communications equipment, and analog and digital conversion equipment are also supported.

Mapped RDOS is available on the Nova 4/X to support two-partition multiprogramming with 32K-word user program areas and an operating system area of up to 32K words (typical size is 8K to 12K words). Mapped RDOS provides an extensive file management capability. It features a common I/O interface, checkpointing for a background task, program segmentation, communication between tasks, and compatibility with RTOS and DOS. Mapped RDOS supports systems of up to 128K words, with each partition protected from the remainder of main memory. Mapped RDOS foreground/background designations are merely a reference to the usual priority designations, which are set up by the user and can be altered at any time. The two partitions could even be given equal priority.

Real-Time Operating System (RTOS) is an upward-compatible subset of the Real-Time Disc Operating System



► (RDOS). RTOS provides standard interrupt servicing, device handling, and executive scheduling functions. Supporting a multi-tasking environment, RTOS sets up multiple execution paths to perform functions nonsequentially. Tasks are scheduled based on their need to use the processor or I/O devices and the availability of those devices. When more than one task is ready to execute, a multi-level priority structure determines processing sequence by insuring that the highest-priority function is serviced first.

RTOS is modular and re-entrant, and provides the user with a library of modules for system, task, and device processing. It also provides executive functions that schedule task execution. Tasks are scheduled under program or operator control, either after a given time delay, periodically by a real-time clock, or in response to an external event. Once a task begins execution, the task can modify its own priority, can terminate or delay its execution, or can suspend, awaiting the completion of an I/O operation.

In an RTOS-controlled application, asynchronous tasks communicate with each other through an intertask message mechanism. This communication capability makes it possible to coordinate parallel activities controlling the user environment.

RTOS runs on a minimum configuration of 8K words of memory and a real-time clock. It will also support up to 32K words of memory, multiple teletypewriters or CRT's, fixed-and moving-head discs, magnetic and cassette tape transports, paper tape reader/punches, card readers, line printers, plotters, process I/O equipment, and asynchronous, synchronous, and intercomputer controllers.

Real-time support for the Extended and Macro Assemblers and FORTRAN IV is available under RTOS.

Disc Operating System (DOS) is another upward-compatible subset of the Real-Time Disc Operating System (RDOS). DOS is diskette-based and provides medium-scale program development on a small-scale system. Like its larger brothers, DOS is memory- and file-based. Features provided by DOS include a comprehensive disc and tape file system, device-independent file transparency, multitasking facilities, user program segmentation, and interactive program development via the DOS Command Line Interpreter (CLI). Besides CLI, DOS operates with other Data General system software including the text editor, library file editor, and relocatable loader. DOS supports up to 32K words of memory and the full range of peripherals available from Data General.

Languages operational under DOS include the Macro and Extended Assemblers, BASIC Interpreter, and FORTRAN IV Compiler. File management is provided for both random-access and fixed-length sequential files. A multi-task scheduler provides for user-written programs designed to handle multiple terminals, instrumentation and control processes, and complex communications schedulers. Three methods of user program segmentation are employed: chaining, swapping, and overlaying.

DOS requires a Nova with a minimum of 16K words of memory, a real-time clock, a single diskette drive and controller, and a terminal. This configuration can be utilized for user program execution and program development with the Extended Assembler. However, a dual-diskette system is required for DOS generation and for software development using BASIC, FORTRAN IV, or the Macro Assembler.

LANGUAGES: High-level compiler languages available for use on Nova computers include FORTRAN 5, single- and multi-user Extended BASIC, Business BASIC, ALGOL, MP/PASCAL, and MP/FORTRAN IV.

MP/FORTRAN IV operates with the MP/OS operating system and implements the ANSI FORTRAN 1966 Standard. MP/FORTRAN IV features real-time multitasking capability; comprehensive file structure; full operating system support; free-form, formatted and conversational I/O; compiler-generated reentrant coding; user program swaps, chains and overlays; relational and logical operators; double-precision and mixed-mode arithmetic; and compatibility with DOS, RDOS, and AOD FORTRAN IV.

MP/PASCAL is a high-level structured programming language based on a subset of Nicklaus Wirth's PASCAL language. It provides data and programming structures that can clearly represent application data and operations. MP/PASCAL features structured data including arrays, strings, records and sets of data, and strong data typing including user-defined types; modular programming extensions; systems-oriented extensions and enhanced I/O; compact threaded code generation; operates under the MP/OS operating system; and offers real-time multitasking capabilities.

FORTRAN 5 is a superset of Data General's FORTRAN IV, ANSI FORTRAN, IBM FORTRAN IV (H Extended), and Univac FORTRAN V. Special compiler features include global code optimization, comprehensive error checking and diagnostics, and re-entrant code. Language extensions beyond ANSI FORTRAN include static and dynamic storage allocation, statement functions expanded as in-line code, data initialization in DIMENSION and data type statements, implied DO loops, flexible declaration order, double-precision and complex arithmetic, IMPLICIT statements, Include statement, full mixed-mode arithmetic, extended array manipulation capabilities, generic library functions, simplified I/O, bit manipulation, and multi-tasking capability.

FORTRAN 5 operates on any Data General minicomputer running under RDOS. It requires a minimum of 32K words of main memory, hardware multiply/divide, the floating-point processor, 512K words of disc storage, and a console terminal. FORTRAN 5 software is supplied on either cassette tape or magnetic tape and requires the appropriate tape drive to interface with the system. Conversational I/O requires a printer terminal or video display console.

Extended BASIC has all the features of Time-Sharing BASIC as well as extended facilities that allow access to Data General I/O peripherals for both data and program files. Supported peripherals include: high-speed paper tape reader/punch, line printer, fixed-head discs, and movinghead disc pack and disc cartridge drives.

Five versions of the Extended BASIC system include:

Single-user with disc,
Single-user without disc,
Multi-user (non-swapping) with disc,
Multi-user (non-swapping) without disc, and
Multi-user (swapping) with disc.

The swapping version of Extended BASIC will time-slice main memory among multiple users. Extended BASIC operates under RDOS or DOS.

Business BASIC, developed as a spin-off of the still-viable RDOS Extended BASIC, can be run under RDOS or Mapped RDOS. Mapped RDOS users can run Business BASIC as a time-sharing system in one partition while concurrently running a batch, time-sharing, or real-time job in the second partition. Extensions to BASIC for business applications in multiple-terminal environments include a multiple-keyed indexed sequential file access method, dynamic record allocation, six-character variable names, common area, direct block I/O, and commercial string func-



tions. Business BASIC uses double-precision integer arithmetic, maintaining accuracy to ±21,474,836.47 and eliminating the rounding problem of floating-point arithmetic. Print formatting statements allow output strings to be left or right justified, filled with a character, or truncated. They also handle decimal points, fixed and floating signs, and zero suppression.

Business BASIC provides access to standard RDOS sequential, random, and contiguous files, but also provides its own multiple-keyed indexed sequential file structure for keyed data access. Each ISAM index is a separate, balanced tree-structured file, and multiple indices may have access to the same data file. Data records can be allocated to disc dynamically. Both index and date files have lock and unlock protection. Record lockout provides file integrity when files are accessible by multiple users. An optional interface to the INFOS data management system is available for use with commercial Eclipse systems. Files created with this interface are compatible with INFOS ISAM and DBAM files created by COBOL, FORTRAN IV, FORTRAN 5, and the Idea data entry software.

Program development features include the RENUMBER statement for the entire program, the RUNUM statement for specific line numbers, and the VAR statement for a sorted listing of all variables used in a program. A crossreference listing can also be produced. An attach and deattach facility lets a user start a job at a terminal and then de-attach it to run independently of any terminal, so that the user can work on other programs at the same terminal. For file maintenance, Business BASIC statements allow files to be sorted on disc and allow editing of ASCII text files. The file maintenance package also supports simultaneous creation, modification, and accessing of files by multiple users. A set of utility programs is provided to simplify program development and aid documentation. System security is maintained by statements that can prevent users from accidentally accessing Business BASIC at the monitor level.

The minimum equipment configuration required for Business BASIC is any Nova 4 computer licensed to run RDOS, with 64K bytes of memory, any Data General terminal, a real-time clock, and 10 megabytes of disc storage.

The Extended Assembler is similar to the basic Nova Absolute Assembler in that it converts symbolic assembly statements into machine-executable code. In addition to the Absolute Assembler features, the Extended Assembler also provides relocation, interprogram communication, conditional assembly, and more powerful number definition facilities.

The Macro Assembler adds extensive macro capabilities to the facilities of the Extended Assembler.

UTILITIES: A library of utility programs for Nova 4 systems includes the FORTRAN Commercial Subroutine Package, a superset of IBM's CSP for data and format conversion and extended-precision integer arithmetic. Also included are a batch job control supervisor for executing stacked jobs; a real-time I/O system to support A-D and D-A interfaces and RDOS sort/merge; a text editor for 1 to 20 users; a symbolic debugger; Dataplot, a set of FORTRAN-callable routines for line drawing and axis rotation on digital plotters; and three editing programs—the Macro Editor, the Library File Editor (LFE), and the Octal Editor (OEDIT).

Utilizing simple command string input, the *Macro Editor* edits paper tape input to produce updated paper tape output. The user may define command strings in a special macro register. The command string may then be executed repeatedly by specifying the macro register name in subsequent command strings. A common application for the Macro Editor is the modification of program source tapes in preparation for a new assembly.

LFE allows the user to analyze the contents of a given library file, to merge and update libraries, and to create his own library files.

OEDIT permits the user to examine and modify, in octal, locations on a disc file. A common use of OEDIT is in making simple changes to executable saved files.

APPLICATIONS: No separate applications packages have been announced for the Nova systems to date. Other software consists of about 20 mathematical routines, more than 24 CPU and peripheral device diagnostics, and a variety of language processor libraries, format conversion routines,

PRICING

POLICY: Data General offers the Nova series on a purchaseonly basis, with two types of separately priced maintenance agreements: the On-Call Service contract and the Depot Service contract, which involves return of faulty equipment to a designated repair location. In either case, all parts and labor are included at no additional cost.

Normal prime-time on-call contract service hours are 9 a.m. to 5 p.m. Charges quoted in the price list are applicable to customers within 100 miles of a service center. Additional but uniform monthly charges are in effect beyond 100 miles of a Data General service center. These charges are \$150 for customers between 100 and 300 miles from the center and \$225 for customers beyond 300 miles.

Under a Depot Service contract, any portion of a system may be covered, the minimum contract being \$75. The customer assumes all transportation and insurance costs. For non-contract on-site service, the hourly maintenance rates are \$40 for prime time and \$48 for all other times. A \$135 regular-time or \$165 premium-time minimum charge applies. Depot service hourly labor charges are \$45 for prime time and \$55 for all other times.

Most of the Data General software is unbundled and offered at license fees which are listed in the "Software Prices" section at the end of this report. One-day on-site consulting service, including RDOS system generation, is billable at \$300 plus transportation costs from the nearest office, except for systems costing over \$30,000 with a high-speed input device, where the service is free.

The Data General Software Subscription Service provides automatic updates and documentation for Data General software at a price ranging from \$50 to \$350 per software product, and for \$75 per product on any order totalling \$1,000 or more.

The Hardware Subscription Service provides automatic updates, additions, and notification of new documentation on all Data General hardware for a fixed yearly fee. It is available to any owner of Data General equipment. This includes owners who have purchased their equipment through another vendor. Initial subscriptions include updates for one year. Prices are as follows: Nova processors, \$980; peripherals, \$920; and communications and I/O, \$920. Additional log books for any of the above topics are \$500 each without updates. Yearly renewal rates are \$480 for Nova processors, \$420 for peripherals, and \$420 for communications and I/O. A 40 percent discount applies for additional updates beyond the first to the same type of log book, ordered at the same time and deliverable to the same address.

Data General provides training courses for customers at its Southboro, Massachusetts, headquarters, at its Western Training Center in El Segundo, California, and at its United Kingdom Training Center in Greenford, Middlesex, England. Two training credits are given for each system purchased (end user) or two training credits per purchase agreement



➤ (OEM). One training credit entitles a customer to approximately one man-week of training. Schedules for training courses can be obtained at any Data General field office.

Courses currently being offered include: Introduction to Small Computers, 3 days, \$175; Introduction to Assembly Language Programming, 10 days, \$625; Assembly Language Program Implementation, 5 days, \$325; FORTRAN Program Implementation, 5 days, \$325; RTOS, 5 days, \$325; RDOS Assembly, 5 days, \$325; RDOS FORTRAN, 5 days, \$325; RDOS Systems Programming, 5 days, \$375; Real-Time FORTRAN for Industrial Control, 3 days, \$225; Small Computer Hardware Fundamentals, 5 days, \$300; Nova Multiply/Divide Option, 2 days, \$150; Floating-Point Unit Option, 3 days, \$225; Basic I/O Interfacing, 2 days, \$150; Magnetic Tape, 3 days, \$275; Data General Cassette, 2 days, \$175; Moving-Head Disc, 3 days, \$275; Line Printer, 2 days, \$175; and Card Reader, 2 days, \$175.

On-site training is available when necessary. Costs involve \$600 per day (with a three-day minimum) for instructional charges including the instructor's daily expenses, instructor's travel expenses, \$100 per weekend for subsistence when incurred, and a per-student charge for actual documentation used.

The Data General Users' Group provides a forum for interchange of programs. The programs are available for a fee to cover reproduction and distribution costs.

Prices shown in the Equipment Prices list are for single-unit quantities. Standard OEM three-to-five quantity discounts of 19 percent apply. Discounts of about 40 percent are available for quantities for 200 or more units. Third-party leases are available.

EQUIPMENT: The following system purchase prices include all required control units, adapters, and cables.

MODEL 8390-H: Consists of a Nova 4/C with 64K bytes of MOS memory, asynchronous interface, automatic program load, power fail/auto restart, and 5-slot chassis. Purchase price is \$3,675.

MODEL 8393-H: Consists of a Nova 4/S with 64K bytes of MOS memory, asynchronous interface, real-time clock, automatic program load, power fail/auto restart, and 16-slot chassis. Purchase price is \$7,980.

MODEL 8395-N: Consists of a Nova 4/X with 256K bytes of MOS memory, MMPU, asynchronous interface, real-time clock, automatic program load, power fail/auto restart, 16-slot chassis, battery backup, and 2-bay cabinet. Purchase price is \$15,015.■

EQUIPMENT PRICES

All Nova 4 computers include four accumulators (hardware), I/O system with programmed data transfer, 16-level programmed priority interrupt, extended stack facility, and direct memory access (DMA) data channel, asynchronous interface, power fail/auto restart, auto program load and virtual console. Prices include power supply, and chassis for 19-inch rack 3839-G Nova 4/C computer with 32K bytes MOS memory in 5-slot chassis \$2,940 \$47 8391-G Nova 4/C computer with 64K bytes MOS memory in 16-slot chassis 4,620 59 8391-H Nova 4/C computer with 64K bytes MOS memory in 16-slot chassis 5,355 64 Nova 4/C computer with 64K bytes MOS memory in 16-slot chassis 5,355 64 Nova 4/C computer with 32K bytes MOS memory in 16-slot chassis 5,355 64 Nova 4/S computer with 32K bytes MOS memory in 16-slot chassis 5,355 64 Nova 4/S computer with 32K bytes MOS memory in 16-slot chassis 6,090 59 8393-G Nova 4/S computer with 32K bytes MOS memory in 16-slot chassis 7,360 70 8393-H Nova 4/S computer with 64K bytes MOS memory in 16-slot chassis 7,360 70 8393-H Nova 4/S computer with 64K bytes MOS memory in 16-slot chassis 7,360 70 8393-H Nova 4/S computer with 64K bytes MOS memory in 16-slot chassis 7,980 79 Nova 4/X computers have 4-way interleaved MOS memory in 16-slot chassis 7,980 79 Nova 4/X computers have 4-way interleaved MOS memory with a full memory cycle time of 400 nanoseconds Nova 4/X computers have 4-way interleaved not succeed an a single 15 inch board and occupy one subassembly slot. Robert memory module mounts on a single 15 inch printed circuit board and occupy one subassembly slot. Each memory module mounts on a single 15 inch printed circuit board and occupies one subassembly slot. Nova 4/X computer with 128K bytes MOS memory in 16-slot chassis 12,180 99 8394-K Nova 4/X computer with 128K bytes MOS memory in 16-slot chassis 12,180 99 8394-N Nova 4/X computer with 128K bytes MOS memory in 16-slot chassis 12,180 99 8394-N Nova 4/X computer with 128K bytes MOS memory in 16-slot chassis 12,180 99 8394-N Nova 4/X computer wi			Purchase Price	Monthly Maint.
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8390-H Nova 4/C computer with 64K bytes MOS memory in 5-slot chassis 3,675 53 8391-H Nova 4/C computer with 64K bytes MOS memory in 16-slot chassis Nova 4/S computers have 4-way interleaved MOS memory with a full memory cycle time of 400 nanoseconds. Each memory module mounts on a single printed circuit board. 8392-G Nova 4/S computer with 32K bytes MOS memory in 5-slot chassis 6,090 59 8393-G Nova 4/S computer with 32K bytes MOS memory in 16-slot chassis 7,350 70 8392-H Nova 4/S computer with 64K bytes MOS memory in 16-slot chassis 6,720 67 8393-H Nova 4/S computer with 64K bytes MOS memory in 16-slot chassis 7,980 79 Nova 4/X computers, including MMPU, are packaged on a single 15 inch board and occupy one subassembly slot. Nova 4/X computer with 128K bytes MOS memory with a full memory cycle time of 400 nanoseconds 8394-K Nova 4/X computer with 128K bytes MOS memory in 16-slot chassis 10,920 82 8395-K Nova 4/X computer with 128K bytes MOS memory in 16-slot chassis 10,920 82 8395-N Nova 4/X computer with 128K bytes MOS memory in 16-slot chassis 11,180 99 8395-N Nova 4/X computer with 256K bytes MOS memory in 16-slot chassis 12,180 99 8395-N Nova 4/X computer with 256K bytes MOS memory in 16-slot chassis 13,755 107 8395-N Nova 4/X computer with 256K bytes MOS memory in 16-slot chassis 13,755 107 8395-N Nova 4/X computer with 256K bytes MOS memory in 16-slot chassis 14,675 124 PACKAGED SYSTEMS 9266-B series packaged systems consist of a Nova 4/C 5-slot with 64K bytes of MOS memory, asynchronous interface, real-time clock, power fail/auto restart, console debug with automatic program load, and one of several magnetic storage device options 9266-A Nova 4/C 5-slot system with 10MB cartridge disk Nova 4/C 5-slot system with 125MB disk and integral 1.26MB diskette 11,675 129 8266-C Nova 4/C 5-slot system with 125MB disk and integral 1.26MB diskette 14,275 137 8266-D Nova 4/C 5-slot system with 125MB disk and integral 1.26MB diskette				
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Nova 4/S computer with 64K bytes MOS memory in 16-slot chassis Nova 4/S computers, including MMPU, are packaged on a single 15 inch board and occupies one subassembly slot. Each memory module mounts on a single 15 inch printed circuit board and occupies one subassembly slot. Nova 4/X computers have 4-way interleaved MOS memory with a full memory cycle time of 400 nanoseconds Nova 4/X computer with 128K bytes MOS memory in 5-slot chassis Nova 4/X computer with 128K bytes MOS memory in 16-slot chassis Nova 4/X computer with 256K bytes MOS memory in 16-slot chassis Nova 4/X computer with 256K bytes MOS memory in 16-slot chassis Nova 4/X computer with 256K bytes MOS memory in 16-slot chassis PACKAGED SYSTEMS 9266 series packaged systems consist of a Nova 4/C 5-slot with 64K bytes of MOS memory, asynchronous interface, real-time clock, power fail/auto restart, console debug with automatic program load, and one of several magnetic storage device options 9266-A Nova 4/C 5-slot system with 10MB cartridge disk 9266-B Nova 4/C 5-slot system with 12.5MB disk and integral 1.26MB diskette 11,675 129 9266-C Nova 4/C 5-slot system with dual 315KB diskettes 7,975 111	8393-G		7,350	70
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PACKAGED SYSTEMS 9266 series packaged systems consist of a Nova 4/C 5-slot with 64K bytes of MOS memory, asynchronous interface, real-time clock, power fail/auto restart, console debug with automatic program load, and one of several magnetic storage device options 9266-A Nova 4/C 5-slot system with 10MB cartridge disk 9266-B Nova 4/C 5-slot system with 12.5MB disk and integral 1.26MB diskette 11,675 129 9266-C Nova 4/C 5-slot system with 25MB disk and integral 1.26MB diskette 14,275 137 9266-D Nova 4/C 5-slot system with dual 315KB diskettes 7,975 111	8394-N	Nova 4/X computer with 256K bytes MOS memory in 5-slot chassis	13,755	107
9266 series packaged systems consist of a Nova 4/C 5-slot with 64K bytes of MOS memory, asynchronous interface, real-time clock, power fail/auto restart, console debug with automatic program load, and one of several magnetic storage device options 9266-A Nova 4/C 5-slot system with 10MB cartridge disk 9266-B Nova 4/C 5-slot system with 12.5MB disk and integral 1.26MB diskette 11,675 129 9266-C Nova 4/C 5-slot system with 25MB disk and integral 1.26MB diskette 14,275 9266-D Nova 4/C 5-slot system with dual 315KB diskettes 7,975 111	8395-N	Nova 4/X computer with 256K bytes MOS memory in 16-slot chassis	15,015	124
real-time clock, power fail/auto restart, console debug with automatic program load, and one of several magnetic storage device options 9266-A Nova 4/C 5-slot system with 10MB cartridge disk 9266-B Nova 4/C 5-slot system with 12.5MB disk and integral 1.26MB diskette 9266-C Nova 4/C 5-slot system with 25MB disk and integral 1.26MB diskette 11,675 129 9266-D Nova 4/C 5-slot system with 25MB disk and integral 1.26MB diskette 7,975 111	PACKAG	SED SYSTEMS		
9266-B Nova 4/C 5-slot system with 12.5MB disk and integral 1.26MB diskette 11,675 129 9266-C Nova 4/C 5-slot system with 25MB disk and integral 1.26MB diskette 14,275 137 9266-D Nova 4/C 5-slot system with dual 315KB diskettes 7,975 111	real-time o	clock, power fail/auto restart, console debug with automatic program load, and one of several magnetic		
9266-B Nova 4/C 5-slot system with 12.5MB disk and integral 1.26MB diskette 11,675 129 9266-C Nova 4/C 5-slot system with 25MB disk and integral 1.26MB diskette 14,275 137 9266-D Nova 4/C 5-slot system with dual 315KB diskettes 7,975 111	9266-A	Nova 4/C 5-slot system with 10MB cartridge disk	14,675	177
9266-C Nova 4/C 5-slot system with 25MB disk and integral 1.26MB diskette 14,275 137 9266-D Nova 4/C 5-slot system with dual 315KB diskettes 7,975 111				
9266-D Nova 4/C 5-slot system with dual 315KB diskettes 7,975 111				

Data General Nova 4 Series EQUIPMENT PRICES

NOVA 4	OPTIONS	Purchase Price	Monthly Maint.
		400	
8381 8382	Real-time clock Multiply/divide, including signed and unsigned operations	400 785	4 6
8380	Battery backup	525	3
8383	Multiply/divide and real-time clock	1,150	11
8388 8397	Floating point unit	3,150	26
8397 8684-A	Internal cable for external I/O bus Direct connect expansion chassis to be rack mounted directly above the CPU	250 4,000	30
8684-B	Remote connect expansion chassis	4,500	30
NOVA 4	EXPANSION MEMORY		
8384	32K byte MOS memory module	2,310	21
8385 8386	64K byte MOS memory module 128K byte MOS memory module	3,150 5,250	29 44
	ROCESSOR OPTIONS	-,	
8080	I/O Bus switch chassis with 14 I/O slots, power supply, front panel and selector switch connectors for cabling to	4,500	59
	2 computers		
8081 8315	Bus control card for connecting one computer I/O bus to the 8080 bus switch chassis I/O bus repeater	1,700 1,200	22 6
4206	Multiprocessor Communications adapter; up to fifteen DGC computers may be interconnected with one 4206 MCA for each computer	2,100	19
1106	Multiprocessor Communications adapter cable for connecting two 4206 MCA's	300	_
4240 1065F	Interprocessor bus unit for synchronization and communication between any two DGC computers Interprocessor bus cable for connecting two IPB units	1,900 300	19
4025			275
	Programmable interface to IBM 360/370; consists of an adapter and 2 printed circuit boards	7,500	275
6067 6067-A	Disk subsystem includes controller and adapter for up to four drives, a 50MB disk drive, cables, and four disk packs	21,800	190
6067-A	50MB disk drive for adding second, third, and fourth drive Disk subsystem includes controller and adapter for up to four drives, a 96MB disk drive, cables, and disk pack	14,800 25,800	140 220
6060-A	96MB disk pack for adding second, third, and fourth drive	20,800	160
6061	Disk subsystem includes controller and adapter for up to four drives, 190MB disk drive, cables, and disk pack	31,000	220
6061-A	190MB disk drive for adding second, third, and fourth drive	26,000	160
6062 6045	Dual port option for 6060, 6061, and 6067; include controller plus internal and external cables for dual porting 10MB cartridge disk subsystem including one cartridge disk drive with 5M bytes of removable and 5M bytes of	6,000 10,600	50 120
6046	fixed disk storage, controller for four drives, associated cables and one removable disk cartridge 20MB cartridge disk subsystem including two 10M byte cartridge disk drives, controller for four drives, associated	19,250	200
6047	cables, and two removable disk cartridges 30MB cartridge disk subsystem including three 10MB cartridge disk drives, controller for four drives, associated	27,900	280
6048	cables, and three removable disk cartridges 40MB cartridge disk subsystem including four 10MB cartridge disk drives, controller for four drives, associated	36,550	360
6051	cables, and four removable disk cartridges Dual port option for 6045, 6046, 6047, and 6048; includes controller plus internal and external cables	4,500	50
6050	for dual porting 10MB cartridge disk drive for adding a drive in the field to an existing 6045, 6046, or 6047 subsystem	9,500	80
6050-F	10MB cartridge disk drive for adding a drive in the field to an existing diskette subsystem	10,100	115
6070	20MB cartridge disk subsystem including one cartridge disk drive with 10M bytes of removable and 10M bytes of fixed disk storage, controller for four drives, associated cables and six removable disk cartridges	13,500	140
6070-B	Same as 6070 except includes two 20MB cartridge disk drives	21,500	230
6070-C 6070-D	Same as 6070 except includes 20MB cartridge disk drives Same as 6070 except includes four 20MB cartridge disk drives	29,500 37,500	320 410
6070-A	20MB cartridge disk drive for adding a drive in the field to an existing 6070, 6070-B, or 6070-C subsystem	10,300	90
6063	1M byte head per track disk subsystem including controller for up to four drives, disk drive, and cables	9,900	90
6063-A	1M byte head per track disk drive for adding second, third, and fourth drive	7,900	80
6064	2M byte head per track disk subsystem including controller for up to four drives, disk drive, and cables	13,900	125
6064-A 6066	2M byte head per track disk drive for adding second, third, and fourth drive 4M byte head per track disk subsystem including controller for up to 4 drives, 2 disk drives and all cabling	11,900 23,800	115 240
6065	Dual port option for 6063, 6064, and 6066; includes controller plus internal and external cables for dual porting	5,000	40
6098	12.5M byte non-removable DG/Disk subsystem with 1.26MB diskette drive	7,600	72
6099	12.5M byte non-removable DG/Disk subsystem	5,500	41
6100	25M byte non-removable DG/Disk subsystem with 1.26MB diskette drive	10,200	80
6103 6099-A	25M byte non-removable DG/Disk subsystem 12.5M byte disk drive for expansion for 1.26MB per drive diskette subsystems	8,100 5,200	49 25
6103-A	25M byte disk drive for expansion of 1.26MB per drive diskette subsystems	5,200 7,800	35 43
6030	Dual diskette subsystem including controller for up to four drives, chassis with power supply, two drives, and cable set	3,900	54
6030-A	Dual diskette drive for adding two drives to dual or single diskette subsystems	3,400	48
6030-B	Dual diskette drive for adding two drives to a 10MB cartridge disk subsystem	3,400	48
6031 6031-A	Single diskette subsystem including controller, chassis with power supply, 1 drive, and cable set Single diskette drive for adding one drive to dual or single diskette subsystems	2,900	48 42
6031-A	Single diskette drive for adding one drive to dual or single diskette subsystems Single diskette drive for adding one drive to a 10MB cartridge disk subsystem	2,400 2,400	42 42
6097-A	Single diskette subsystem including controller that handles diskette and/or 12.5/25MB disk drives	4,200	55
6097-B	Dual diskette subsystem including controller that handles diskette and/or 12.5/25MB disk drives	5,600	80
6096-C	Single diskette drive for adding to 1.26MB per drive diskette subsystem or subsystems containing 12.5 or 25MB disk drive	3,800	35

EQUIPMENT PRICES

		Purchase Price	Monthly Maint.
MAGNET	TIC TAPE		
6026	Magnetic tape subsystem includes transport, 9-track, 75 ips, 800/1600 bpi, switch selectable, industry compatible; includes control for up to eight transports	15,500	121
6026-A 6027	Magnetic tape transport; 9-track, 75 ips, 800/1600 bpi, switch selectable, industry compatible Magnetic tape subsystem includes 9-track transport, 75 ips, 800 bpi, switch selectable, industry compatible; includes control for up to eight transports	11,300 11,900	89 105
6021	Magnetic tape subsystem includes transports 9-track, 75 ips, 800 bpi, switch selectable, industry compatible; includes control for up to eight transports	10,900	95
6023	Magnetic tape transport 9-track, 75 ips, 800 bpi, switch selectable, industry compatible	7,700	68
PRINTER	s		
6073	DASHER LP2 line printer; 180 cps, logic-seeking, 7 x 9 impact dot matrix, and 132 characters per line (cpl)	3,750	41
6074 6086	Same as 6073 except with condensed and expanded/condensed printing DASHER LP2 line printer subsystem; 180 cps, logic-seeking, bidirectional 7 x 9 impact dot matrix, 132 cpl; includes programmed I/O controller, wire paper basket, and cable	3,850 4,450	42 55
6087	Same as 6086 except with condensed and expanded/condensed printing	4,550	56
6088	DASHER LP2 data channel printer subsystem; 180 cps logic-seeking, bidirectional 7 x 9 impact dot matrix, 132 cpl; includes line printer, data channel controller, wire paper basket, and cables	5,150	58
6089	Same as 6088 except with condensed and expanded/condensed printing	5,300	59
4323	300 lpm DG/band printer; 64 upper case character set; includes DAVFU and forms length selector switch	9,500	85
4324 4325	230 lpm DG/band printer; 96 upper/lower case character set; includes DAVFU and forms length selector switch 300 lpm programmed I/O DG/band printer subsystem; 64 upper case character set; includes DAVFU, forms length selector switch, controller and cables	10,400 10,400	85 97
4327	300 lpm Data Channel DG/band printer subsystem; 64 upper case character set; includes DAVFU, forms length selector switch, D/CH controller, and cables	10,400	101
4328	Same as 4327 except 230 lpm and 96 upper/lower case character set	11,300	101
4244	900 lpm line printer subsystem; 136 columns, 64 ASCII character set, 6-part form capability, 6 or 8 lines per inch; includes line printer, data channel controller, static eliminator, direct-access vertical format unit, active ribbon control, paper receptacle and cable	25,500	200
4245	Same as 4244 except includes 660 lpm printer and 96 ASCII character set	27,000	208
4215	Same as 4244 except includes 600 lpm printer	18,000	150
4216 4218	Same as 4244 except includes 436 lpm printer and 96 ASCII character set Same as 4244 except includes 300 lpm printer	19,900 13,750	155 115
4219	Same as 4244 except includes 240 lpm printer and 96 ASCII character set	14,400	127
4217	Optional programmable interval timer	900	7
4014	I/O interface subassembly	200	5
4034 4034C	Line printer control for 4034C, DASHER LP2 or 4034C, 4034D printers Serial matrix printer; 165 cps, 10 cpi, up to 132 cpl; character formation is with a 5 x 7 matrix	200 4,500	5 58
4034D	Same as 4034C except 7 x 9 matrix character formation	4,900	58
4193	Line printer control for 4034G, 4034H printers	1,400	15
4034G 4034H	300 lpm printer; 136 columns, 64 ASCII character set, 6-part form capability, 6 or 8 lines per inch switch- selectable static eliminator standard Same as 4034G except includes 240 lpm printer and 96 ASCII character set	8,550 10,550	86 86
		10,550	00
CARD R			
4306 4016D	Buffered card reader controller for 4016D—4016J card readers Medium-speed punched card reader, 285 cpm	1,800 4,680	19 73
4016F	High-speed punched card reader, 600 cpm	5,970	130
40161	Medium-speed mark sense card reader, 285 cpm	5,450	79
PAPER T	APE READER/PUNCH		
4007	I/O interface subassembly	200	5
4011	Paper tape reader control for 6013 reader	850	12
6013 4012	High-speed paper tape reader, 400 cps, fanfold, 8-channel tape, rack mountable Paper tape punch control for 4012A and 4012B paper tape punch	1,150 700	19 12
4012A	High-speed paper tape punch, 63.3 cps fanfold, 8-channel paper tape	2,400	24
4013	Remote-operation modification to punch; allows power turn-on, turn-off under program control	300	5
4012B	High-speed paper tape punch for use with 4013 remote operations modification	2,400	26
PLOTTER			
4014 4017	I/O interface subassembly Incremental plotter control for all 4017 series plotters	200 1,650	5 12
4017 4017E	Incremental plotter (z-fold paper), 11-inch paper, 0.005-inch step size (450 steps per second)	5,000	144
4017E-A	Incremental plotter (z-fold paper), 11-inch paper, .010-inch step size (400 steps per second)	5,000	144
4017E-B 4017E-C	Incremental plotter (z-fold paper), 11-inch paper, .10 mm step size (450 steps per second) Incremental plotter (z-fold paper), 11-inch paper, .25 mm step size (400 steps per second)	5,000 5,000	144 144
TERMINA	ALS		
6106	DASHER D100 display terminal with detached keyboard; includes standard 20mA and EIA interfaces, selectable	1,750	18
	data rate to 9600 bps, and odd/even/mark or no parity bit; 14 key data entry/cursor control pad mode	.,,	.5
	selectable, 35 programmable function codes, 96 character upper/lower case characters, typewriter style		
6107	keyboard, and 24 lines x 80 character screen with 7 x 11 dot matrix format Same as 6106 except includes EIA printer interface for attaching DASHER TP1 or TP2 printer for hard copy of screen contents	2,150	23

EQUIPMENT PRICES

		Purchase Price	Monthly Maint.
TERMIN	NALS (Continued)		
6108	DASHER D200 display terminal with detached keyboard; standard 20mA and EIA interfaces, selectable data rate	1,950	19
0108	to 9600 bps, and odd/even/mark or no bit parity; 14 key data entry pad and separate cursor control pad, 19 dedicated function keys generate 75 programmable functions, 96 character upper/lower case characters, typewriter style keyboard, and 24 line x 80 character screen with 7 x 11 dot matrix format	1,330	13
6109	Same as 6108 except includes EIA printer interface for attaching DASHER TP1 or TP2 printer for hard copy of screen contents	2,350	24
6052	DASHER D1 alphanumeric video (CRT) display terminal with detachable keyboard; standard EIA or 20mA interface, 11 key data entry pad, 8 function keys, 64 character set (ASCII upper case), teletype-style keyboard, and 24 line x 80 character screen with 5 x 7 dot matrix characters	2,190	22
6053	Same as 6052 except includes 96 character set	2,650	22
6093	DASHER D3 display, CRT with detachable typewriter keyboard, switch-selectable receive and transmit speeds up to 19.2K bps; 20mA or EIA interface standard; 15 key data entry pad, 18 function keys, 96 character set, direct cursor positioning and sensing, programmable intensity plus blink, underscore, reverse video, block fill and field protect	2,890	24
6054	Printer interface option for 6052 or 6053 display	400	6
6055	Print station for 6052 or 6053 CRT terminal	2,500	33
6083-A	DASHER D2 display subsystem, 9600 baud, typewriter style keyboard with real-time clock, console interface and cable	3,090	31
6084-A	Same as 6083-A except includes foreground console	2,690	27
6085-A	DASHER D1 display subsystem, 9600 baud teletype style keyboard with real-time clock, console interface and cable	2,790	31
6042	DASHER TP1, 30 cps, keyboard send/receive terminal, 132 column 5 x 7 dot matrix typewriter style keyboard, EIA or 20mA serial interface, 128 character ASCII upper and lower case, buffered carriage return for continuous 30 cps operation	2,400	25
6043	Same as 6042 except receive only without keyboard	2,200	23
6040	DASHER TP1, 60 cps terminal printer, 132 column dot matrix, typewriter style keyboard, EIA serial interface, 128 character ASCII upper and lower case	2,650	28
6041	Same as 6040 except receive only without keyboard	2,450	26
6080-A	DASHER TP1 printer subsystem, 60 cps, with real-time clock, console interface, and cable	3,450	37
6081-A	Same as 6080-A except includes foreground console	3,050	33
6082-A	Same as 6080-A except 30 cps	3,200	34
6075	DASHER TP2 receive-only terminal printer; 180 cps logic-seeking, bidirectional, 7 x 9 impact dot matrix, 132 cpl Same as 6075 except with condensed and expanded/condensed printing	4,050 4,200	53 54
6076 6077	DASHER TP2 keyboard send/receive terminal printer; 180 cps logic-seeking, bidirectional 7 x 9 impact dot matrix,	4,250	37
6078	132 cpl Same as above except with condensed and expanded/condensed printing	4,400	38
	NICATIONS		
4007 4008	I/O interface subassembly Real-time clock	200 400	5 5
4010	Asynchronous line controller; 20mA current loop interface, full duplex	150	4
4023	EIA (RS232-C) interface for 4010 asynchronous controller	50	3
5029	Modem control for 4023 interface	200	3
4077	Asynchronous line controller, 20mA current loop interface, full duplex	150	2
4078	EIA (RE232-C) interface for 4077 asynchronous controller	50	
4079	Real-time clock; same as 4008 except uses same I/O interface subassembly as cassette control	400	4
4241	ULM-5 four-line asynchronous multiplexor subsystem for full or half-duplex operation; includes full modem control for Bell 103, 202 or equivalent data sets	1,400	16
4241-A	Add-on ULM-5 four-line asynchronous multiplexor to expand models 4241 or 4243 to a maximum of 8 asynchronous lines	1,400	16
4243	ULM-5, functional combination of model 4241 four-line asynchronous subsystem plus model 4242 one-line synchronous controller subsystem	2,700	30
4242	ULM-5 one-line synchronous controller; full or half-duplex operation; full modem control for Bell 201, 203, 208, 209 or equivalent data sets and internal clock for direct connection	1,400	16
4074	Synchronous line controller with hardware character assembly, disassembly and full character buffering; programmed I/O	1,500	16
4251	First 4-slot communication chassis; four available slots; includes chassis power supply, terminator card, and I/O cable	1,900	24
4253	Additional 4-slot expansion chassis; includes chassis power supply and four available slots for line multiplexor cards	1,500	16
4254	Data Control Unit (DCU/200) provides a high performance data channel interface to any DGC computer	3,900	40
4232 4255	DG/CS dual access controller subassembly and control panel for implementing dual access to DG/CS communications subsystems Eight-line programmable asynchronous line multiplexor; full or half-duplex operation; full modem control provided;	2,625 2,100	33 18
4256	supports Bell 103, 202 data sets or equivalent Four-line version of 4255 programmable asynchronous line multiplexor	•	
4256 4257	Sixteen-line programmable asynchronous line multiplexor; for use with local or dedicated lines, full or half-duplex operation	1,310 2,100	18 22
4258	Eight-line version of 4257 programmable asynchronous line multiplexor	1,365	22
4260	Four-line 20mA current loop interface module	170	3
4261 4263	Four-line EIA (RS-232-C/CCITT V24) compatible interface module Two-line programmable synchronous multiplexor; full or half-duplex operation, full modem control; supports	1,575	14
4264	transparent mode operation One-line version of 4263 programmable synchronous multiplexor	1,050	14
4264 4265	Optional single-line current loop (23mA) interface for use with Bell 303 data sets or equivalent	210	3
4266	Optional CRC generator and checker	525	5
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EQUIPMENT PRICES

	Purchase Price	Monthly Maint.
VICATIONS (Continued)		
Eight-line asynchronous modem interface (AMI-8), full duplex operation, full modem control provided; supports Bell 103, 202 data sets or equivalent	2,400	10
Sixteen-line asynchronous terminal interface (ATI-16) for use with local or dedicated lines; full duplex operation	2,520	22
Four-line 20mA current interface module	170	3
Four-line EIA (RS232-C/CCITT V24) compatible interface module; for use with Bell 103 data set or equivalent when manual answer only is used	170	3
Two-line programmable synchronous multiplexor; full duplex operation, full modem control; supports transparent mode operation	2,400	14
One-line version of programmable synchronous multiplexor	1,600	14
Optional single-line current loop (23mA) interface for use with Bell 303 data sets or equivalent	200	3
PURPOSE INTERFACES		
Basic I/O subsystem, includes chassis for up to 16 I/O cards, power supply module, one control card with cable, I/O terminator, and terminal boards for sensor signal connections	2,200	18
Additional I/O chassis; same as 4300 except does not include I/O terminator	2,100	18
Chassis control card and cable	1,050	9
Additional chassis control card; same as 4301 except does not include I/O terminator	875	9
Data Control Unit (DCU/50) provides a high-performance Data Channel interface to any DGC computer	3,000	50
General purpose digital input module for 16 lines	325	4
TTL digital input module; provides input lines plus one external strobe line for user interrupt trigger signals	200	3
	Bell 103, 202 data sets or equivalent Sixteen-line asynchronous terminal interface (ATI-16) for use with local or dedicated lines; full duplex operation Four-line 20mA current interface module Four-line EIA (RS232-C/CCITT V24) compatible interface module; for use with Bell 103 data set or equivalent when manual answer only is used Two-line programmable synchronous multiplexor; full duplex operation, full modem control; supports transparent mode operation One-line version of programmable synchronous multiplexor Optional single-line current loop (23mA) interface for use with Bell 303 data sets or equivalent PURPOSE INTERFACES Basic I/O subsystem, includes chassis for up to 16 I/O cards, power supply module, one control card with cable, I/O terminator, and terminal boards for sensor signal connections Additional I/O chassis; same as 4300 except does not include I/O terminator Chassis control card and cable Additional chassis control card; same as 4301 except does not include I/O terminator Data Control Unit (DCU/50) provides a high-performance Data Channel interface to any DGC computer General purpose digital input module for 16 lines	Eight-line asynchronous modem interface (AMI-8), full duplex operation, full modem control provided; supports Bell 103, 202 data sets or equivalent Sixteen-line asynchronous terminal interface (ATI-16) for use with local or dedicated lines; full duplex operation Four-line 20mA current interface module Four-line EIA (RS232-C/CCITT V24) compatible interface module; for use with Bell 103 data set or equivalent when manual answer only is used Two-line programmable synchronous multiplexor; full duplex operation, full modem control; supports transparent mode operation One-line version of programmable synchronous multiplexor Optional single-line current loop (23mA) interface for use with Bell 303 data sets or equivalent 200 PURPOSE INTERFACES Basic I/O subsystem, includes chassis for up to 16 I/O cards, power supply module, one control card with cable, I/O terminator, and terminal boards for sensor signal connections Additional I/O chassis; same as 4300 except does not include I/O terminator Chassis control card and cable Additional chassis control card; same as 4301 except does not include I/O terminator Data Control Unit (DCU/50) provides a high-performance Data Channel interface to any DGC computer 3,000 General purpose digital input module for 16 lines

SOFTWARE PRICES

		Pu Initial	rchase Price Subsequent
3100	Real-Time Operating System (Unmapped RTOS)	\$ 100	\$ NC
3175	RDOS Extended BASIC for Nova	950	150
3181	Real-Time Disk Operating System (Unmapped RDOS)	1,750	250
3212	RDOS FORTRAN IV with SFP	750	350
3218	RDOS ALGOL	NC	NC
3239	RDOS FORTRAN 5	1,000	500
3243	RTOS Magnetic Capabilities	NC	NC
3247	DATAPLOT	100	NC
3329	RDOS Sort/Merge	325	NC
3460	Communications Access Manager Version 1 (CAM 1)	100	NC
3481	RDOS FORTRAN IV with HFP	750	350
3510	RDOS Commercial Subroutine Package (CSP)	50	NC
3556	Real-Time Disk Operating System (Mapped Nova 3 and 4 RDOS)	2,100	400
3574	DOS with Magnetic Tape Support	700	150
3597	DOS Extended BASIC	1,360	100
3601	RJE80	1,250	1,000
3619	SAM Sensor Access Manager	NC	NC
3657	DOS FORTRAN IV with SFP	750	200
3709	RDOS Business BASIC	4,200	2,500
3720	Real-Time Operating System (Mapped RTOS)	750	NC
3724	DOS Business BASIC	1,600	550
3725	HASP II Workstation Emulator	1,500	1,250
3740	RDOS MBC/1 Monitor Emulator	75	NC
3743	DOS-Disk Based Operating System	700	150
3770	DOS FORTRAN IV with HFP	750	200
3777	Nova 4 MP/PASCAL	1,000	400
3778	Nova 4 MP/FORTRAN IV	500	200
3795	Nova 4 MP/OS Operating System and Utilities	1,000	600
3802	DTOS for the Nova 4	NC	NC
3813	RDOS X.25	1,000	600
3830-00F	Nova 4 IC/COBOL — Disk Swapping	7,000	5,000
3830-08F	Nova 4 IC/COBOL — Memory Mapping	8,000	6,000
3831	Nova 4 IC/RJE80 (Interactive COBOL)	2,150	2,000
3832	Nova 4 IC/HASP-II (Interactive COBOL)		