

October 22, 1963

DIGITAL Computer Bulletin

PDP-1 Approved Options

The PDP-1D is fast becoming a popular system, because of a flexible method of processor memory sharing. Gerry Moore has designed this system for Stanford, BBN, and he should be contacted for details.

From: R. F. Maxcy
To: Distribution
All Sales Personnel
All Engineers

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1. PDP-1D
Includes reader, punch, typewriter,
automatic multiply/divide. \$100,000.
2. Memory renaming facility 5,000.
3. Restricted Program Mode 3,375.
4. Special Instructions
 - a. szi 200.
 - b. cmi 200.
 - c. tia, tai, & tsw 1,000.
 - d. lch, dch, and facility for Ring Mode 5,600.
 - e. dpf, lpf 1,000.

\$8,000.

- f. ida 200.
- g. idc 200.
5. Clock 2,000.
6. 16 Channel SBS 15,300.

PDP-1D Memory Pricing Schedule

Memory Module 170A - 4K 30,000.

170B - 8K	40,000.
170C - 12K	50,000.
170D - 16K	60,000.

Memory Control 121A	8,000.
121B	10,000.
121C	12,000.
121D	14,000.

Minimum System PDP-1D (170A, 121A)

PDP-1D	100,000.
170A	30,000.
121A	8,000.
Total	\$138,000.