EL & CLASS DOCU-MENTS INDEX

TABLE 1 ABSTRACTS OF DEC STANDARDS

SECTION 0 REV. C

TITLE: EI & 7665 CLASS DOCUMENTS INDEX

ABSTRACT: This index includes a list of all Digital Standards, A-SP-7665XXX-XX-XXXX Specifications, and manuals maintained by Standards & Methods Information and Control. References to all documents contain latest version dates, responsible person, department, and the standards management group responsible for the document. Standards are listed with abstracts.

FOR INTERNAL USE ONLY

DATE	ECO #	ORIGINATOR	APPROVED	REV
30-Jun-80	ML001	Digital Stds. Administration	Joe Kurta	B
27-Mar-81	ML002	Digital Stds. Administration	Joe Kurta	C

Document Identifier

Size	Code	Number	Rev
A	DS	ELINDEX-0-0	C

TABLE OF CONTENTS/REVISION STATUS

1 INTRODUCTION

1.1 PURPOSE

This index contains a complete list of Digital Standards, A-SP-7665XXX-XX-XXXX Specifications, and other levels of documentation under EL Class control arranged to facilitate the location of pertinent documentation.

1.2 SCOPE

Table 1 lists each Digital Standard in numerical order with an abstract, revision level, latest ECO date, level and category of information, department responsible for implementing the standard, person responsible for the standard's technical content, and the standards management group responsible for maintaining the standard.

Table 2 lists all EL Class Manuals by title and document number. Each reference lists the revision level, the latest ECO date, and the responsible department and person for each manual.

Table 3 lists each A-SP-7665XXX-XX-XXX Specification in numerical order with title, revision level, latest ECO date, responsible department, person, and the standards management group responsible for maintaining the specification.

Section 1 (a separate document) contains two tables to help individuals find information.

Table 1-1 lists standards according to specific technical areas covered by Digital Standards. These areas include Design/Drafting Services, Documentation, Field Service Product Support, Inspection/Quality Control, Hardware Engineering Design, Manuals, Manufacturing, Project Management, and Software Engineering.

Table 1-2 lists subjects keywords for Standard, Manual or A-SP-7665XXX-XX-XXXX documents and is arranged alphanumerically to help readers locate the appropriate source of information for each subject.

1.2.1 Notations; Status And Distribution Restrictions

Proposed standards that are not yet approved are listed as "NEW STD IN PROCESS".

Approved standards that are currently being revised are listed with a note, "CHANGE IN PROCESS". If the change is not actively being worked but planned it will be identified: "CHANGE PLANNED".

For "CHANGE IN PROCESS" documents, the revision of interim passes will be identified by the (XØØ) scheme defined in DEC STD Ø14. The latest pass on record with DEC Standards is indicated in this index.

Those standards that are company confidential and released on a "need to know" basis are listed with the note, "RESTRICTED DISTRIBUTION".

1.3 RESPONSIBILITIES

1.3.1 Digital Standards Administration

The Digital Standards Administrator in EI Standards and Methods Information and Control is responsible for maintaining and publishing this index on a regular basis in accordance with DEC STD ØØ1, Section Ø.

1.3.2 Standards Management Groups

Standards Management groups are responsible for the management of the update and maintenance of Standards, Policies, Guidelines, etc. in their indicated sphere of responsibility. This includes establishing review cycles for existing standards, identifying the need for new standards, planning and securing resources for development of new standards, insuring that existing documents have owners and responsible departments, retiring obsolete documents and interfacing with other Standards Management Groups and Digital Standards Administration to coordinate writing reviews and distribution/control issues.

Standards and Methods provides an individual to assist the Standards Group Managers in coordinating review and distribution/control issues.

Existing Standards Management Groups and Standards and Methods contacts are:

Software and Architecture Standards

Pat White
ML12-3/E51
DTN: 223-4094

Standards and Methods Contact
Josephine McCarthy
ML4-4/E99 DTN: 223-2029

Hardware Design Assurance Standards

Paul Rey	Standards and Methods Contact
ML8-4/H19	Steve Millard
DTN: 223-2348	ML3-2/E56 DTN: 223-8501

Eng. Information and Documentation Standards

Mgr.: Joe Kurta	Standards and Methods Contact
ML4-4/E99	Joe McCullough
DTN: 223-8895	ML3-2/E56 DTN: 223-3947

Efforts are in process to identify other Standards Management Groups.

Groups are proposed for the following categories:

- Manufacturing Process and Quality Assurance

  Contact: Dennis Majikas
  ML4-4/E99
  DTN: 223-8004

- Component Engineering and Specifications

  Contact: Dennis Majikas
  ML4-4/E99
  DTN: 223-8004

- Product/Program Management

  Contact: Jack Downing
  ML3-2/E56
  DTN: 223-6843

Until group managers are identified; all questions and concerns in this area should be forwarded to the listed Standards and Methods group contact for that category.

1.3.3 Responsible Departments

Each standard will have a department or organization responsible for the content of a specific standard. This organization is required to identify an individual to handle questions, problems, etc. associated with that standard should the listed individual be reassigned or leave the company.

Note that DEC STD Ø01 requires listing of those who have specific responsibilities for following or conforming to the requirements of that standard.

Table 1

Abstracts of Digital Standards

(Status as of 27-Mar-81)

Table 1. Abstracts of Digital Standards

DEC STD 001, Section 0 — Digital Standards System Policy
Rev. J. 27-Sep-79 (Level 1: Policy)

ABSTRACT: Establishes the policy regarding Digital Standards, describes the categories and levels of information included in Digital Standards, and defines the responsibilities and roles assigned to the various committees and organizations involved in the management and administration of the Digital Standards System.

Department: Standards and Methods Information and Control
Responsible Person: Joe Kurta
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD 001, Section 1 — Creation and Change Procedures
Rev. J 27-Sep-79 (Level 1: Procedure)

ABSTRACT: Describes procedures for the creation, revision, release, and distribution of Digital Standards.

Department: Standards and Methods Information and Control
Responsible Person: Joe Kurta
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD 001, Section 2 — Format and Style Requirements
Rev. J 27-Sep-79 (Level 1: Requirements)

ABSTRACT: Describes the format and style requirements and general organization of Digital Standards.

Department: Standards and Methods Information and Control
Responsible Person: Joe Kurta
Stds. Mgmt Group: Engineering Information and Documentation Standards

Table 1 (Cont'd)

DEC STD 002

AC Power Wiring, Safety Grounding, Receptacle and Electrical Rating Information Requirements
Rev. C 4-Dec-80 (Level 1: Requirements)

ABSTRACT: Defines requirements for AC power wiring and grounding, types of outlets, power cords and plugs, and nameplates to be used on Digital.

Department: Power Supply Engineering
Responsible Person: F. Loya
Stds. Mgmt. Group: Hardware Design Assurance

DEC STD 003

Hardware Manual Standard
Rev. C 7-Feb-80 (Level 1: Requirements)

ABSTRACT: Establishes planning, control, contents, and format requirements for the publication of all hardware manuals and hardware-related customer user guides.

Department: Communciations Development and Publishing
Responsible Person: Clem Daems
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD 004
CHANGE PLANNED
NOT SCHEDULED

Circuit Design Guidelines
Rev. A 19-Jun-70 (Level 1: Guidelines)

ABSTRACT: Presents design information, rules, and formulas for use in circuit design. Includes guidelines for using active and passive components, printed circuit boards, and information about circuit performance.

Department:
Responsible Person: Don Marshall
Stds. Mgmt. Group: Hardware Design Assurance

Table 1 (Cont'd)

DEC STD 005, Section 0 — Product Waivers - Types and Conditions
NEW STD IN PROCESS — Rev. A(X01) 15-Oct-80 (Level 1: Requirements)

ABSTRACT: Describes the required procedure for reporting products that do not comply with applicable Digital Standards.

FOR INTERNAL USE ONLY

STATUS: BEING WORKED

Department: Field Service Installation Quality
Responsible Person: Bob Brown
Stds. Mgmt. Group: Hardware Design Assurance

DEC STD 005, Section 1 — Product Waivers - Technical
NEW STD IN PROCESS — REV. A(X01) 15-Oct-80 (Level 1: Procedures)

ABSTRACT: Describes procedures for processing technical waivers.

FOR INTERNAL USE ONLY

STATUS: BEING WORKED

Department: Field Service Installation Quality
Responsible Person: Bob Brown
Stds. Mgmt. Group: Hardware Design Assurance

DEC STD 005, Section 2 — Product Waivers - Product Line
NEW STD IN PROCESS — Rev. A(X01) 15-Oct-80 (Level 1: Procedures)

ABSTRACT: Describes procedures for processing Product Line waivers.

FOR INTERNAL USE ONLY

STATUS: BEING WORKED

Department: Field Service Installation Quality
Responsible Person: Bob Brown
Stds. Mgmt. Group: Hardware Design Assurance

Table 1 (Cont'd)

DEC STD 005, Section 3
NEW STD IN PROCESS

Product Waivers - Repetitive, Informational, and Other
Rev. A(X01) 15-Oct-80 (Level 1: Procedures)

FOR INTERNAL USE ONLY

STATUS: BEING WORKED

Department: Field Service Installation Quality
Responsible Person: Bob Brown
Stds. Mgmt. Group: Hardware Design Assurance

DEC STD 005, Section 4

Product Waivers - Reporting Products Non-Compliant With Digital Standards
Rev. A(X01) 15-Oct-80 (Level 1: Procedures)

ABSTRACT: Describes the required procedure for reporting products that do not comply with applicable Digital Standards.

FOR INTERNAL USE ONLY

STATUS: BEING WORKED

Department: Hardware Design Assurance
Responsible Person: Dick Amann
Stds. Mgmt. Group: Hardware Design Assurance

DEC STD 005, Section 5
NEW STD IN PROCESS

Quality Assurance Operational Alert - Product Hold Procedure
Rev. A(X01) 15-Oct-80 (Level 1: Requirements, Procedures)

ABSTRACT: Describes how an Operational Alert (OPAL) message is authorized and issued to stop shipment of a product that has a safety defect or a serious functional defect.

FOR INTERNAL USE ONLY

STATUS: BEING WORKED - WILL SUPERSEDE A-SP-7665298-0-0 WHEN RELEASED

Department: Central Quality Assurance
Responsible Person: Dennis Majikas
Stds. Mgmt. Group:

Table 1 (Cont'd)

DEC STD 005, Section 6
NEW STD IN PROCESS

In-Plant Product Safety Hold Procedure
Rev. A(X01) 15-Oct-80 (Level 1: Procedure)

ABSTRACT: Describes how the product safety hold procedure is implemented.

FOR INTERNAL USE ONLY

STATUS: BEING WORKED - WILL SUPERSEDE A-SP-7665310-0-0 WHEN RELEASED

Department: Corporate Product Safety
Responsible Person: Ron Minezzi
Stds. Mgmt. Group: Hardware Design Assurance

DEC STD 006

Part and Document Naming Conventions
Rev. A 16-Oct-80 (Level 2: Requirements)

ABSTRACT: Provides rules and requirements for naming parts and engineering drawings with names that are brief, consistent, and follow a uniform format. It applies to the naming of all 50-79, 94, and 95 Inventory Class parts and drawings.

Department: Engineering Information Control
Responsible Person: Frank Alla
Stds. Mgmt. Group: Eng. Information & Documentation Standards

DEC STD 007

Design Review Process
Rev. C 10-Nov-74 (Level 1: Policy & Requirements)

ABSTRACT: Describes what projects require design reviews, how a design review committee is formed, when design reviews are held, and what the design review committees responsibilities are.

Department: Chief Engineer's Office
Responsible Person: Carl Noelcke
Stds. Mgmt. Group: Product/Program Management

Table 1 (Cont'd)

DEC STD 008
CHANGE PLANNED
NOT SCHEDULED

Project Scheduling System
Rev. A 10-Nov-74 (Level 1. Requirements)

ABSTRACT: Intended to facilitate the planning, execution, and review of development projects. All discrete projects which are expected to involve total expenditures of $10,000 or more must be included in the system. Describes scheduling techniques that are used as well as scheduling reviews.

Department: Engineering Operations Management Committee
Responsible Person: Charlie Picariello
Stds. Mgmt. Group: Product/Program Management Standards

DEC STD 009
CHANGE PLANNED
NOT SCHEDULED

Project Specification
Rev. A 31-May-68 (Level 1: Policy & Requirement)

ABSTRACT: Describes requirements for a Project Specification, including approval procedure, hardware, software, cost estimate, schedule and design reviews.

Department: Chief Engineer's Office
Responsible Person: Carl Noelcke
Stds. Mgmt. Group: Product/Program Management Standards

DEC STD 010, Section 0
CHANGE IN PROCESS
TO RESTRUCTURE INTO
SECTIONS

Engineering Documentation Checking: Requirements
Rev. A 25 May 65 (Level 2: Requirements)

ABSTRACT: Defines the responsibilities of the checker in the acceptance and release of engineering documentation. Describes what information is needed from Engineering and Design/Drafting to meet this obligation.

STATUS: REV. B (X02) 27-Oct 80 OUT FOR LIMITED REVIEW 3 NOV 80

Department: Standards and Methods Information and Control
Responsible Person: Joe Kurta
Stds. Mgmt. Group: Engineering Information and Documentation Standards

Table 1 (Cont'd)

DEC STD Ø1Ø, Section 1
NEW SECTION OF STD.
IN PROCESS

Document Requirements Checklist: Mechanical, Electrical, General and Special Purpose
Rev. A(XØ2) 27-Oct-8Ø (Level 2: Guidelines)

ABSTRACT: Provides guidelines for checkers in meeting document requirements as specified in Digital and other documentation standards.

STATUS: REV. A (XØ2) 27-Oct-8Ø OUT FOR LIMITED REVIEW 3 NOV 8Ø

Department: Standards and Methods Information and Control
Responsible Person: Joe Kurta
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD Ø1Ø, Section 2
NEW SECTION PLANNED

Document Requirements Checklist: Printed Circuit Design
REV. (Level 2: Guidelines)

STATUS: NOT STARTED - ASSIGNED 2Ø MAY 8Ø

Department: Standards and Methods Information and Control
Responsible Person: Joe Kurta
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD Ø11
NEW STANDARD PLANNED

VAX-11 Procedure Calling and Condition Handling Standard
Rev. (Level 1: Requirements,

ABSTRACT: Not available

Department:
Responsible Person: Tom Hastings
Stds. Mgmt. Group: Software and Architecture Standards

Table 1 (Cont'd)

DEC STD Ø12, Section Ø
CHANGE IN PROCESS
TO RESTRUCTURE INTO
SECTIONS

Unified Numbering Code: Policy on Part and Document Identification Conventions
Rev. E 12-Jul-78 (Level 1: Policy)

ABSTRACT: Governs the assignment of identifying numbers to parts and all related drawings employed for reference and construction purposes. The code has been purposely devised to accommodate the diversified requirements of Digital Equipment Corporation Organizational groups, while retaining uniform numbering formats which can be easily interpreted by all concerned.

STATUS: CHANGE IN PROCESS AT REV. F (X13) 15-Mar-81
AWAITING DETAILS OF IMPLEMENTATION

Department: Standards and Methods Information and Control
Responsible Person: Bill Buck
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD Ø12, Section 1
CHANGE IN PROCESS

Mnemonic Drawing Codes
Rev. H 6-Dec-79 (Level 1: Requirements)

ABSTRACT: Defines the requirements for the assignment of Mnemonic Codes to all documentation under the scope of DEC STD Ø12. No code is considered valid on documentation covered by DEC STD Ø12 unless listed herein.

STATUS: Rev. J (XØØ) BEING PREPARED

Department: Standards and Methods Information and Control
Responsible Person: Joe Kurta
Stds. Mgmt. Group: Engineering Information and Documentation Standards

Table 1 (Cont'd)

DEC STD Ø12, Section 2 — Inventory Class Codes
CHANGE IN PROCESS — Rev. F 6-Dec-79 (Level 1: Requirements)

ABSTRACT: Lists assigned part identifers and document identifer codes authorized for use within Digital. It identifers person/organizations responsible for issuing numbers within each class. References to other Digital Standards are provided for details regarding special class code applications.

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STATUS: REV. H(XØØ) 16 JAN 81 OUT FOR LIMITED REVIEW 16 JAN 81

Department: Standards and Methods Information and Control
Responsible Person: Joe Kurta
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD Ø12, Section 3 — Package System Identification
Rev. C 6-Sep-79 (Level 2: Requirements)

ABSTRACT: Standardizes the application of the Unified Numbering Code (UNC) for identification of part numbers for packaged systems marketed and sold by Digital.

Department: Packaged Systems
Responsible Person: Walt Colby
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD Ø12, Section 4 — Software Distribution Center Part Numbering Conventions
Rev. B 24-Sep-79 (Level 2: Requirements)

ABSTRACT: Standardizes the application of the Unified Numbering Code (UNC) for identification of part numbers assigned and controlled by the Software Distribution Center.

Department: Software Distribution Center
Responsible Person: Tom Marrone
Stds. Mgmt. Group: Engineering Information and Documentation Standards

Table 1 (Cont'd)

DEC STD Ø12, Section 5 — Manufacturing Control Part Numbering
Rev. A 17-Apr-79 (Level 2: Requirements)

ABSTRACT: Establishes the procedure for assigning Unified Numbering Code (UNC) part numbers by Manufacturing to permit greater flexibility in measuring and controlling material and process flow.

Department: Manufacturing Accounting
Responsible Person: Jim Marine
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD Ø12, Section 6
NEW STD IN PROCESS — Computer Special Systems Part Numbering Conventions
Rev. A(XØ4) 2-Apr-81 (Level 2: Requirements)

ABSTRACT: Standardizes the application of the Unified Numbering Code (UNC) for identification of part numbers assigned and controlled by Computer Special Systems (CSS).

STATUS: REV A(XØ4) 2-Apr-81 OUT FOR ENG. COMM. REVIEW 2-Apr-81.
Department: CSS Engineering
Responsible Person: Andy White
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD Ø12, Section 7 — Unified Numbering Code: 74 Class Part Numbering Conventions and Assignment Procedures
Rev. A 19-Feb-81 (Level 1: Requirements and Procedures)

FOR INTERNAL USE ONLY

ABSTRACT: Defines the requirements for the assignment and control of 74 class part identifiers.

Department: Specification Control Systems
Responsible Person: Jerry Lepire
Stds. Mgmt. Group: Engineering Information and Documentation Standards

Table 1 (Cont'd)

DEC STD Ø12, Section 8
NEW SECTION IN PROCESS

Field Service Numbering Conventions and Assignment Procedures
Rev. A (XØØ) 8-Oct-8Ø (Level 2: Requirements)

ABSTRACT: Defines the requirements for assignment and control of Field Service class part identifiers

STATUS: REV A (XØ1) BEING PREPARED

Department:
Responsible Person: Ann Bostwick
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD Ø12, Section 9

Unified Numbering Code: 94 Class Part Numbering Conventions and Assignment Procedures
Rev. A(XØ2) 26-Mar-81 (Level 2: Requirements)

FOR INTERNAL USE ONLY

ABSTRACT: Defines the requirements for the assignment and control of 94 class tooling part identifiers.

STATUS: REV A(XØ2) 26-Mar-81 TO STEERING COMMITTEE REVIEW APR 81

Department: Central Tool Control
Responsible Person: Charles Kaberry
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD Ø13, Section Ø

Introduction
Rev. C 26-Mar-81 (Level 2: Requirements)

ABSTRACT: Lists and describes all authorized engineering drawing sizes and formats and essential preprinted forms used by Engineering Services and Engineering organizations.

Department: Standards and Methods Information and Control
Responsible Person: Joe Kurta
Stds. Mgmt. Group: Engineering Information and Documentation Standards

Table 1 (Cont'd)

DEC STD Ø13, Section 1 — General Purpose Drawing Sizes and Format
Rev. C 26-Mar-81 (Level 2: Requirements)

ABSTRACT: Describes the drawing sizes and formats established for producing general purpose engineering drawings.

Department: Standards and Methods Information and Control
Responsible Person: Joe Kurta
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD Ø13, Section 2 — Standard Engineering Drawing Formats, Decals, and Forms - Preprinted Special Purpose Formats
Rev. C 26-Mar-81 (Level 2: Requirements)

ABSTRACT: Lists all preprinted engineering drawing formats.

Department: Standards and Methods Information and Control
Responsible Person: Joe Kurta
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD Ø13, Section 3 — Standard Engineering Drawings Formats, and Forms Computer Output Drawing Formats
Rev. C 26-Mar-81 (Level 2: Requirements)

ABSTRACT: Establishes the format for each type of computer - produced engineering drawing.

Department: Standards and Methods Information and Control
Responsible Person: Joe Kurta
Stds. Mgmt. Group: Engineering Information and Documentation Standards

Table 1 (Cont'd)

DEC STD Ø13, Section 4 — Essential Preprinted Forms For Engineering
Rev. C 26-Mar-81 (Level 2: Requirements)

ABSTRACT: Lists essential preprinted forms used throughout the engineering organizations. Included are samples of any forms that are referred to by the standards in the Digital Standards system. Examples are included for identification.

Department: Standards and Methods Information and Control
Responsible Person: Joe Kurta
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD Ø13, Section 5 — Printed Circuit Mats
Rev. C 26-Mar-81 (Level 2: Requirements)

ABSTRACT: Lists mats currently used for digitizing printed circuit layouts and describes how they can be ordered.

Department: Standards and Methods Information and Control
Responsible Person: Joe Kurta
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD Ø14 — Revisions on Engineering Drawings
Rev. D 26-Mar-81 (Level 1: Requirements)

ABSTRACT: Establishes a revision control scheme for engineering drawings and documents within the preliminary, release, and ECO cycles.

Department: Engineering Information Control
Responsible Person: Sue McElroy
Stds. Mgmt. Group: Engineering Information and Documentation Standards

Table 1 (Cont'd)

DEC STD Ø15 — Abbreviations and Units of Measurement
CHANGE IN PROCESS — Rev. B 13-Jan-77 (Level 1: Requirements)

ABSTRACT: Requires that documentation for commerce in European Economic Community (EEC) use SI (metric) units of measurement and unit symbols for all quantities. This standard also provides abbreviations for use on engineering drawings. Conversion factors for commonly used USA customary units are provided.

STATUS: CHANGE IN PROCESS

Department: Standards and Methods Information and Control
Responsible Person: Allan Kent
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD Ø16 — Printed Wiring Terminology
NEW STD IN PROCESS — Rev. A (XØ5) 11-Nov-8Ø (Level 2: Guidelines)

ABSTRACT: Establishes terms and definitions for consistent usage of Printed Wiring terms in Engineering and Manufacturing documents.

STATUS: Rev. A (XØ5) 11-Nov-8Ø OUT FOR ENG. COMMITTEE REVIEW 13-FEB-81

Department: Process Quality Mgmt.
Responsible Person: Dave Nevala
Stds. Mgmt. Group: Mfg. Process and Quality Assurance

DEC STD Ø17 — Corporate Quality Control Policy
NEW STD PLANNED

ABSTRACT: Not available

STATUS: AWAITING INPUT FROM ORIGINATOR

Department: Central Quality Assurance
Responsible Person: Dennis Majikas
Stds. Mgmt. Group: Mfg. Process and Quality Assurance

Table 1 (Cont'd)

DEC STD Ø18 — Lettering — REPLACED BY DEC STD 182
OBSOLETE 21-Jun-79

DEC STD Ø19 — Decimal Dimensioning Standard
CHANGE PLANNED TO OBSOLETE AND REPLACE WITH NEW DEC STD 114 SECTION 1
Rev. A 1968 (Level 1: Requirements)

ABSTRACT: Describes procedures for using decimal dimensions and rounding off even and odd decimals to a lesser number of places. A fraction to decimal equivalent chart is part of this standard.

STATUS: TO BE OBSOLETED ONCE DEC STD 114, SEC. 1 APPROVED.

Department: Standards and Methods Information and Control
Responsible Person: Joe Kurta
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD Ø2Ø — Casting Standard
CHANGE PLANNED
Rev. A 9-Oct-72 (Level 1: Requirements)

ABSTRACT: Establishes rules and design guides to be used in the preparation of drawings to define machined castings.

Department: Standards and Methods Information and Control
Responsible Person: Joe Kurta/B. Majors
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD Ø21 — Harness Drawings - REPLACED BY DEC STD Ø22
OBSOLETE 21-Oct-80

Table 1 (Cont'd)

DEC STD 022, Section 0 — Cable and Harness Documentation: Part Identification Requirements
Rev. D 13-Dec-80 (Level 2: Requirements)

ABSTRACT: Defines the part and numbering system for cables and harnesses.

Department: Module Process Management
Responsible Person: Steve Spaulding
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD 022, Section 1 — Cable and Harness Documentation: Drawing Requirements
Rev A. 18-Sep-80 (Level 2: Requirements)

ABSTRACT: Defines the drawing requirements for cable and harness design/assembly documentation.

Department: Module Process Management
Responsible Person: Steve Spaulding
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD 023 — Schematics - REPLACED BY DEC STD 056
OBSOLETE 1-Jul-80

DEC STD 024, Section 0 — DRB 106A, DRB 107, and DRB 108A Format Drawing Directory: Requirement
Rev. C 18-Sep-80 (Level 1: Requirements)

ABSTRACT: Describes the drawing directory used to list all drawings and variations required to manufacture a unit or option.

Department: Standards and Methods Information and Control
Responsible Person: Joe Kurta
Stds. Mgmt. Group: Engineering Information and Documentation Standards

Table 1 (Cont'd)

DEC STD Ø24, Section 1 — DRB 126A Format Drawing Directory: Requirements
Rev. A 18-Sep-8Ø (Level 1: Requirements)

ABSTRACT: Defines the information content requirements for drawing directory format DRB 126A, which is used to list all drawings and documentation required to manufacture modules.

Department. Standards and Methods Information and Control
Responsible Person: Joe Kurta
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD Ø25, Section Ø — Parts Lists - General Requirements
Rev. C 26-Mar-81 (Level 1: Requirements)

ABSTRACT: Establishes the information content and format for parts lists used in the design and manufacture of Digital hardware products. The general requirements are provided for both manual and automated parts lists.

Department: Engineering Information Control
Responsible Person: Sue McElroy
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD Ø25, Section 1 — Manual Parts Lists
Rev. B 18-Sep-8Ø (Level 1: Requirements)

ABSTRACT: Provides detailed information requirements for manual parts lists.

Department: Engineering Information Control
Responsible Person: Sue McElroy
Stds. Mgmt. Group: Engineering Information and Documentation Standards

Table 1 (Cont'd)

DEC STD Ø25, Section 2 — Automated Parts Lists
Rev. B 18-Sep-80 (Level 1: Requirements)

ABSTRACT: Provides detailed information requirements for automated parts lists.

Department: Engineering Information Control
Responsible Person: Sue McElroy
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD Ø26
NEW STD IN PROCESS — Hybrid Assembly Documentation: Requirements Process
Rev. A (XØ3) 1-Sep-80 (Level 1: Requirements and Procedures)

ABSTRACT: Defines the requirements and process for release and control of a hybrid assembly and its related substrate.

STATUS: REV. A (XØ3) 1-Sep-80 OUT FOR EC REVIEW 15-SEP-80

Department: Standards and Methods Information and Control
Responsible Person: Joe Kurta
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD Ø27
NEW STD PLANNED
NOT SCHEDULED — LSI/VLSI Documentation: Requirements

ABSTRACT: Defines the requirements and process for release and control of Engineering Documentation for new in-house custom LSI/VLSI design information.

STATUS: NOT STARTED

Department: Standards and Methods Information and Control
Responsible Person: Joe Kurta
Stds. Mgmt. Group: Engineering Information and Documentation Standards

Table 1 (Cont'd)

DEC STD 028
NEW STD PLANNED
NOT SCHEDULED

Phase Review Process
Rev. A (X01) 8-Sep-80 (Level 1: Requirements)

ABSTRACT: Defines the overall structure of the Phase Review Process for both hardware and software products. It names the phases, establishes phase exit criteria, identifies a minimum set of milestones within each phase, addresses phase transition meetings and identifies reference information.

STATUS: NOT AVAILABLE - AWAITING INPUT FROM EOMC

Department: Engineering Operations Management Committee
Responsible Person: Charlie Picariello
Stds. Mgmt. Group: Product and Program Management

DEC STD 029
NEW STANDARD
IN PROCESS

Graphic COM System: Requirements and Procedure
Rev. A(X02) 10-Mar-81 (Level 2: Requirements and Procedure)

ABSTRACT: Defines requirements and procedures for processing released computerized design information on graphic computer output microfilm (COM).

FOR INTERNAL USE ONLY

STATUS: Rev. A(X02) 10-Mar-81 OUT TO LIMITED REVIEW 27-MAR-81

Department: Advanced Documentation Systems
Responsible Person: Roy Smith
Stds. Mgmt. Group: Engineering Information and Documentation Standards

Table 1 (Cont'd)

DEC STD Ø30
CHANGE PLANNED
NOT STARTED

Module Manufacturing Standard
Rev. E Ø8-Jan-81 (Level 1: Requirements)

ABSTRACT: Describes the module manufacturing capability of Digital and the circuit layout standards and procedures which allow that capability to be optimized. Contains all the rules that ensure the circuit design engineer a fast and economical module, and Product Line Manager volume deliveries during the production life of his/her product.

STATUS: CHANGE REQUESTS RECEIVED; ECOs BEING PREPARED

Department: TSS Producibility Engineering
Responsible Person: Dick Dunlop
Stds. Mgmt. Group: Mfg. Process and Quality Assurance

DEC STD Ø31, Section Ø
CHANGE IN PROCESS

Product Serialization and Identification
Rev. C 9-Oct-8Ø (Level 2: Requirements)

ABSTRACT: Provides a uniform serial numbering and identification scheme and format for all saleable Digital products.

STATUS: ECO ML ØØ3 TO ENG. COMMITTEE AND QC MGRS 19 REVIEW MARCH 1981

Department: Manufacturing Product Safety
Responsible Person: Bill Fischer
Stds. Mgmt. Group: Mfg. Process and Quality Assurance

DEC STD Ø31, Section 1

Product Serialization and Identification - Product Variation Changes
Rev. A 9-Oct-8Ø (Level 2: Requirements)

ABSTRACT: Provides serial numbering and identification rules for product modified after serial tag has been applied but before shipment.

Department: Manufacturing Product Safety
Responsible Person: Bill Fischer
Stds. Mgmt. Group: Mfg. Process and Quality Assurance

Table 1 (Cont'd)

DEC STD 031, Section 2 — Product Serialization and Identification - Plant Code Identifiers
Rev. A 9-Oct-80 (Level 2: Requirements)

ABSTRACT: Lists plant code identifiers used to mark products with their place of manufacture.

Department: DEC Standards Administration
Responsible Person: Bev Simonetti
Stds. Mgmt. Group: Mfg. Process and Quality Assurance

DEC STD 032 — Vax Architecture Standard
10-Jul-80 (Level 1: Requirements)

ABSTRACT: Provides a definition of the VAX architecture. Provides a complete description of the VAX central processor hardware as seen by machine language programs.

Section 0	General	Rev A	10-Jul-80
Section 1	INTRODUCTION	Rev A	01-Feb-80
Section 2	BASIC ARCHITECTURE	Rev A	29-Feb-80
Section 3	INSTRUCTION FORMATS AND ADDRESSING MODES	Rev A	5-May-80
Section 4	INSTRUCTIONS	Rev A	8-Feb-80
Section 5	MEMORY MANAGEMENT	Rev A	8-Feb-80
Section 6	EXCEPTIONS AND INTERRUPTS	Rev A	12-May-80
Section 7	PROCESS STRUCTURE	Rev A	21-May-80
Section 8	SYSTEM ARCHITECTURE IMPLICATIONS	Rev A	17-Jun-80
Section 9	PRIVILEGED REGISTERS AND CONSOLE	Rev A	23-Jun-80
Section 10	PDP-11 COMPATIBILITY MODE	Rev A	20-Feb-80
Appendix B	ASSEMBLER NOTATION	Rev A	31-Oct-78

Table 1 (Cont'd)

Appendix F	INSTRUCTION SETS AND OP CODE ASSIGNMENTS	Rev A	17-Jun-80
Appendix H	MULTIPRECISION ARITHMETIC	Rev A	21-Mar-77
Appendix I	PDP-11 TO VAX-11 CONVERSION GUIDE	Rev A	24-Mar-77
Appendix J	ADDRESS VALIDATION RULES	Rev A	1-Feb-77

Department: VAX Architecture
Responsible Person: Tom Eggers
Stds. Mgmt. Group: Software and Architecture Standards

DEC STD 033, Section 0 — Microfilm Aperture Cards - Creation and Distribution Process
Rev. A 10-Apr-80 (Level 2: Requirements)

ABSTRACT: Describes microfilm aperture card creation and distribution process for engineering documentation. It also defines the format and quality requirements for microfilm aperture cards, and provides the procedures for establishing and maintaining a Microfilm Reference Library.

Department: Engineering Information Micrographics
Responsible Person: Bob Marshall
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD 033, Section 1 — Microfilm Aperture Card Requirements
Rev. A 10-Apr-80 (Level 2: Requirements)

ABSTRACT: Defines the format and quality requirements for microfilm aperture cards of engineering documentation.

Department: Engineering Information Micrographics
Responsible Person: Irene Fredette
Stds. Mgmt. Group: Engineering Information and Documentation Standards

Table 1 (Cont'd)

DEC STD 033, Section 2 — Microfilm Reference Library Setup and Maintenance Procedures
Rev. A 10-Apr-80 (Level 2: Procedures)

ABSTRACT: Provides procedures for establishing a Microfilm Reference Library for microfilm aperture cards.

Department: Engineering Information Micrographics
Responsible Person: Irene Fredette
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD 034
NEW STANDARD
IN PROCESS

Plotted Master Artwork Specifications and Acceptance Criteria
Rev. A(X00) 22-Oct-80 (Level 3: Requirements)

ABSTRACT: A specification of materials to be used, quality assurance inspection provisions, and the acceptance criteria for plotted master artwork used to manufacture printed wiring boards.

STATUS: Rev. A(X00) 22-Oct-80 OUT FOR REVIEW 13-Nov-80

Department: TSS Manufacturing Tools Generation
Responsible Person: Leo Crosby
Stds. Mgmt. Group: Mfg. Process and Quality Assurance

DEC STD 050
OBSOLETE 26-Mar-81

Standard Engineering Preprinted Formats
Replaced by DEC STD 013

DEC STD 051
CHANGE IN PROCESS

Standard Coded Character Set
Rev. A 6-Nov-70 (Level 1: Requirements)

ABSTRACT: Defines preferred character sets to be used in hardware printers and displays and in software programming. This standard embodies the American National Standard Code for Information Interchange (ANSI X.34-1968) as a subset.

Department: VAX Architecture
Responsible Person: Tom Eggers
Stds. Mgmt. Group: Software & Architecture Standards

Table 1 (Cont'd)

DEC STD Ø52, SECTION Ø — Operational Requirements For Serial Terminals and Serial System Interfaces Operating as DTEs Connected To EIA RS-232-C or CCITT V.28 Point-to-Point Modems; Terminology and Requirements
Rev. A 6-Nov-8Ø (Level 1: Requirements)

ABSTRACT: Signal definitions and special terms used in Serial Data Communications.

 Department: Hardware Design Assurance
Responsible Person: Ranjit Singh
Stds. Mgmt. Group: Hardware Design Assurance

DEC STD Ø52, Section 1 — Operational Requirements For Serial Terminals and Serial System Interfaces Operating as DTEs Connected To EIA RS-232-C or CCITT V.28 Point-to-Point Modems; Operational Requirements
Rev. A 6-Nov-8Ø (Level 1: Requirements)

INTERNAL USE ONLY

ABSTRACT: Defines the operational interface characteristics of serial terminals and serial system interfaces operating as manual originate or answer or as automatic answer data terminal equipments (DTEs) connected to either "data leads only" or "full-modem control" point-to-point modems (DCEs) whose interfaces generally conform to EIA standard RS-232-C or CCITT recommendation V.28. This standard also covers manual and automatic disconnection of the DTE at the end of a call. The operational characteristics also apply to many cases where the electrical interface does not conform to RS-232-C or V.28; for example, a modem integral to a terminal.

Department: Hardware Design Assurance.
Responsible Person: Ranjit Singh
Stds. Mgmt. Group: Hardware Design Assurance

Table 1 (Cont'd)

DEC STD Ø53
NEW STD
IN PROCESS
PROCESS

Electrical Requirements For Binary Interfaces That Conform To EIA RS-232-C or CCITT V.28
Rev. A(XØ3) 13-Jun-8Ø (Level 1: Requirements)

ABSTRACT: Defines the minimum electrical interface requirements for the drivers, receivers, and interconnecting cable used to connect DTEs to DCEs and modems in accordance with EIA RS-232-C or CCITT V.28.

STATUS: AWAITING INPUT FROM ORIGINATOR

Department: Hardware Design Assurance
Responsible Person: Ranjit Singh
Standards Mgmt. Group: Hardware Design Assurance

DEC STD Ø55
CHANGE PLANNED
NOT SCHEDULED

Purchase Specifications: Guidelines
Rev. B 24-May-79 (Level 1: Guidelines)

ABSTRACT: Establishes the general instructions and responsibilities for the preparation and control of Digital Purchase Specifications.

Department: Specifications Control Systems
Responsible Person: John Peachey
Stds. Mgmt. Group: Components Engineering and Specifications

DEC STD Ø56, Section Ø

Logic Symbology - Circuit Schematic Requirements
Rev. C 27-Jun-8Ø (Level 1: Requirements)

ABSTRACT: Establishes the format and requirements for Logic Symbology used by Digital Equipment Corporation including the requirements for schematic logic diagrams, and the composition and form of symbols. This section also establishes general guidelines for a Logic Symbology Handbook.

Department: Engineering Information Control
Responsible Person: Tom Witowski
Stds. Mgmt. Group: Engineering Information and Documentation Standards

Table 1 (Cont'd)

DEC STD 056, Section 1 — Symbology - Distinctive Shape Logic Symbols
Rev. C 27-Jun-80 (Level 1: Requirements)

ABSTRACT: Provides detailed requirements for the use of distinctive-shape logic symbols in schematic logic diagrams so that logic functions may be understood directly from either the shape of the symbol or the notation within the symbol.

Department: Engineering Information Control
Responsible Person: Tom Witowski
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD 056, Section 2 — Complex (Uniform-Shape) Logic Symbols
Rev. C 27-Jun-80 (Level 1: Requirements)

ABSTRACT: Provides detailed requirements for the use of complex (uniform-shape) logic symbols in schematic logic diagrams.

Department: Engineering Information Control
Responsible Person: Tom Witowski
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD 056, Section 3 — Discrete Electronic and Electromechanical Component Symbols
Rev. C 27-Jun-80 (Level 1: Requirements)

ABSTRACT: Provides detailed requirements for representing discrete electrical-mechanical components on schematic logic diagrams.

Department: Engineering Information Control
Responsible Person: Tom Witowski
Stds. Mgmt. Group: Engineering Information and Documentation Standards

Table 1 (Cont'd)

DEC STD Ø56, Section 4 — Electrical Interconnections Between Graphic Symbols
Rev. C 27-Jun-80 (Level 1: Requirements)

ABSTRACT: Specifies the requirements for electrical connections between logic symbols, and provides rules for the use of signal mnemonics in the connections.

Department: Engineering Information Control
Responsible Person: Tom Witowski
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD Ø56, Section 5 — Symbology - Waivers
Rev. C 27-Jun-80 (Level 1: Procedures & Requirements)

ABSTRACT: Establishes the procedures and requirements for obtaining waivers and exceptions to this standard.

Department: Engineering Information Control
Responsible Person: Tom Witowski
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD Ø56, Section 6 — Symbology - Glossary of Terms
Rev. C 27-Jun-80 (Level 1: Requirements)

ABSTRACT: Provides definitions for certain terms used in DEC STD Ø56.

Department: Engineering Information Control
Responsible Person: Tom Witowski
Stds. Mgmt. Group: Engineering Information and Documentation Standards

Table 1 (Cont'd)

DEC STD Ø56, Section 7

Symbology - Current Logic Function Labels and Current Pin Label Definitions
Rev. C 27-Jun-80 (Level 1: Requirements)

ABSTRACT: Provides a list of current logic function labels and pin label definitions.

Department: Engineering Information Control
Responsible Person: Tom Witowski
Stds. Mgmt. Group: Engineering Information and Documentation Control

DEC STD Ø59
CHANGE IN PROCESS TO RESTRUCTURE INTO SECTIONS

Incoming Inspection Procedure
Rev. A 29-Mar-71 (Level 1: Procedure)

DEC STD Ø59, Section Ø

Incoming Inspection Procedures; General Policy
Rev. B(XØ3) 18-Mar-81 (Level 1: Policy and Requirements)

ABSTRACT: Establishes the general policy regarding requirements and responsibilities for Incoming Inspection Procedures.

FOR INTERNAL USE ONLY

STATUS: REV B(XØ3) 18-MAR-81 OUT FOR LIMITED REVIEW 1-Apr-81

Department: Standards and Methods Information Control
Responsible Person: Joe Kurta
Stds. Mgmt. Group: Mfg. Process and Quality Assurance

Table 1 (Cont'd)

DEC STD 059, Section 1

PAVES Incoming Inspection Documentation Requirements
Rev. B(X05) 18-Mar-81 (Level 2: Requirements)

ABSTRACT: Describes requirements for the Incoming Inspection documentation on the Part Analysis Vendor Evaluation System (PAVES).

FOR INTERNAL USE ONLY

STATUS: REV B(X05) 18-MAR-81 OUT FOR LIMITED REVIEW 19 JAN 81

Department: Component Engineering
Responsible Person: Joe Belliveau
Stds. Mgmt. Group: Component Engineering and Specifications

DEC STD 059, Section 2

Incoming Inspection Procedures - Metal Fabrication And Plastics
Rev. A(X02) 18-Mar-81 (Level 2: Requirements and Procedures)

ABSTRACT: Establishes a uniform method for generating, controlling, and distributing Incoming Inspection Procedures (II's) for metal fabrication and plastics.

FOR INTERNAL USE ONLY

STATUS: REV A(X02) 18-MAR-81 OUT FOR LIMITE REVIEW 1-APR-81

Department:
Responsible Person: Fred Spring
Stds. Mgmt. Group: Mfg. Process and Quality Assurance

Table 1 (Cont'd)

DEC STD Ø59, Section 3 — Incoming Inspection; Standard Operating Procedures
Rev. A(XØ2) 18-Mar-81 (Level 2: Requirements)

ABSTRACT: Establishes the minimum requirements for documenting standard operating procedures for Incoming Inspection areas. Defines flow of materials and forms, methods of identification and traceability, methods to control measuring equipment, and the required quality documentation for Incoming Inspection areas.

FOR INTERNAL USE ONLY

STATUS: REV A(XØ2) 18-MAR-81 OUT FOR LIMITED REVIEW 1-APR-81

Department: Standards and Methods Information Control
Responsible Person: Dennis Majikas
Stds. Mgmt. Group: Mfg. Process and Quality Assurance

DEC STD Ø6Ø, Section Ø — Design and Certification of Hardware Products To National and International Regulations and Standards - Policy and Procedures
Rev. H 16-Oct-8Ø (Level 1: Policy and Requirements)

FOR INTERNAL USE ONLY

ABSTRACT: Defines the intentions, responsibilities and controls for designing and certifying Digital hardware products to meet the requirements of nationally - and internationally - recognized organizations.

Department: Hardware Design Assurance
Responsible Person: Dick Amann
Stds. Mgmt. Group: Hardware Design Assurance

Table 1 (Cont'd)

DEC STD 060, Section 1 — Design and Certification of Hardware, etc. - Specific Requirements
Rev. A 16-Oct-80 (Level 1: Requirements)

ABSTRACT: Lists the specific Digital standards and external regulations and standards that apply to Digital's hardware products.

FOR INTERNAL USE ONLY

Department: Hardware Design Assurance
Responsible Person: Dick Amann
Stds. Mgmt. Group: Hardware Design Assurance

DEC STD 062
NEW STD
IN PROCESS

Submittal of Hardware Products to National and International Agencies
(Level 1: Procedure)

ABSTRACT: Supports DEC STD 060 requirements by describing the usual procedure for submitting hardware to regulatory agencies. Provides checklist to ensure product has all necessary approvals.

STATUS: AX00 BEING PREPARED

Department: Hardware Design Assurance
Responsible Person: Dick Amann
Stds. Mgmt. Group: Hardware Design Assurrance

DEC STD 092
CHANGE IN PROCESS
TO RESTRUCTURE
AS SHOWN BELOW

Finish and Color Standard
Rev D. 11-May-79 (Level 1: Requirements)

NOTE: Revision D is made up of Revision C dated 15-Sep-77 and ECO EL00092-0-00003 dated 11-May-79.

STATUS: RESTRUCTURE OF DEC STD 092 INTO FOLLOWING 3 SECTIONS IS BEING INVESTIGATED

Table 1 (Cont'd)

DEC STD Ø92, Section Ø — Finish and Color Standards: Specification of Paints and Finishes
NEW SECTION
AWAITING INPUT

ABSTRACT: Provides the minimum requirements for specification of finish and color on Engineering documentation. Includes definition of 3-4-3 finisn/color identifier and references detailed specifications where specific requirements for approved finish/colors are documented.

STATUS: AWAITING INPUT

Department: Manufacturing Metals Engineering
Responsible Person: Art Clockedile
Stds. Mfmt. Group: Manufacturing Process and Quality Assurance

DEC STD Ø92, Section 1 — Finish and Color Standard: Requirements for Material Suppliers
NEW SECTION
AWAITING INPUT — REV. E(XØ2) 28-JAN-81 (LEVEL 1: REQUIREMENTS)

ABSTRACT: Provides requirements for Material Suppliers providing paint or finish materials necessary to meet detailed Ø92 finish/material specifications.

STATUS: REV E(XØ2) 28-JAN-81 OUT FOR LIMITED REVIEW

Department: Manufacturing Metals Engineering
Responsible Person: Art Clockedile
Stds. Mfmt. Group: Manufacturing Process and Quality Assurance

Table 1 (Cont'd)

DEC STD 092, Section 2
NEW SECTION
AWAITING INPUT

Finish and Color Standard: Requirements for Paint Applicators
Rev. E(X02) 4-Apr-81 (Level 1: Requirements)

ABSTRACT: Provides information required by paint applicators to meet the requirements of detailed 092 finish material specifications.

STATUS: REV E(X02) 4-APR-81 OUT FOR LIMITED REVIEW

Department: Manufacturing Metals Engineering
Responsible Person: Art Clockedile
Stds. Mfmt. Group: Manufacturing Process and Quality Assurance

DEC STD 100
CHANGE IN PROCESS
TO RESTRUCTURE
INTO SECTIONS
AS FOLLOWS:

Engineering Change Orders
Rev. E 12-Jan-78 (Level 1: Procedures)

ABSTRACT: Pertains to a unified procedure for submission of Engineering Change Orders. All engineering drawings and documentation filed in Engineering Document Control Centers can only be changed by the procedures outlined herein. ECOs are controlled and issued by the Engineering Services groups within Engineering.

Department: Engineering Information Control
Responsible Person: Nancy Moore
Stds. Mgmt. Group:

DEC STD 100, Section 1
NEW SECTION
IN PROCESS

Engineering Change Orders - Hardware
Rev. F(X01) 23-Jun-80 (Level 1: Policy and Requirements)

ABSTRACT: Describes the policies, procedures, and guidelines used to create ECOs for Digital hardware. It also specifies the responsibilities and roles assigned to the various individuals and organizations that create, administer, and implement them.

STATUS: REV. F (X01) OUT FOR LIMITED REVIEW

Department: Engineering Information Control
Responsible Person: Nancy Moore
Stds. Mgmt. Group:

Table 1 (Cont'd)

DEC STD 100, Section 2
NEW SECTION
IN PROCESS

Engineering Change Orders - Purchase Specifications
Rev. F(X02) 17-Apr-80 (Level 2: Policy and Requirements)

ABSTRACT: Describes the policy and procedure for changing purchase specifications. Also specifies responsibilities and roles assigned to various individuals and organizations involved in purchase specification ECO process.

STATUS: REV. F (X02) 17 APR 80 IN LIMITED REVIEW

Department: Specification Control Systems
Responsible Person: John Peachey
Stds. Mgmt. Group:

DEC STD 100, Section 3
NEW SECTION
IN PROCESS

Diagnostic Engineering Change and Patch Orders (DECO's and DEPO's)
Rev. F(X04) 20-Jan-81 (Level 2: Policy and Procedures)

ABSTRACT: A level 2 standard that describes the policy for DECO's, DEPO's, and Submissions of new diagnostic products to the Software Distribution Center. Also specifies responsibilities and roles of involved organizations.

STATUS: REV. F(X04) TO ENG. COMMITTEE REVIEW IN 23-APR-81

Department: Diagnostic Systems Engineering
Responsible Person: Gunars Zagars
Stds. Mgmt. Group: Engineering Information and Documentation

Table 1 (Cont'd)

DEC STD 100, Section 4 — Field Service Change Orders (FCO's)
PLANNED
NOT STARTED
NOT AVAILABLE

ABSTRACT: A level 1 document that describes the policies and procedures used to create FCO's. Although a standalone document, it is an extension of the ECO process described in Section 1. Details the implementation procedure for FCO's, and specifies the financial reporting involved.

STATUS: AWAITING INPUT FROM CUSTOMER SERVICES

Department:
Responsible Person: Ann Bostwick
Stds. Mgmt. Group: Customer Services and Repair

DEC STD 101 — Manufacturing Operations Plan for Assembly, Inspection, and Testing
Rev. C 21-Oct-76 (Level 1: Policy)

CHANGE IN PROCESS

ABSTRACT: Presents a policy for the structure of a Manufacturing Operations Plan for all product lines and businesses within Digital Equipment Corporation. This Manufacturing Operations Plan allows product lines and businesses the flexibility to assure that controls are implemented so all products are produced in conformance to specifications.

STATUS; REV D(X00) 3-MAR-81 OUT FOR EC, QBOD REVIEW 6-MAR-81

Department: Central Manufacturing Quality Assurance
Responsible Person: E. Mondani
Stds. Mgmt. Group: Manufacturing Process and Quality Assurance

Table 1 (Cont'd)

DEC STD 102 — Environmental Standard for Computers and Peripherals
CHANGE IN PROCESS TO RESTRUCTURE INTO SEPARATE STDS AND SECTIONS AS FOLLOWS:
Rev. C 12-Jan-78

ABSTRACT: Defines the environmental conditions to which products marketed by Digital Equipment Corporation must conform before being considered acceptable for shipment.

Department: Environmental Engineering
Responsible Person: F. Grimaldi
Stds. Mgmt. Group: Hardware Design Assurance

DEC STD 102, Section 0 — Environmental Standard for Computers and Peripherals - General Test Requirements
NEW SECTION IN PROCESS
Rev. D(X01) 16-Feb-81 (Level 1: Requirements)

STATUS: REV D(X01) OUT FOR ACOUSTICS GROUP REVIEW - REVIEW COMMENTS BEING WORKED APR 81

DEC STD 102, Section 1 — Temperature, Humidity, and Altitude Test Requirements
NEW SECTION IN PROCESS
Rev. D(X01) 16-Feb-81 (Level 1: Requirements)

STATUS: REV D(X01) OUT FOR ACOUSTICS GROUP REVIEW - REVIEW COMMENTS BEING WORKED APR 81

DEC STD 102, Section 2 — Mechanical Shock and Vibration Test Requirements
NEW SECTION IN PROCESS
Rev. D(X01) 16-Feb-81 (Level 1: Requirements)

STATUS: REV D OUT FOR ACOUSTICS GROUP REVIEW - REVIEW COMMENTS BEING WORKED APR 81

DEC STD 102, Section 3 — Physical Stability Requirements During Shipping and Handling
NEW SECTION IN PROCESS
Rev. D(X00) 17-Oct-79 (Level 1: Requirements)

STATUS: AWAITING INPUT

Table 1 (Cont'd)

DEC STD 102, Section 4 — Acoustic Noise Test Requirements
NEW SECTION — Rev. D(X00) 21-Jul-80 (Level 1: Requirements)
IN PROCESS

STATUS: D(X00) 21-Jul-80 TO PROD. ACOUSTICS GROUP REVIEW - REVIEW COMMENTS BEING WORKED APR 81

DEC STD 102 Section 7 — EMI/Electromagnetic Interface
CHANGE IN PROCESS — Rev. B 9-Nov-78 (Level 1: Requirements)
SEE DEC STD 103

ABSTRACT: Defines the electromagnetic environment that Digital products can be expected to be subjected to and define the limits of the electromagnetic interface that these devices are allowed to produce.

Department: Electromagnetic Compatibility
Responsible Person: Peter Boers
Stds. Mgmt. Group: Hardware Design Assurance

DEC STD 103, Section 0 — Electromagnetic Compatibility (EMC)
NEW STD IN PROCESS. — Hardware Design Requirements
DEC STD 102, SECTION 7 TO BE RESTRUCTURED INTO THE FOLLOWING SECTION OF DEC STD 103 — Rev. A(X00) 20-May-80 (Level 1: Requirements)

STATUS : WAITING INPUT FROM ORIGINATOR

Department: Hardware Design Assurance
Responsible Person: Peter Boers
Stds. Mgmt. Group: Hardware Design Assurance

DEC STD 103, Section 1 — FCC Labeling And User Manual Information
Rev. A 18-Dec-80 (Level 1: Requirements)

ABSTRACT: Provides an overview of the process for labeling equipment and modifying user manuals in response to FCC regulations cited in FCC Rules Part 15.J.

Department: Engineering Information Control
Responsible Person: Don Call
Stds. Mgmt. Group: Hardware Design Assurance

Table 1 (Cont'd)

DEC STD 103, Section 1A — FCC Non-Compliance Labeling
Rev. A 18-Dec-80 (Level 1: Requirements)

ABSTRACT: Describes the policy for labeling applicable Digital equipment that has not been verified or certified as complying with FCC regulations cited in FCC Part 15.J.

Department: Engineering Information Control
Responsible Person: Don Call
Stds. Mgmt. Group: Hardware Design Assurance

DEC STD 103, Section 1B — FCC Compliance Labeling
NEW SECTION IN PROCESS — Rev. A(X00) 15-Apr-81 (Level 1: Requirements)

ABSTRACT: Describes the policy for labeling applicable Digital equipment that has been verified or certified as complying with FCC requlations cited in FCC Rules, Part 15.J.

STATUS: IN WRITING PROCESS

Department: Engineering Information Control
Responsible Person: Don Call
Stds. Mgmt. Group: Hardware Design Assurance

DEC STD 103, Section 1C — FCC Compliance Equipment User Information
NEW SECTION IN PROCESS

ABSTRACT: Describes the policy for associating user information with Digital equipment that has been verified or certified as complying with FCC regulations cited in FCC Rules, Part 15.J.

STATUS: IN WRITING PROCESS

Department: Engineering Information Control
Responsible Person: Don Call
Stds. Mgmt. Group: Hardware Design Assurance

Table 1 (Cont'd)

DEC STD 103, Section 1D — FCC Certification Approval Process
NEW SECTION IN PROCESS

ABSTRACT: Describes the process for verifying or certifying Digital equipment as complying with FCC regulations cited in FCC Rules, Part 15.J.

STATUS: WAITING INPUT

Department: Hardware Design Assurance
Responsible Person: Peter Boers
Stds Mgmt Group: Hardware Design Assurance

DEC STD 103, Section 2 — Radio Interference (RFI) Emission: Acceptance Levels For Digital Hardware Products
NEW SECTION IN PROCESS
Rev. A(X00) 20-Mar-80 (Level 1: Requirements)

STATUS: WAITING INPUT FROM ORIGINATOR

Department: Hardware Design Assurance
Responsible Person: Peter Boers
Stds. Mgmt. Group: Hardware Design Assurance

DEC STD 103, Section 3 — Electromagnetic Interference (EMI) Susceptibility: Minimum Requirements for Digital Products
NEW SECTION IN PROCESS
Rev. A(X00) 20-Mar-80 (Level 1: Requirements)

STATUS: WAITING INPUT FROM ORIGINATOR

Department: Hardware Design Assurance
Responsible Person: Peter Boers
Stds. Mgmt. Group: Hardware Design Assurance

Table 1 (Cont'd)

DEC STD 103, Section 4
NEW SECTION
IN PROCESS

Electrostatic Discharge (ESD) Susceptibility of Hardware Products: Requirements and Test Methods
Rev. A(X00) 20-Mar-80 (Level 1: Requirements)

STATUS: WAITING INPUT FROM ORIGINATOR

Department: Hardware Design Assurance
Responsible Person: Paul Rey
Stds. Mgmt. Group: Hardware Design Assurance

DEC STD 104
NEW STD IN PROCESS

Product Acoustic Noise Acceptability
Rev. A(X00) 27-Aug-80 (Level 1: Requirements)

ABSTRACT: Defines acceptability criteria for acoustic noise emitted from Digital products and groups of products.

RESTRICTED DISTRIBUTION

STATUS: REV. A(X00) 27-AUG-80 IN REVIEW BY PRODUCT ACOUSTICS GROUP - REVIEW COMMENTS BEING WORKED APR 81

Department: Product Acoustics Group
Responsible Person: Bob Lotz
Stds. Mgmt. Group: Hardware Design Assurance

DEC STD 105
NEW STD IN PROCESS

Display Work Station Ergonomics (Human Factors)
Rev. A(X03) 18-Mar-81 (Level 1: Requirements)

ABSTRACT: Not Available

STATUS: REV A.(X03) 18-MAR-81 OUT FOR LIMITED REVIEW 20-MAR-81

Department:
Responsible Person: Charles Abernethy
Stds. Mgmt. Group: Hardware Design Assurance

Table 1 (Cont'd)

DEC STD 106 — Standard for In-House Acceptance Procedures
Rev. A 10-Dec-73 (Level 1: Procedure)

ABSTRACT: Outlines the general steps to be followed in creating an acceptance procedure for all systems and options manufactured by Digital. Included are: computers, computer options, special systems, interfaces, etc.

Department: In-House Field Service
Responsible Person: Steve Hoyt
Stds. Mgmt. Group: Mfg. Process and Quality Assurance

DEC STD 107, Section 0 — Digital Standard for Terminal Keyboards Standard Keyboard Layouts
Rev. B 3-Jan-80 (Level 1: Requirements)

ABSTRACT: Defines requirements for keyboard layouts, keyboard codes, and key pads to be used for all terminal designs that are introduced into production after January 1, 1978.

Department: Keyboard Design Committee
Responsible Person: Jim McGinnis
Stds. Mgmt. Group: Software & Architecture Standards

DEC STD 107, Section 1 — Digital Standard For Terminal Keyboards Registry Of Graphic Character Sets
CHANGE IN PROCESS
Rev. A 3-Jan-80 (Level 1: Requirements)

ABSTRACT: Defines the graphic character sets to be used for Digital hardware and software products for information interchange. The definitions include code generated by each graphic character.

STATUS: Rev. B(X00) 25-NOV-80 OUT FOR REVIEW 2-Dec-80

Department: Keyboard Design Committee
Responsible Person: Jim McGinnis
Stds. Mgmt. Group: Software and Architecture Standards

Table 1 (Cont'd)

DEC STD 109
NEW STD PROPOSED

Chemical and Corrosive Environmental Classifications
Rev. A(X01) 4-Feb-80 (Level 1: Guidelines)

ABSTRACT: Defines four categories of equipment operating conditions, based upon average concentrations of various reactive chemicals that may be present at an equipment site. It also establishes the chemical environmental classifications in which Digital hardware products can be specified to perform reliably.

STATUS: REV. A (X01) AWAITING INPUTS FROM ORIGINATOR

Department: Component Engineering
Responsible Person: J.P. Keller/Bob Berman
Stds. Mgmt. Group: Hardware Design Assurance

DEC STD 110

DEC Standard for Escape Sequence
Rev. B 7-Mar-75 (Level 2: Requirements)

ABSTRACT: Indiscriminate echoing of ESC as 33_8 is prohibited. Where it is desirable to print some displayable character to provide visible confirmation that ESC has been received by the program, than that character must be single dollar sign ($;(44)_8$).

ESC is the character which initially delimits an ESC sequence and ESC may carry no other meaning, even though ESC currently has many other meanings. Applies to all new DEC terminals.

Department: Software
Responsible Person: David Hughes
Stds. Mgmt. Group: Software & Architecture Standards

Table 1 (Cont'd)

DEC STD 111
CHANGE PLANNED
NOT SCHEDULED

DEC Standard for Terminal Synchronization
Rev. A 6-Mar-75 (Level 2: Requirements)

ABSTRACT: DC1 and DC3, 21_8 and 23_8 formerly XON and XOFF respectively, are to be used for synchronization of terminal keyboards in the manner described in the standard DC2 and DC4, 22_8 and 24_8 formerly TAPE and NOT-TAPE respectively, are reserved for future use, likely for synchronization as well.

Department: Terminals Engineering
Responsible Person: David Hughes
Stds. Mgmt. Group: Software & Architecture Standards

DEC STD 112

Standard Date Format for Output
Rev. B 10-Feb-77 (Level 1: Requirements)

ABSTRACT: This standard ensures an unambiguous interpretation of dates by readers around the world. This format is one which is in common use throughout most of the world, is reasonably terse, is well human engineered and is easy to produce in any computer system.

Department: Software & Architecture Standards
Responsible Person: Peter Conklin
Stds. Mgmt. Group: Software & Architecture Standards

DEC STD 114
CHANGE IN PROCESS
TO RESTRUCTURE
STANDARD

Metric Dimensioning on Engineering Drawings - General Requirements
Rev. A 24-Aug-74 (Level 1: Requirements)

ABSTRACT: Presents requirements for converting from the inch to the metric system while maintaining interchangeability.

STATUS: REV. B(X00) AWAITING WRITING-WILL BECOME DEC STD 114, SECTION 3

Department: Standards and Methods Information and Control
Responsible Person: Joe Kurta
Stds. Mgmt. Group: Engineering Information and Documentation Standards

Table 1 (Cont'd)

DEC STD 114, Section 0 — Drawing Requirements for Engineering Drawings
NEW STD PLANNED

ABSTRACT: Defines the Industry Standards and Company Unique requirements for Engineering Documentation Practices within Digital.

STATUS: AWAITING INPUT

Department: Standards and Methods Information and Control
Responsible Person: Joe Kurta
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD 114, Section 1 — Dimensioning and Tolerancing on Engineering Drawings - Decimal Inch Requirements
NEW STD IN PROCESS TO OBSOLETE AND REPLACE DEC STD 019 — Rev. B(X02) 22-Apr-80 (Level 1: Requirements)

ABSTRACT: Describes the requirements for dimensioning and tolerancing engineering drawings and documentation using the decimal presentation of the inch.

STATUS: AWAITING DISPOSITION OF REVIEW COMMENTS

Department: Standards and Methods Information and Control
Responsible Person: Joe Kurta
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD 114, Section 2 — Dimensioning and Tolerancing on Engineering Drawings - Metric and Dual Dimensions
NEW SECTION PLANNED

ABSTRACT: Not Available

STATUS: AWAITING INPUT

Department: Standards and Methods Information and Control
Responsible Person: Joe Kurta
Stds. Mgmt. Group: Engineering Information and Documents Standards

Table 1 (Cont'd)

DEC STD 115
CHANGE IN PROCESS

Manufacturing Process Documentation (MPD) Control Requirements and Plant
Rev. B(X05) 24-Dec-80 (Level 2: Requirements)

ABSTRACT: Provides Manufacturing organizations with a control system for the origination, revision, and dissemination of process documentation.

STATUS: REV. B (X05) 24 DEC 80 OUT FOR REVIEW 9 JAN 81

Department: Standards And Methods Information and Control
Responsible Person: Dennis Majikas
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD 116

Workmanship Standards Manual
1-Oct-80 (Level 2: Policy)

ABSTRACT: This document provides the criteria for craftmanship to be utilized in manufacturing and maintaining DIGITAL products.

Section 0 Introduction - Rev. C, 1-Oct-80

Section 1 Printed Circuit Boards - Rev. F, 1-Oct-80

Section 2 Soldered Terminations - Rev. E, 1-Oct-80

Section 3 Solderless Crimped Terminations - Rev. D, 1-Oct-80

Section 4 Cable and Harness - Rev. C, 24-Oct-79

Section 5 Hardware - Rev. D, 1-Oct-80

Section 6 Wirewrap/Logics - Rev. D, 1-Oct-80

Section 7 Safety - Rev. C, 24-Oct-79

Section 8 Technical Data - Rev. C, 24-Oct-79

Department: Central Manufacturing Quality Workmanship Committee
Responsible Person: Pat Sullivan
Stds. Mgmt. Group: Mfg. Process and Quality Assurance

Table 1 (Cont'd)

DEC STD 117
CHANGE IN PROCESS

Field Maintenance Print Sets
Rev. D 19-Apr-79 (Level 2: Requirements)

ABSTRACT: Establishes criteria for the content of Field Maintenance Print Sets. Specifies the types of the engineering drawings to be included and how they are to be organized for a particular hardware product.

STATUS: REV E(XØ1) 21-MAR-81 OUT FOR REVIEW 30-MAR-81

Department: Eng. Data Services
Responsible Person: Bob Marshall
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD 118

Standard for Indexes, Appendixes, Running Heads and Section Numbering for Software Documentation Manuals
Rev. B 12-Jan-78 (Level 2: Guidelines)

ABSTRACT: Defines index requirements and describes material suitable for appendixes. The use of running heads for chapter-oriented manuals is specified.)stds. Mgmt. Group: Hardware Design Assurance

DEC STD 119, Section Ø
CHANGE IN PROCESS

Digital Product Safety - Introduction and General Requirements
Rev. C 1-May-8Ø (Level 1: Requirements)

ABSTRACT: Defines the intentions annd criteria to be used during design and development of new products.

STATUS: REV D(XØØ) BEING PREPARED

Department: Product Safety
Responsible Person: R. Minezzi
Stds. Mgmt. Group: Hardware Design Assurance

Table 1 (Cont'd)

DEC STD 119, Section 1 — Digital Product Safety - Design Criteria
CHANGE IN PROCESS — Rev. C 1-May-80 (Level 1: Requirements)

ABSTRACT: Presents product safety design criteria.

STATUS: REV D(X00) BEING PREPARED

Department: Product Safety
Responsible Person: R. Minezzi
Stds. Mgmt. Group: Hardware Design Assurance

DEC STD 119, Section 2 — Digital Product Safety - Test Procedures
CHANGE IN PROCESS — Rev. C 1-May-80 (Level 1: Requirements)

ABSTRACT: Presents test procedures required to determine if products meet design criteria.

STATUS: REV D(X00) BEING PREPARED

Department: Product Safety
Responsible Person: R. Minnezzi
Stds. Mgmt. Group: Hardware Design Assurance

DEC STD 119, Section 3 — Digital Product Safety - Glossary
TO BE CONSOLIDATED IN SECTION 0 — Rev. C 1-May-80 (Level 1: Requirements)

STATUS: NEW REV D(X00) BEING PREPARED

Department: Product Safety
Responsible Person: R. Minezzi
Stds. Mgmt. Group: Hardware Design Assurance

Table 1 (Cont'd)

DEC STD 119, Section 3
NEW CONTENT for SECTION 3 starting at Rev. D

Digital Product Safety - VDE 0804 Requirement
Rev. D(X00) 1-May-81

ABSTRACT: Copntains Product Safety Criteria from VDE 0804. Requirements are mandatory for all products intended to be sold in Germany

STATUS: IN WRITING

Department: Product Safety
Responsible Person: R. Minezzi
Std. Mgmt. Group: Hardware Design Assurance

DEC STD 119, Section 4
TO BE OBSOLETED-CHECKLIST WILL BE AVAILABLE FROM PRODUCT SAFETY

Product Safety Design Review Checklist
Rev. C 1-May-80 (Level 1: Requirements)

ABSTRACT: Provides a product safety checklist for use during a new product's design review.

Department: Product Safety
Responsbile Person: R. Minezzi
Stds. Mgmt. Group: Hardware Design Assurance

DEC STD 120

Cooling Standard
Rev. A 6-Mar-75 (Level 2: Guidelines)

ABSTRACT: A quick reference to which a Design Engineer can refer for questions on cooling conventional circuit boards. There are also included some general guidelines for cabinets and component level thermal calculations to enable the Engineer to estimate the cooling required for this system.

Department: Environmental Engineering
Responsible Person: Rob [illegible] nemann
Stds. Mgmt. Group: Hardw [illegible] Design Assurance

Table 1 (Cont'd)

DEC STD 121, Section Ø — DIGITAL Data Communications Message Protocol (DDCMP)
CHANGE IN PROCESS — Rev. A 30-Mar-78 (Level 2: Requirements)

ABSTRACT: Describes the functions, characteristics, interfaces, message formats, and operation of the DDCMP protocol. It is primarily intended to assist the individual implementing DDCMP. It is structured to also provide general information describing the protocol to others who may need this level of information. It is not intended to instruct those unfamiliar with the basic principles of data communications.

STATUS: REV B(XØ2) 27-JAN-81 FOR SPECIAL REVIEW 30-JAN-81 INCLUDES DDCMP SPEC V4.1

Department: Distributed Systems
Responsible Person: S. Wecker
Stds. Mgmt. Group: Software & Architecture Standards

DEC STD 122 — AC Power Line Standard
CHANGE IN PROCESS — Rev. B 15-Apr-76 (Level 1: Requirements)

ABSTRACT: Describes the primary AC power characteristics normally provided at utilization points of major distribution networks and establishes minimum design criteria and constraints for future DEC computer systems and equipment.

STATUS: REV. C(XØØ) 30-OCT-8Ø AWAITING INPUT FROM ORIGINATOR

Department: Power Supply Engineering
Responsible Person: F. Loya
Stds. Mgmt. Group: Hardware Design Assurance

Table 1 (Cont'd)

DEC STD 123 Power Control Bus Standard
Rev. A 29-Apr-76 (Level 1: Requirements)

ABSTRACT: Defines the DEC Power Control Bus function, electrical and hardware. Hardware designed and tested to the limits stipulated may be interfaced with any other equipment complying with this standard. All hardware released following the issue date that interfaces with the Power Control Bus must comply with this Standard.

Department: Office of the Chief Engineer
Responsible Person: C. Noelcke
Stds. Mgmt. Group: Hardware Design Assurance

DEC STD 124 Format Standard for Manuals Produced on Typeset Media
Rev. A 5-Oct-78 (Level 2: Procedures)

ABSTRACT: For personnel who are involved in preparing hardware related product literature for typeset media. It does not apply to software documentation. It must be used for any typeset manuals to be published on microfiche. This standard governs formatting procedures only.

Department: Technical Documentation
Responsible Person: P. Walsh
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD 125 Cassette Format Standard for Labelled and Unlabelled Files
Rev. A 21-Feb-75 (Level 2: Requirements)

ABSTRACT: Describes the format and labelling conventions for files, physical blocks, logical records and data written on Digital Equipment Cassettes. It also describes the unlabelled standard. This standard must be followed when reading and writing cassettes intended for interchange between systems; it is recommended for other cassettes.

Department: Small Systems Software
Responsible Person: S. Rabinowitz
Stds. Mgmt. Group: Software & Architecture Standards

Table 1 (Cont'd)

DEC STD 126 — Packaged Systems Documentation Structure
Rev. A 12-Apr-79 (Level 2: Requirements)

ABSTRACT: Describes the minimum engineering drawings and documents that are required to document packaged systems.

Department: Packaged Systems
Responsible Person: J. Beatty
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD 128 — Confidential Engineering Information and Documentation: Policy and Requirements
Rev. A 4-Sep-80 (Level 1: Policy & Requirements)

FOR INTERNAL USE ONLY

ABSTRACT: Defines Digital policy and requirements for classifying, labelling, storing, and distributing documentation classified as "Restricted Distribution" or "For Internal Use Only".

Department: Corporate Security
Responsible Person: Mike Carter
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD 129 — Software Box Requirements and Procedures
Rev. A 8-Jan-81 (Level 2: Requirements and Procedures)

FOR INTERNAL USE ONLY

ABSTRACT: Establishes the requirements for content, identification, creation, and quality control of software boxes.

Department: Mfg. Software Distribution Center
Responsible Person: Fred Forte
Stds. Mgmt. Group: Engineering Information and Documentation Standards

Table 1 (Cont'd)

DEC STD 130

CHANGE IN PROCESS TO RESTRUCTURE AND CHANGE SCOPE OF DOCUMENT

System Business Plans: Content and Format Requirements
Rev. B(X01) 1-Apr-81 (Level 1: Policy & Procedures)

ABSTRACT: Describes content requirements for a system business plan. Applies to all new products being considered for development. It outlines requirements for the Executive Summary, System Description, Forecast, Assumptions, and Financial Analysis. A sample business plan with a recommended entries is provided.

FOR INTERNAL USE ONLY

STATUS: The scope and content of this document are still being defined. B(X01) Draft to originator in April 81.

Department: Engineering Operations
Responsible Person: Per Hjerppe
Stds. Mgmt. Group: Product and Program Management

DEC STD 133

CHANGE PLANNED NOT SCHEDULED

Integrated Circuit Documentation and Test System Control
Rev. A 10-Jun-76 (Level 1: Requirements)

ABSTRACT:

Section 1 Includes purpose, scope, and detailed descriptions of documentation and overall system.

Section 2 Includes procedures for new ICs and revising documentation, and test-software and test-hardware associated with existing ICs.

Section 3 Includes responsibilities for general operation, introduction of new ICs, ECO's to existing ICs, and introduction of new IC testers. Also includes an index of relevant engineering notes.

Department: Components Engineering
Responsible Person: Leo Tiernan/Ken Hall
Stds. Mgmt. Group: Components Engineering and Specifications

Table 1 (Cont'd)

DEC STD 137
CHANGE IN PROCESS TO COMPLETELY REWRITE & RESTRUCTURE

TO FOCUS ONLY ON FIELD DEFINITIONS

Master Parts File Definitions
Rev. A 8-Aug-76 (Level 1: Requirements)

ABSTRACT: Applies to persons involved with internal DEC business programming application. It describes the field formats initially developed by the corporate manufacturing production and inventory system. As local internal DEC systems emerge and our systems mature, the advantage develops for these systems to pass information to each other. it becomes, therefore, increasingly more significant to recognize the need for standard formats.

STATUS: REV. B(X03) 1-Mar-81 OUT FOR ENG. COMM REVIEW 3-MAR-81

Department: EPLS Operations
Responsible Person: Carolyn Rodriguez
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD 138
DEC STD PLANNED

Standard For the Registration of Control Characters, Escape Sequences, and Control Sequence (Level 1: Requirements)

ABSTRACT: Defines the encoding, interpretation, names, and the mnemonics of all control functions used by Digital hardware and representation of information occurs in seven-bit or eight-bit characters.

STATUS: AWAITING INPUT

Department: Terminals Engineering
Responsible Person: Dave Hughes
Stds. Mgmt. Group: Software & Architecture Standards

Table 1 (Cont'd)

DEC STD 139 Reliability Prediction
Rev. A 22-Jan-76 (Level 2: Requirements)

ABSTRACT: Establishes MIL HBK 217B as the official Reliability Prediction technique to be used by DEC and establishes the responsibility for maintaining key parameters to assure consistent interpretations throughout the corporation.

Department: Chief Engineering Office
Responsible Person: C. Noelcke
Stds. Mgmt. Group: Hardware Design Assurance

DEC STD 140, Section 0 Module Documentation Structure: Basic Requirements
Rev. C 26-Feb-81 (Level 1: Requirements)

FOR INTERNAL USE ONLY

ABSTRACT: Describes the documentation structure required to accomodate and control the release of modules, 54-class assemblies, and printed circuit (50-class) boards.

Department: Standards and Methods Information Control
Responsible Person: Joe Kurta
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD 140, Section 1 Module Documentation Structure: Wire Adds and Etch Cuts Requirements
Rev. B 24-May-79 (Level 1: Requirements)

ABSTRACT: Specifies the additional documentation required to describe modules revised by wire adds and etch cuts.

Department: Standards and Methods Information Control
Responsible Person: Joe Kurta
Stds. Mgmt. Group: Engineering Information and Documentation Standards

Table 1 (Cont'd)

DEC STD 140, Section 2 — Module Documentation Structure: Wire Ink Revisions
Rev. A 24-May-79 (Level 1: Requirements)

ABSTRACT: Specifies the additional documentation required to describe modules revised by means of wire ink.

Department: Standards and Methods Information Control
Responsible Person: J. Kurta
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD 141 — Engineering Notebook Policy and Requirements
Rev. A 4-Oct-79 (Level 1: Requirements)

ABSTRACT: Defines Digital policy and requirements for issuance, use, control, and retention of Engineering Notebooks for the purposes of capturing and retaining essential information.

Department: Engineering Information Control/ Corporate Legal Department
Responsible Person: Nat Rounds/Tom Siekman
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD 142, Section 0 — Etch Board and Module Release Verification Requirements and Procedures - Manufacturing Production Release
Rev. E 26-Feb-81 (Level 1: Procedures)

FOR INTERNAL USE ONLY

ABSTRACT: Describes the etch board (50 level), module (54-level), and parallel (50/54-level) release processes. Lists documentation items in various release package required to meet the acceptance requirements for manufacturing PC boards and modules.

Department: Engineering Information Control
Responsible Person: Dick Bubnel
Stds. Mgmt. Group: Engineering Information and Documentation Standards

Table 1 (Cont'd)

DEC STD 142, Section 1

NEW SECTION
IN PROCESS

Etch Board And Module Release Verification Requirements - Prototype Process
Rev. A(X04) 25-Feb-81 (Level 1: Requirements and Procedures)

ABSTRACT: Describes the protoype process and the interface between Engineering and Manufacturing.

FOR INTERNAL USE ONLY

STATUS: REV A(X04) 25-FEB-81 OUT FOR LIMITED REVIEW 11-MAR-81

Department: Engineering Information Control
Responsible Person: Dick Bubnell
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD 142, Section 2

Etch Board And Module Release Requirements and Procedures - Engineering Supervised Build (ESB) Process
Rev. A 26-Feb-81 (Level 1: Requirements and Procedures)

ABSTRACT: Describes "sign-off" process for engineering-supervised build PC boards (formerly called low volume process). Defines interaction between Engineering and Manufacturing that applies to all Digital design engineering sites.

FOR INTERNAL USE ONLY

Department: Engineering Information Control
Responsible Person: Dick Bubnell
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD 143

Standard for Updating Hardware/Software Manuals
Rev. A 19-Aug-76 (Level 2: Requirements)

ABSTRACT: Defines the format in which document updates are to be published.

Department: Software Publications
Responsible Person: S. Porada
Stds. Mgmt. Group: Software & Architecture Standards

Table 1 (Cont'd)

DEC STD 144

Disk Standard for Recording and Handling Manufacturing Detected Bad Sectors
Rev B. 18-Nov-76 (Level 2: Requirements)

ABSTRACT: Specifies the hardware disk format, controller requirements and software handling of manufacturing site determined bad sectors of the RK06 and RK07 data cartridges and future disks. Conformance to this standard will result in improving reliability for the combined hardware/software system as experienced by our customers.

Department: Disk Engineering
Responsible Person: R. Rottmayer
Stds. Mgmt. Group: Software & Architecture Standards

DEC STD 145
ECO IN PROCESS
TO WITHDRAW
AND OBSOLETE

DEC Representation of Data Values in ASCII Character Strings for Information Interchange Standard
Rev. A 27-May-76 (Level 2: Requirements)

ABSTRACT: Defines the representation of data in character strings for interchange among DEC systems. It is an extension of ANSI X3.42. American National Standard for the Representation of Numeric Values in Characters Strings for Information Interchange.

Department: Software & Architecture Standards
Responsible Person: Pat White
Stds. Mgmt. Group: Software & Architecture Standards

DEC STD 146

Standard Order for Front and Back Pages of Manuals
Rev. B 12-Jan-78 (Level 2: Requirements)

ABSTRACT: Establishes sequence of pages preceding and following the text in a software or hardware manual. The required preliminary and back matter pages are listed; and each part of the preliminaries and back matter is defined.

Department: Software Documentation Methods
Responsible Person: Hank Moran
Stds. Mgmt. Group: Software & Architecture Standards

Table 1 (Cont'd)

DEC STD 147
PROPOSED

Digital Equipment Corporation Hardware and Software Editing Standard

ABSTRACT: Defines standards for editing functions performed in hardware and software. The scope includes traditional editing software and newer editing terminals.

STATUS: AWAITING INPUT FROM ORIGINATOR

Department: Commercial Languages
Responsible Person: Jeff Rudy
Stds. Mgmt. Group: Software & Architecture Standards

DEC STD 148

User Mode Diagnostic Standard
Rev. A 10-Feb-77 (Level 2: Requirements)

ABSTRACT: Defines general guidelines for user mode diagnostics. This standard specifies both the functions performed by the diagnostic and the operating system services required to support each of these levels of user mode diagnostics. Applies to PDP-10 and PDP-11 processor families and all future systems.

Department: Customer Services
Responsible Person: W. Moncsko
Stds. Mgmt. Group: Software & Architecture Standards

DEC STD 149

DIGITAL Magnetic Tape Labels and File Structure Standard
Rev. A 18-Jan-79 (Level 2: Requirements)

ABSTRACT: Defines 4 levels of magnetic tape label formats, record formats and tape mark relationships. Tapes written in conformance to this standard will also conform to American National Standard ANSI X.27-1977, Magnetic Tape Labels and File Structure for Information Interchange.

Department: Software & Architecture Standards
Responsible Person: L. Frampton
Stds. Mgmt. Group: Software & Architecture Standards

Table 1 (Cont'd)

DEC STD 150
PROPOSED

BASIC

ABSTRACT: Intends to standardize all capabilities in extant DEC BASICS. The standard is further intended to establish a stable DEC BASIC that provides for coherent extensions to BASIC system capabilities for all DEC processors. Compatibility with major industry competitive BASICs (ANSI, etc.) is also a goal.

STATUS: AWAITING INPUT FROM ORIGINATOR

Department: Commercial Languages
Responsible Person: Jim Totten
Stds. Mgmt. Group: Software & Architecture Standards

DEC STD 151

Punched Card Format: Requirements
Rev. C 3-Mar-81 (Level 2: Requirements)

ABSTRACT: Defines two formats for encoding data on industry-compatible 80 column tabulating cards for the purpose of ensuring that such cards may be used as a compatible means of information interchange between DIGITAL computer systems.

Department: Software & Architecture Standards
Responsible Person: L. Samberg
Stds. Mgmt. Group: Software & Architecture Standards

DEC STD 152
PROPOSED

COBOL
(Level 2: Requirements)

ABSTRACT: Defines a single DEC COBOL language specification, COBOL compiler options and COBOL object-time semantics that will assure compatible implementation within DEC for all future COBOL development. The standard is intended to conform to ANSI X3. 23-1974 and to selected features of the CODASYL COBOL JOD (1978).

STATUS: AWAITING INPUT FROM ORIGINATOR

Department: Commercial Languages
Responsible Person: Jeff Rudy
Stds. Mgmt. Group: Software & Architecture Standards

Table 1 (Cont'd)

DEC STD 153
ECO PROPOSED
TO UPDATE

Error Logging Standard
Rev. A 26-May-77 (Level 1: Requirements)

ABSTRACT: Describes the error logging system in terms of the data which should be captured into an error log file, the method of packaging the binary data into error log entries in the error log file, and the format necessary for compatible displays of the error log file.

STATUS: UPDATE IN PROCESS - MEMO DEFINES SCOPE OF PLANNED REVISION 8-Aug-80

Department: Customer Services
Responsible Person: Ray Drueke
Stds. Mgmt. Group: Software & Architecture Standards

DEC STD 154
ECO IN PROCESS
TO WITHDRAW
AND OBSOLETE

Standard for Floppy Disk (RX01) Volume Identification and Data Interchange
Rev. A 19-May-77 (Level 2: Requirements)

ABSTRACT: Defines the data recording conventions to allow RX01 disks to be identified across all DEC systems which support the Diskette. Each conforming system will be capable of writing and reading the volume identification. This volume I.D. will specify the origin and format of the data present on the volume. This standard applies when reading and writing diskettes intended for interchange.

Department: Small Systems Software
Responsible Person: R. Olsen
Stds. Mgmt. Group: Software & Architecture Standards

DEC STD 156
NEW STANDARD
PLANNED

AWAITING
INPUT

Introduction of New Purchased Parts

ABSTRACT: Defines the process for introducing a Purchased Part into the Digital system and defines Purchased Part Information System.

STATUS: AWAITING INPUT FROM SPECIFICATION CONTROL SYSTEMS

Department: Specification Control Systems
Responsible Person: John Peachey
Stds. Mgmt. Group: Components Engineering and Specifications

Table 1 (Cont'd)

DEC STD 157

OMNIBUS Specification
Rev. A 19-Aug-76 (Level 1: Requirements)

ABSTRACT: Describes in detail the mechanical and electrical characteristics of a bus scheme used to interconnect circuit modules that form the various PDP8 series of mini-computers. This specification should be followed carefully when designing a device that is going to connect to the OMNIBUS.

Department: PDP-8 Engineering
Responsible Person: L. Narhi
Stds. Mgmt. Group: Software & Architecture Standards

DEC STD 158
NEW STD IN PROCESS

UNIBUS Specification
Rev. A(X00) 30-Nov-78 (Level 1: Requirements)

ABSTRACT: Defines and specifies the minimum requirements of the PDP-11 Unibus to which products designed by Digital Equipment Corporation must conform before being considered for production release.

STATUS: AWAITING INPUT FROM ORIGINATOR

Department: Systems Integration
Responsible Person: Don Vonada
Stds. Mgmt. Group: Software & Architecture Standards

DEC STD 159

MASSBUS Interface Specifications
Rev. B 31-Jan-80 (Level 1: Requirements)

ABSTRACT: Specifies a standard interface between controllers and mass-storage devices. It is a company standard applied to disks, drums, tapes, and other magnetic or cyclic storage media.

RESTRICTED DISTRIBUTION - DO NOT REPRODUCE

Department: Systems Integration
Responsible Person: Don Vonada
Stds. Mgmt. Group: Software & Architecture Standards

Table 1 (Cont'd)

DEC STD 160, Section 0
NEW STD IN PROCESS

LSI-11 Bus Specification - Design Specification
Rev. A(X04) 10-Mar-81 (Level 1: Requirements)

RESTRICTED DISTRIBUTION-

DO NOT REPRODUCE

ABSTRACT: This standard includes the information necessary to interface to the LSI-11 Bus, including the Q-Bus, which supports 16 and 18 bits of address space, and the Q22 Bus, which supports 16, 18, and 22 bits of address space.

STATUS: REV A(X04) 10-MAR-81 OUT FOR REVIEW TO Q22 BUS TASK FORCE 27-MAR-1981.

Department: Q Bus Task Force
Responsible Person: Bill Newton
Stds. Mgmt. Group: Software & Architecture Standards

DEC STD 160, Section 1
NEW STD IN PROCESS

LSI-11 BUS Specification - History of the LSI-11 BUS
Rev. A(X01) 15-Dec-80 (Level 1: Guidelines)

ABSTRACT: Describes earlier versions of the LSI-11 Bus for historical reference.

STATUS: OUT FOR Q-BUS TASK FORCE REVIEW 27-MAR-81

Department: Q Bus Task Force
Responsible Person: Bill Newton
Stds. Mgmt. Group. Software and Architecture Standards

DEC STD 162

Micrographics: Format and Quality Requirements for Microforms
Rev. A 7-Dec-78 (Level 2: Requirements)

ABSTRACT: Describes the general format and quality requirements for each type of microform produced by Digital Equipment Corporation. The requirements are based on appropriate industry standards and U.S. Government specifications that have been adopted by the Digital Micrographics Committee.

Department: Product Descriptive Systems
Responsible Person: LeRoy Smith
Stds. Mgmt. Group: Engineering Information and Documentation Standards

Table 1· (Cont'd)

DEC STD 165 Standard for Documentation Symbology
Rev. A 21-Sep-78 (Level 2: Requirements)

ABSTRACT: Defines character names, special key names, and notation conventions that are to be used in user documentation.

Department: Software Publications
Responsible Person: S. Porada
Stds. Mgmt. Group: Software & Architecture Standards

DEC STD 167 Volume Identification for Removable Disk Pack Disk Systems
Rev. A 19-May-77 (Level 1: Requirements)

ABSTRACT: Defines the format and location of the volume identification block required to allow disk packs of removable disk-pack systems to be identifed in all CPU families. This block will enable operating systems to identify the origin and format of volume and decide if the volume can be processed. This standard also defines a standard error message for volumes that can not be processed.

Department: Software & Architecture Standards
Responsible Person: D. Lewine
Stds. Mgmt. Group: Software & Architecture Standards

DEC STD 168 PDP-11 Extended Instructions
Rev. A 18-Jan-79 (Level 1: Requirements)

ABSTRACT: Provides architectural definition and control for PDP-11 instruction whose opcodes are in the reserved and extended opcode spaces.

Department: VAX Architecture
Responsible Person: Dileep Bhandarkar
Stds. Mgmt. Group: Software & Architecture Standards

Table 1 (Cont'd)

DEC STD 170

Standard for Documenting Systems Messages
Rev. A 28-Jul-77 (Level 2: Guidelines)

ABSTRACT: Every operation system will have a single manual describing all messages produced by all modules of the operating system. Unbundled software marketed by Digital will have a single message manual or a message section within its manual(s). Messages will be presented in alphabetical order with an explanation of the message, the severity of the error, the action that has been taken by the system, the recommended procedure to be taken by the system and the user, and the name of the module that produced the message.

Department: Software Publications
Responsible Person: S. Porada
Stds. Mgmt. Group: Software & Architecture Standards

DEC STD 172

Legal Notices Required for Software Manuals and Licensed Software Sources
Rev. B 22-Dec-80 (Level 2: Requirements)

ABSTRACT: Defines the legal notices to be printed in Software Manuals to coded into licensed software sources.

Department: Corporate Legal Department
Responsible Person: T. Siekman
Stds. Mgmt. Group: Software & Architecture Standards

DEC STD 173
NEW STANDARD IN PROCESS

Naming System Software Products and Releases
(Level 1: Requirements)

ABSTRACT: Provides a method to describe software products and software releases by using a standard method to identify new software products as well as updates to existing software products.

STATUS: REV. A(X31) 17-Nov-80 AWAITING INPUT FROM ORIGINATOR

Department: Software & Architecture Standards
Reponsible Person: Pat White
Stds. Mgmt. Group: Software & Architecture Standards

Table 1 (Cont'd)

DEC STD 174 Magnetic Tape Error Recovery Procedures for Read and Write Errors
Rev. A 18-Jan-79 (Level 1: Procedure)

ABSTRACT: Defines the procedure and algorithms, including their sequence of execution to recover from operational read and write errors.

Department: Customer Services
Responsible Person: J. Shebell
Stds. Mgmt. Group: Software & Architecture Standards

DEC STD 176 Printed - Wiring Board Acceptance Criteria
CHANGE IN PROCESS Rev. A 21-Aug-80 (Level 2: Requirements)

ABSTRACT: Specifies end-product criteria for rigid printed-wiring boards that have been fabricated or purchased for Digital Equipment.

STATUS: REQUEST FOR CHANGE RECEIVED, PREPARING ECO

Department: Process Management Quality Assurance
Responsible Engineer: Dave Nevala
Stds. Mgmt. Group: Manufacturing Process and Quality Assurance

DEC STD 178 Digital Marking Standard
Rev. A 2-Feb-78 (Level 1: Requirements)

ABSTRACT: Establishes the item marking requirements for identification of items produces by or for Digital Equipment Corporation.

Department: Computer Systems Development
Responsible Person: P. Porreca
Stds. Mgmt. Group: Mfg. Process and Quality Assurance

Table 1 (Cont'd)

DEC STD 179, Section 1 — Powder Metal Bearings and Bushings
Rev. A 11-May-78 (Level 1: Requirements)

ABSTRACT: Provides the necessary information for the design engineer and/or manufacturing engineer to make an inital choice of Powder Metal Bearings and Bushings in cooperation with a Powder Metal parts supplier.

Department: Central Mechanical Manufacturing Engineering
Responsible Person: C. Vaillant
Stds. Mgmt. Group: Mfg Process and Quality Assurance

DEC STD 179, Section 2 — Powder Metal Structural Parts
CHANGE PLANNED IN QUEUE
Rev. A 11-May-78 (Level 1 Requirements)

ABSTRACT: Provides the necessary information for the design engineer and/or manufacturing engineer to make an initial choice of Powder Metal Structural parts.

Department: Central mechanical Manufcturing Engineering
Responsible Person: C. Vaillant
Stds. mgmt. Group: Mfg. Process and Quality Assurance

DEC STD 181 — Wirewrap Backplane and Wirewrap Module Release Process
CHANGE PLANNED IN QUEUE
Rev. A 21-Jun-79 (Level 1: Procedures)

ABSTRACT: Defines the process used for conversion of design information from an engineer's drawings into a released wirewrap data base and related soft tools necessary to build backplanes and/or wirewrap modules. Also describes the procedures for release, control, and distribution of wirewrap related information in the Engineering Documentation System.

CHANGE PLANNED TO ADD ADVANCED ECO PROCESS-AWAITING ACTION

Department: Standards and Methods Information and Control
Responsible Person: Joe Kurta
Stds. Mgmt. Group: Engineering Information and Documentation Standards

Table 1 (Cont'd)

DEC STD 182 Engineering Documentation Acceptance Criteria
Rev. B 1-May-80 (Level 1: Requirements)

ABSTRACT: Establishes the lettering requirements and relating drafting practices and procedures necessary to produce engineering drawings and documentation of a quality that is acceptable for microfilm and subsequent reproduction.

Department: Standards and Methods Information and Control
Responsible Person: Joe Kurta
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD 183 Archiving Microcode in the Engineering Documentation System
Rev. A 8-Jun-78 (Level 2: Procedures & Guidelines)

ABSTRACT: Describes the procedures and guidelines for release and control of Microcode Documentation that can be archived in the Engineering Documentation System.

Department: Standards and Methods Information and Control
Responsible Person: Joe Kurta
Stds. Mgmt. Group: Engineering Information and Documentation Standards

Table 1 (Cont'd)

DEC STD 184 — ROM/PROM Documentation: Process and Requirements
CHANGE IN PROCESS — Rev. A 13-Sep-79 (Level 2: Requirements & Procedures)

ABSTRACT: Describes the procedures and requirements for development release, and control of ROM/PROM documentation in the Engineering Documentation System.

STATUS: REV. B(X00) 21-MAY-80 AWAITING INPUT ON REVIEW ISSUES

Department: Standards and Methods Information and Control
Responsible Person: Joe Kurta
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD 185 — Documentation of Computer Media in the Engineering Documentation System
CHANGE IN PROCESS — Rev. A 14-Dec-78 (Level 2: Requirements)

ABSTRACT: Describes how to identify and control revision of computer media used in the design of products at Digital.

STATUS: AWAITING DISPOSITION OF REV. B(X01) 29-APR-80 REVIEW COMMENTS

Department: Standards and Methods Information and Control
Responsible Person: Joe Kurta
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD 186 — Signal Integrity
Rev. A 9-Nov-78 (Level 1: Requirement)

ABSTRACT: Describes how Digital systems should be designed, configured, and installed in order to maintain system signal integrity and thereby preserve funtionality and reliability.

Department: Electrical Integrity
Responsible Person: Rich Seifert
Stds. Mgmt. Group: Hardware Design Assurance

Table 1 (Cont'd)

DEC STD 187
NEW STD PLANNED

Mechanical Fab Workmanship Manual
Rev. (Level 1: Requirements)

ABSTRACT: None Available

STATUS: AWAITING INPUT FROM ORIGINATOR - EXPECTED Q4-FY81

Department: Process Management Quality Assurance
Responsible Person: Dave Nevala
Stds. Mgmt. Group: Mfg. Process and Quality Assurance

DEC STD 188

Archiving Engineering Information: Policy and Procedures
Rev. A 4-Sep-80 (Level 1: Policy & Procedures)

INTERNAL USE ONLY

ABSTRACT: Digital policy and procedures for submitting Engineering information to the Archive Administration are defined. Describes what should be submitted, who should submit it, and how information should be submitted.

Department: Engineering Information Control
Responsible Person: Nat Rounds
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD 189
NEW STD PLANNED

Data Access Protocol

STATUS: AWAITING INPUT FROM ORIGINATOR

Department: Distributed Systems
Responsible Person: Henry Lowe
Stds. Mgmt. Group: Software & Architecture Standards

DEC STD 190
NEW STD PLANNED

Network Services Protocol

STATUS: AWAITING INPUT FROM ORIGINATOR

Department: Distributed Systems
Responsible Person: George Conant
Stds. Mgmt. Group: Software & Architecture Standards

Table 1 (Cont'd)

DEC STD 191
NEW STD PLANNED

Maintenance Operation Protocol

STATUS: AWAITING INPUT FROM ORIGINATOR

Department: Distributed Systems
Responsible Person: Stu Wecker
Stds. Mgmt. Group: Software & Architecture Standards

DEC STD 193
NEW STD IN PROCESS

Backplane Documentation Structure
Rev. A(X05) 17-Nov-80 (Level 1: Requirements)

ABSTRACT: Describes the documentation structure required to define, document and control engineering backplane design information.

STATUS: BEING WORKED BY WIREWRAP COMMITTEE 1-DEC-80

Department: Standards and Methods Information Control
Responsible Person: Joe Kurta
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD 194
NEW STD IN PROCESS

SUDS Documentation Standard
Rev. A (X03) 28-Apr-80 (Level 2: Requirements)

ABSTRACT: Describes the requirements for identification, control, and release of SUDS generated documentation.

STATUS: AWAITING ACTION BY ORIGINATOR

Department: Stds & Methods Information & Control
Responsible Person: Joe Kurta
Stds. Mgmt. Group: Engineering Information and Documentation Standards

Table 1 (Cont'd)

DEC STD 195
NEW STD PLANNED
NOT SCHEDULED

Unigraphics Documentation Standard
(Level 2: Requirements)

ABSTRACT: Describe the requirements for identification, control and release of Unigraphics generated documentation.

STATUS: CONTENTS PUBLISHED IN MEMO; STANDARD IN QUEUE

Department: Standards and Methods Information and Control
Responsible Person: Joe Kurta
Stds. Mgmt. Group: Engineering Information and Documentation Standards

DEC STD 196
NEW STD PLANNED

Dec Tape II Interchange Volume Format

ABSTRACT: Not available

STATUS: AWAITING INPUT FROM ORIGINATOR

Department: Small Systems Software
Responsible Person: T.W. McIntyre
Stds. Mgmt. Group: Software & Architecture Standards

DEC STD 197

Legal Guidelines for Digital Publications
Rev. B 8-Jan-81 (Level 1: Guidelines)

ABSTRACT: Defines Legal Guidelines for writing and reviewing major Digital publications for the purpose of controlling Digital proprietary information and protecting Digital against liability.

Department: Corporate Legal Department
Responsible Person: Tom Seikman
Stds. Mgmt. Group: Engineering Information and Documentation Standards