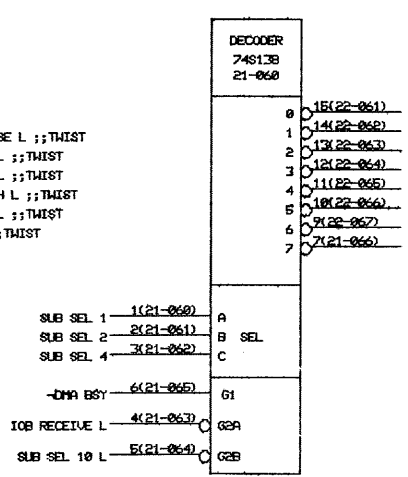
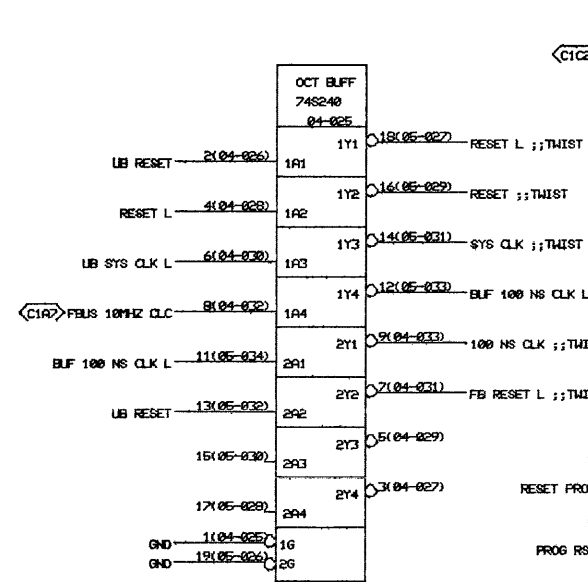
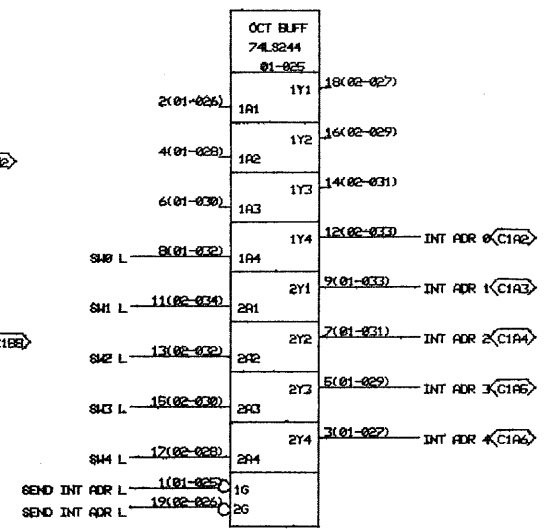
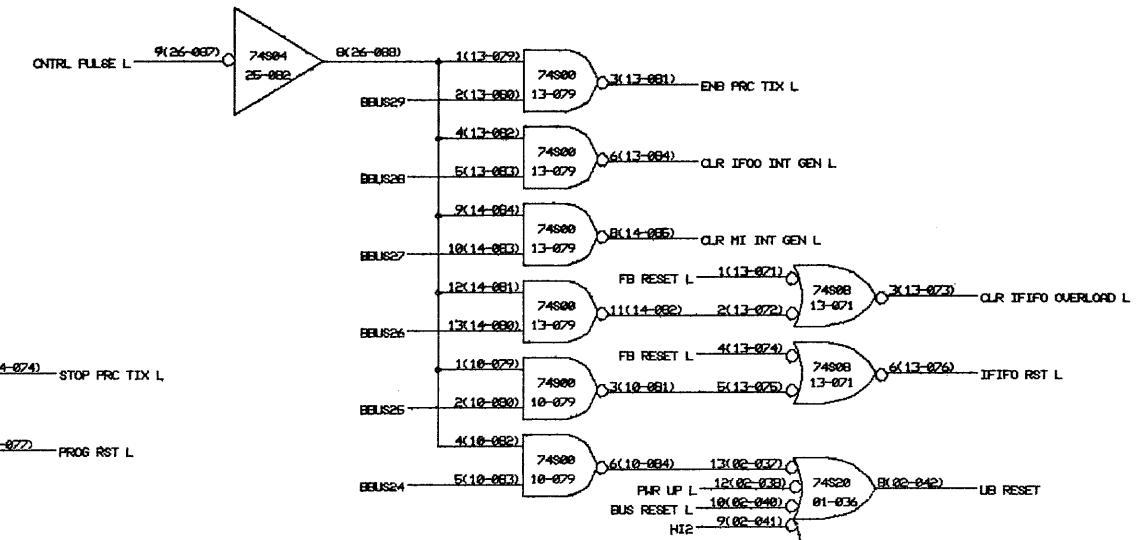
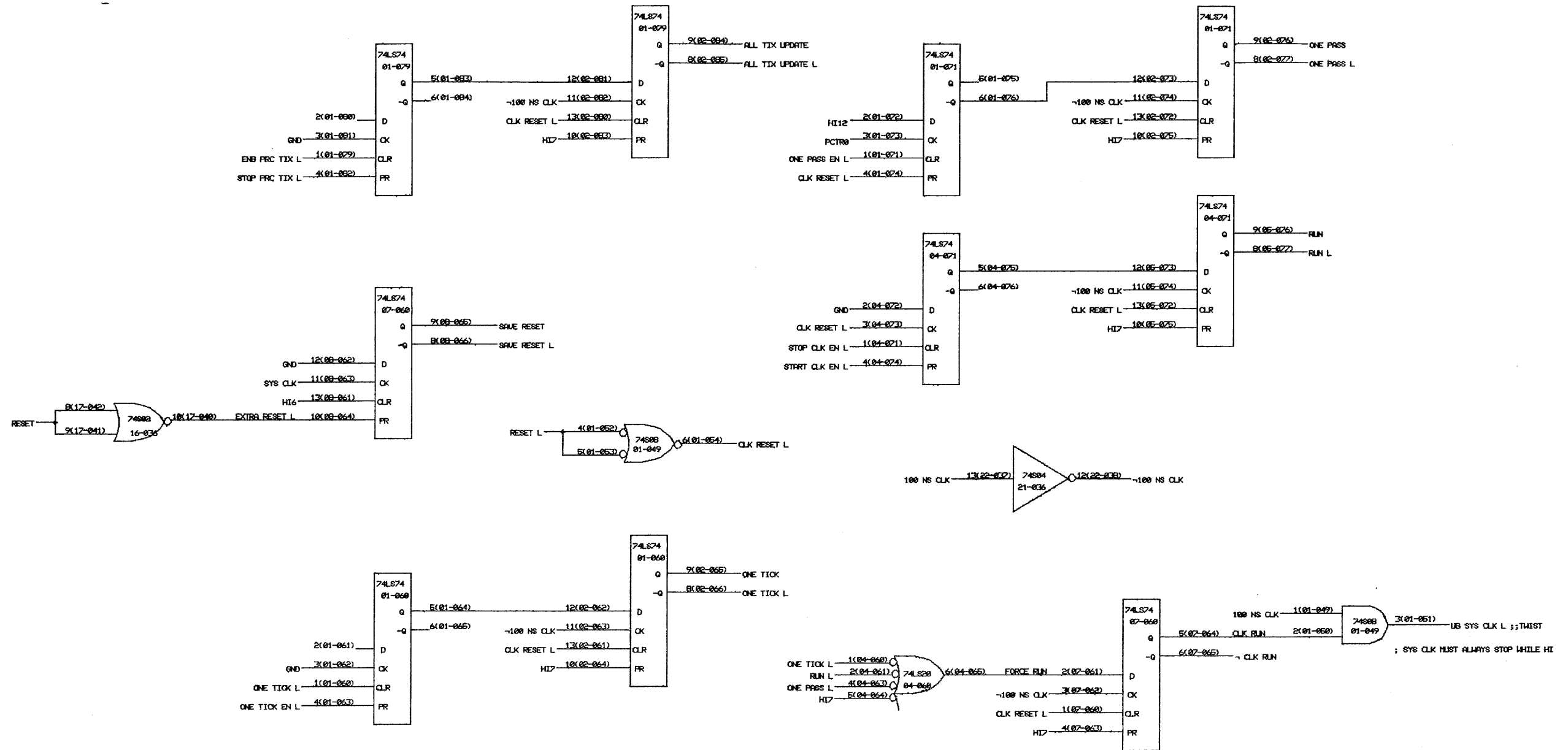
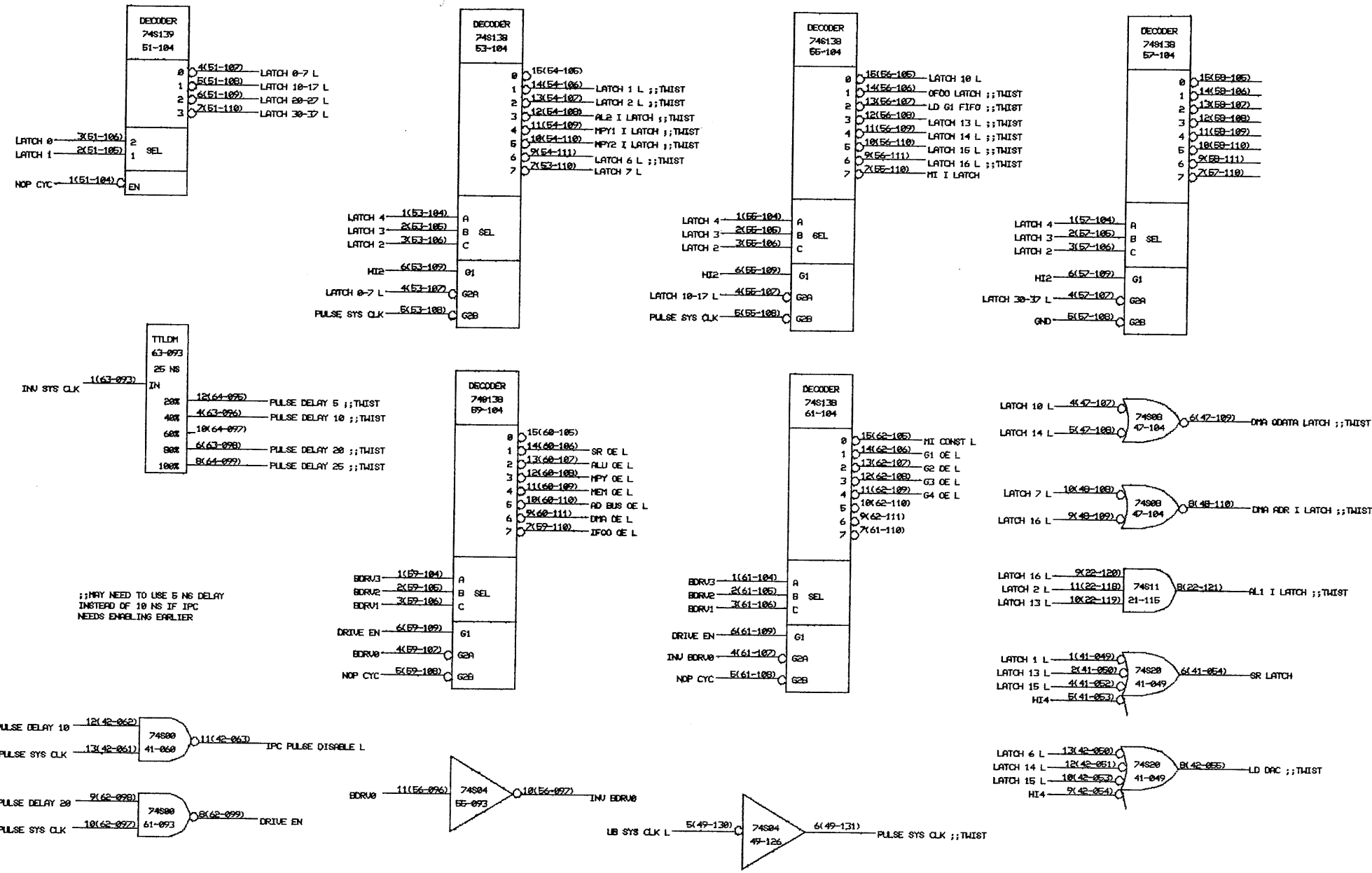
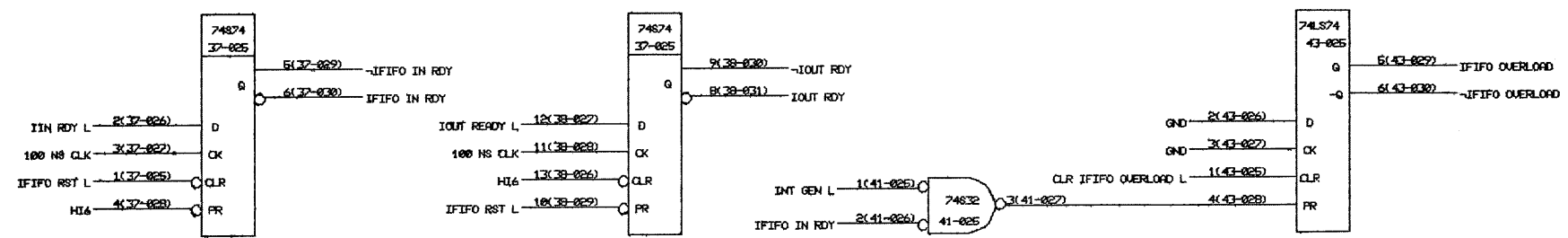
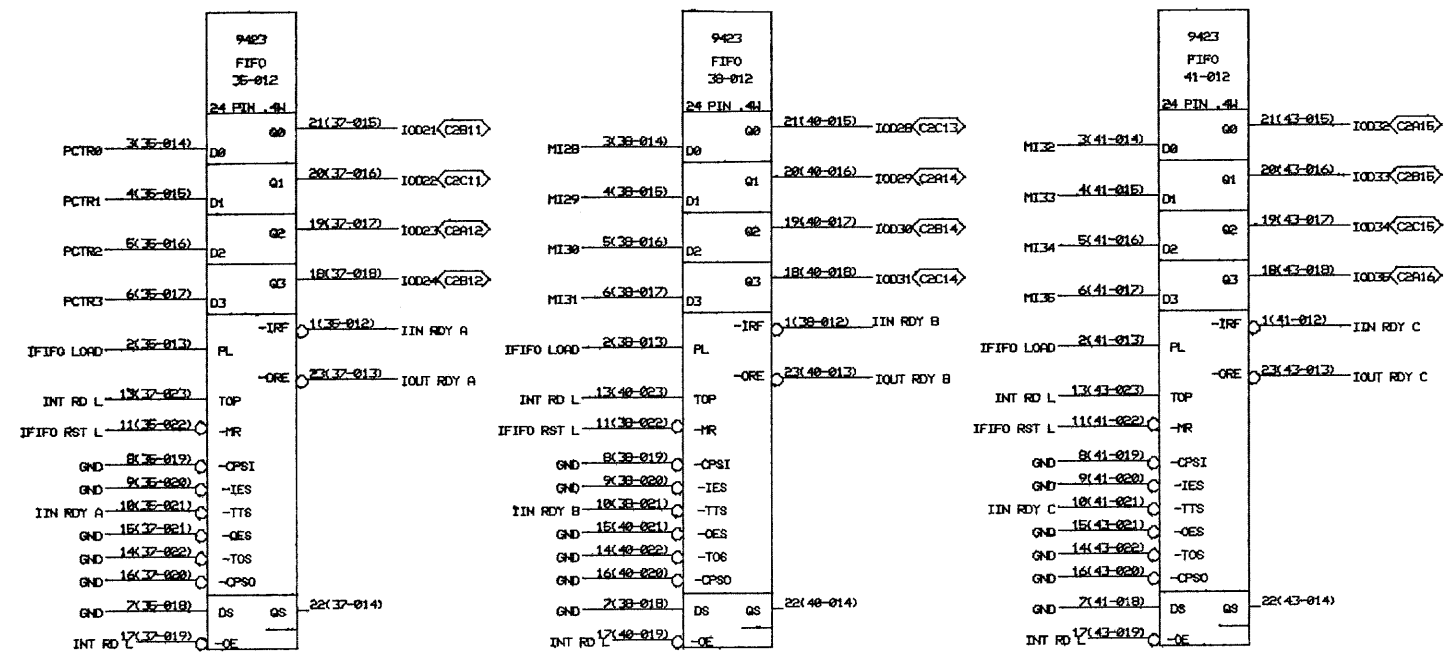


THIS IS THE INTERRUPT DRAISY CHAIN

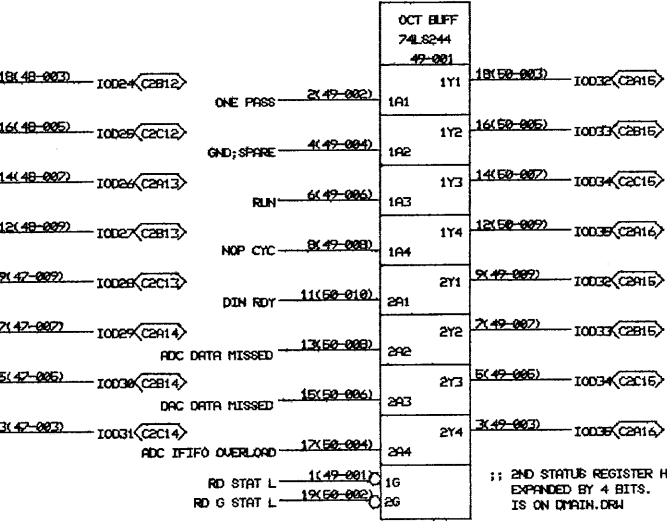
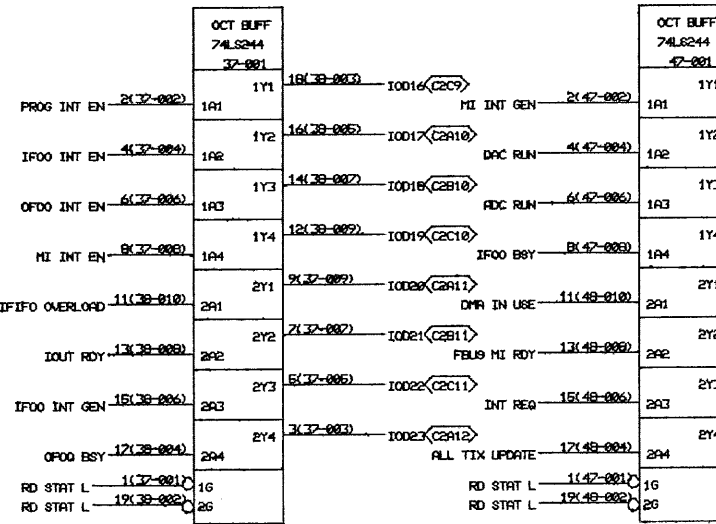
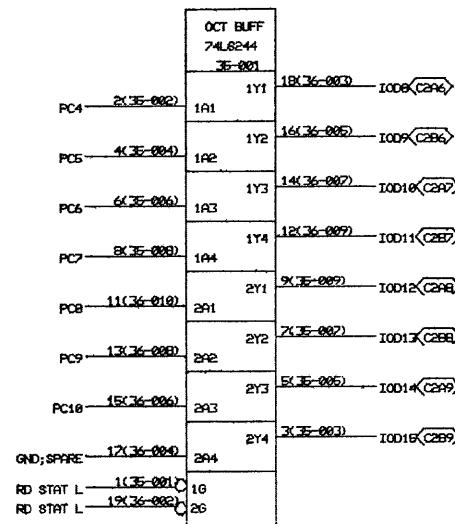
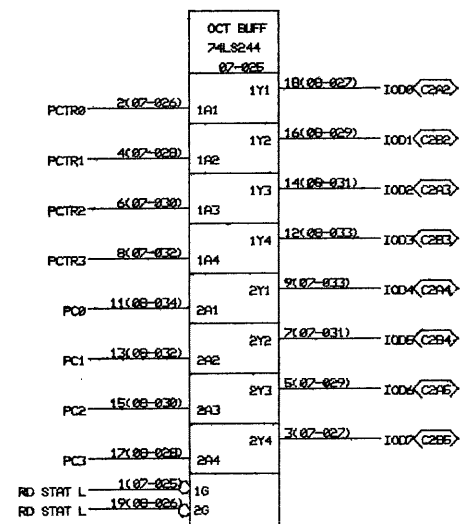




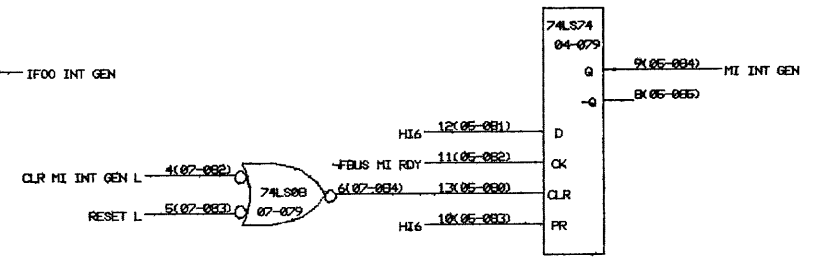
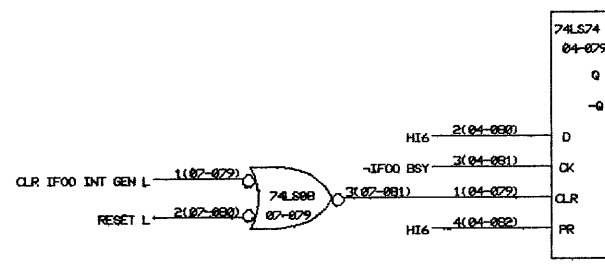
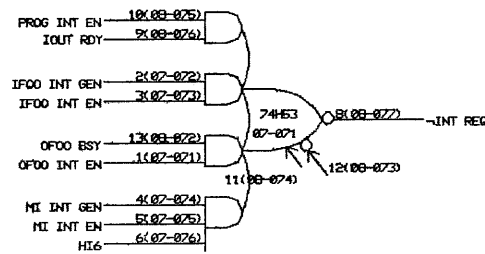
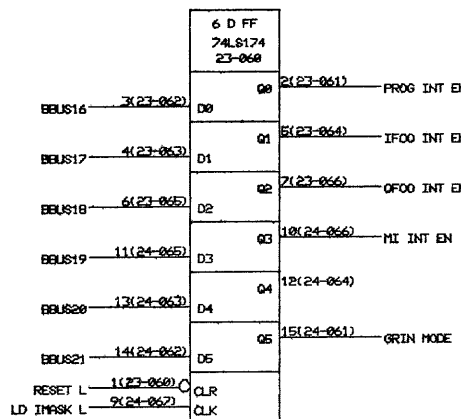




CCRMA Stanford University		POLY Interface Interrupt FIFO		06-MAR-82 19:03	
				IFIFO.DRW [605754,524155]	
DRAWN BY:	John Gordon	PAGE	5	OF	31
APPROVED BY:	<i>Zippy</i>	PROJECT:			
				NUMBER	REV.



;; 2ND STATUS REGISTER HAS BEEN EXPANDED BY 4 BITS. REST IS ON DRAIN.DRW



OCT BUFF 74LS244 04-012	
D0	2(04-013) 1A1 1Y1 18(05-014) I000(C292)
D1	4(04-015) 1A2 1Y2 16(05-016) I001(C292)
D2	6(04-017) 1A3 1Y3 14(05-018) I002(C293)
D3	8(04-019) 1A4 1Y4 12(05-020) I003(C293)
LATCH0	11(05-021) 2A1 2Y1 9(04-022) I004(C292)
LATCH1	13(05-019) 2A2 2Y2 7(04-018) I001(C292)
LATCH2	15(05-017) 2A3 2Y3 5(04-016) I002(C293)
LATCH3	17(05-015) 2A4 2Y4 3(04-014) I003(C293)
D READ L	1(04-012) 1G
MI READ L	19(05-013) 2G

OCT BUFF 74LS244 07-012	
D4	2(07-013) 1A1 1Y1 18(08-014) I004(C294)
D5	4(07-015) 1A2 1Y2 16(08-016) I005(C294)
D6	6(07-017) 1A3 1Y3 14(08-018) I006(C294)
D7	8(07-019) 1A4 1Y4 12(08-020) I007(C295)
LATCH4	11(08-021) 2A1 2Y1 9(07-022) I004(C294)
PAGE	13(08-019) 2A2 2Y2 7(07-018) I005(C294)
MAP3	15(08-017) 2A3 2Y3 5(07-016) I006(C294)
MAP4	17(08-015) 2A4 2Y4 3(07-014) I007(C295)
D READ L	1(07-012) 1G
MI READ L	19(08-013) 2G

OCT BUFF 74LS244 10-012	
D8	2(10-013) 1A1 1Y1 18(11-014) I008(C296)
D9	4(10-015) 1A2 1Y2 16(11-016) I009(C296)
D10	6(10-017) 1A3 1Y3 14(11-018) I010(C297)
D11	8(10-019) 1A4 1Y4 12(11-020) I011(C297)
MAP5	11(11-021) 2A1 2Y1 9(10-022) I008(C296)
MAP6	13(11-019) 2A2 2Y2 7(10-018) I009(C296)
MAP7	15(11-017) 2A3 2Y3 5(10-016) I010(C297)
MAP8	17(11-015) 2A4 2Y4 3(10-014) I011(C297)
D READ L	1(10-012) 1G
MI READ L	19(11-013) 2G

OCT BUFF 74LS244 13-012	
D12	2(13-013) 1A1 1Y1 18(14-014) I012(C298)
D13	4(13-015) 1A2 1Y2 16(14-016) I013(C298)
D14	6(13-017) 1A3 1Y3 14(14-018) I014(C299)
D15	8(13-019) 1A4 1Y4 12(14-020) I015(C299)
BDR0	11(14-021) 2A1 2Y1 9(13-022) I012(C298)
BDR1	13(14-019) 2A2 2Y2 7(13-018) I013(C298)
BDR2	15(14-017) 2A3 2Y3 5(13-016) I014(C299)
BDR3	17(14-015) 2A4 2Y4 3(13-014) I015(C299)
D READ L	1(13-012) 1G
MI READ L	19(14-013) 2G

OCT BUFF 74LS244 16-012	
D16	2(16-013) 1A1 1Y1 18(17-014) I016(C299)
D17	4(16-015) 1A2 1Y2 16(17-016) I017(C299)
D18	6(16-017) 1A3 1Y3 14(17-018) I018(C299)
D19	8(16-019) 1A4 1Y4 12(17-020) I019(C299)
OP0	11(17-021) 2A1 2Y1 9(16-022) I016(C299)
OP1	13(17-019) 2A2 2Y2 7(16-018) I017(C299)
OP2	15(17-017) 2A3 2Y3 5(16-016) I018(C299)
OP3	17(17-015) 2A4 2Y4 3(16-014) I019(C299)
D READ L	1(16-012) 1G
MI READ L	19(17-013) 2G

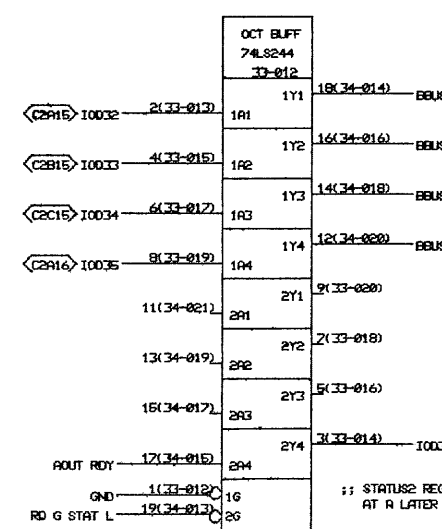
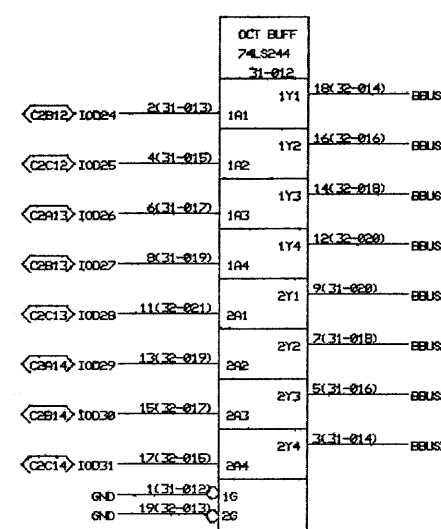
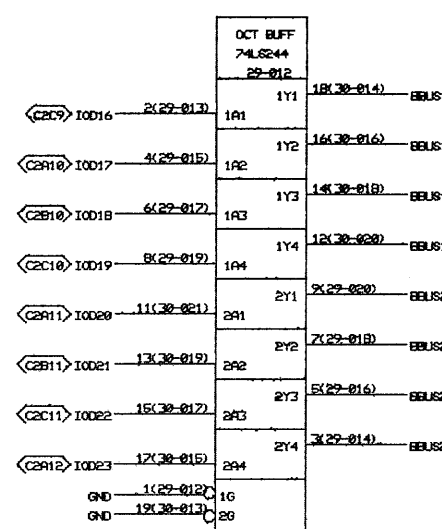
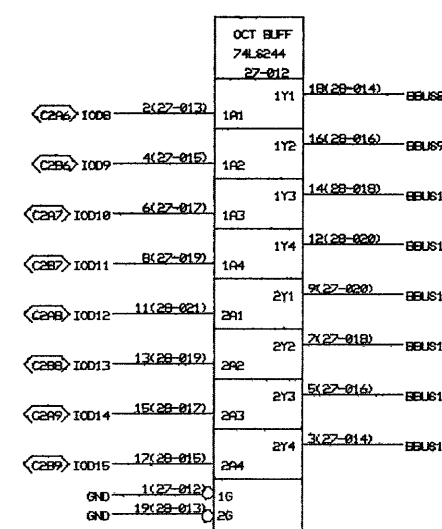
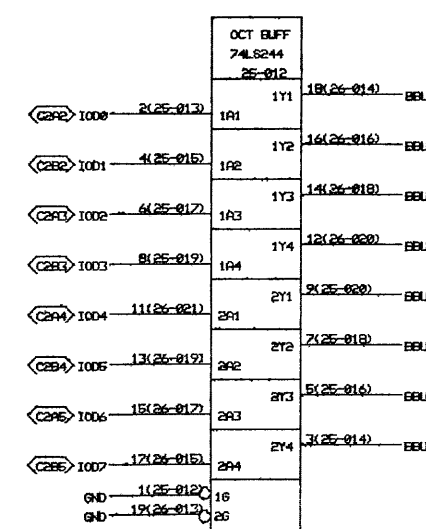
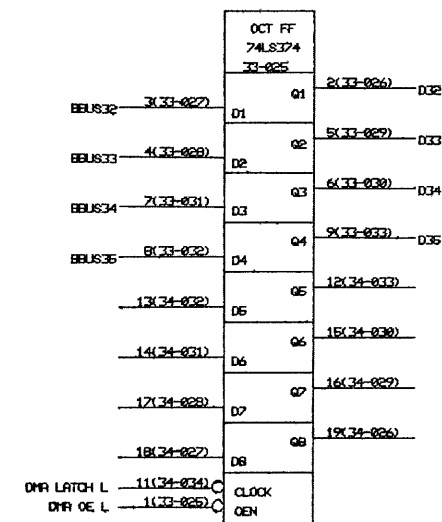
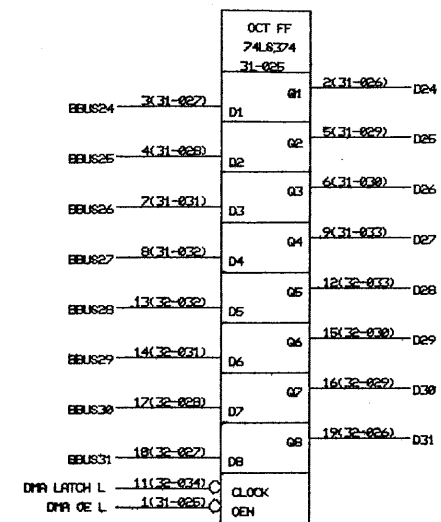
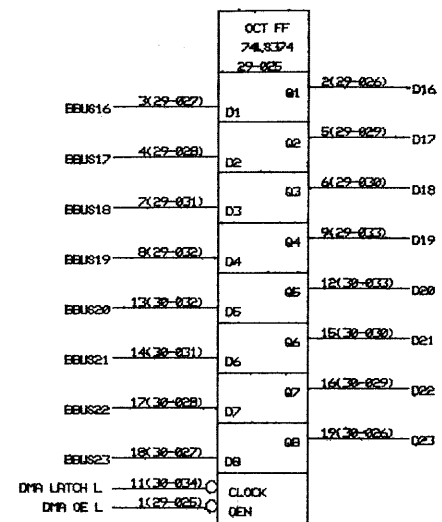
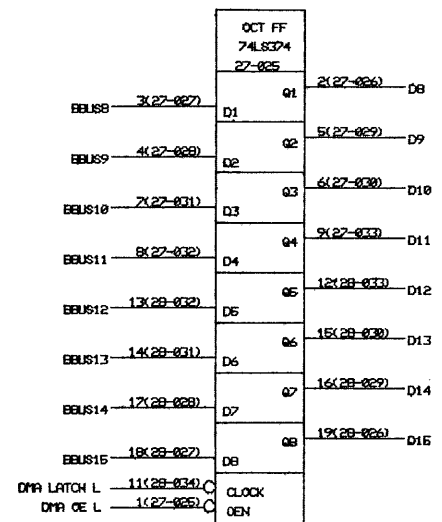
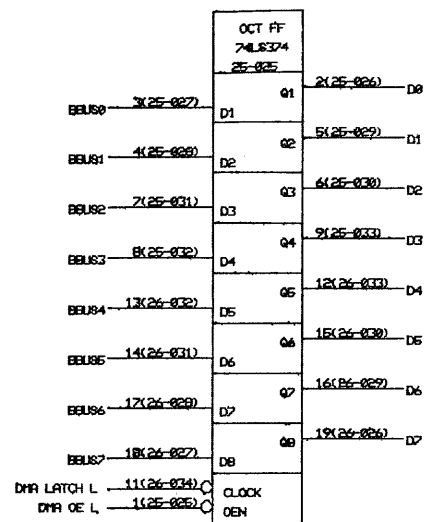
OCT BUFF 74LS244 10-025	
D20	2(10-026) 1A1 1Y1 18(11-027) I020(C291)
D21	4(10-028) 1A2 1Y2 16(11-029) I021(C291)
D22	6(10-030) 1A3 1Y3 14(11-031) I022(C291)
D23	8(10-032) 1A4 1Y4 12(11-033) I023(C291)
OP4	11(11-034) 2A1 2Y1 9(10-033) I020(C291)
OP5	13(11-032) 2A2 2Y2 7(10-031) I021(C291)
MI20	15(11-030) 2A3 2Y3 5(10-029) I022(C291)
MI21	17(11-028) 2A4 2Y4 3(10-027) I023(C291)
D READ L	1(10-025) 1G
MI READ L	19(11-026) 2G

OCT BUFF 74LS244 13-025	
D24	2(13-026) 1A1 1Y1 18(14-027) I024(C291)
D25	4(13-028) 1A2 1Y2 16(14-029) I025(C291)
D26	6(13-030) 1A3 1Y3 14(14-031) I026(C291)
D27	8(13-032) 1A4 1Y4 12(14-033) I027(C291)
MI24	11(14-034) 2A1 2Y1 9(13-033) I024(C291)
MI25	13(14-032) 2A2 2Y2 7(13-031) I025(C291)
MI26	15(14-030) 2A3 2Y3 5(13-029) I026(C291)
MI27	17(14-028) 2A4 2Y4 3(13-027) I027(C291)
D READ L	1(13-025) 1G
MI READ L	19(14-026) 2G

OCT BUFF 74LS244 16-025	
D28	2(16-026) 1A1 1Y1 18(17-027) I028(C291)
D29	4(16-028) 1A2 1Y2 16(17-029) I029(C291)
D30	6(16-030) 1A3 1Y3 14(17-031) I030(C291)
D31	8(16-032) 1A4 1Y4 12(17-033) I031(C291)
MI28	11(17-034) 2A1 2Y1 9(16-033) I028(C291)
MI29	13(17-032) 2A2 2Y2 7(16-031) I029(C291)
MI30	15(17-030) 2A3 2Y3 5(16-029) I030(C291)
MI31	17(17-028) 2A4 2Y4 3(16-027) I031(C291)
D READ L	1(16-025) 1G
MI READ L	19(17-026) 2G

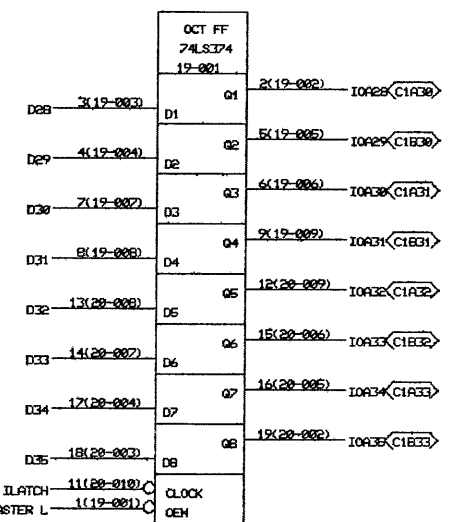
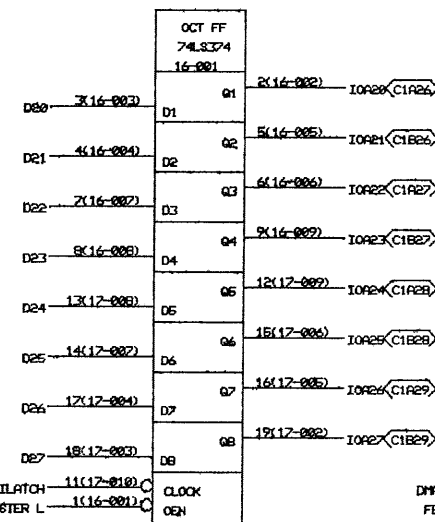
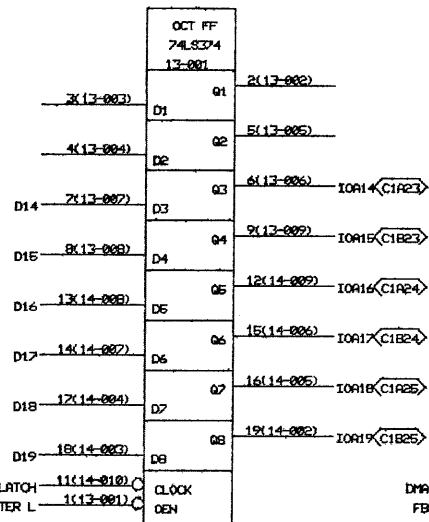
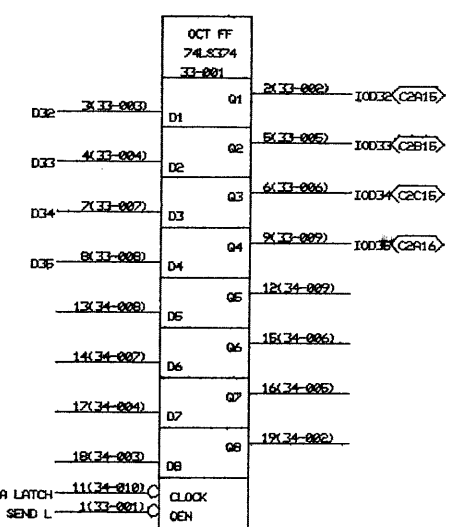
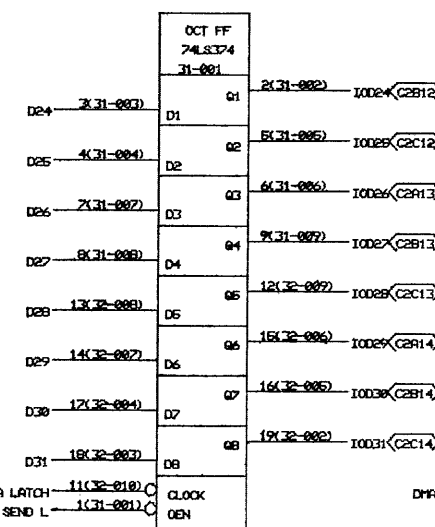
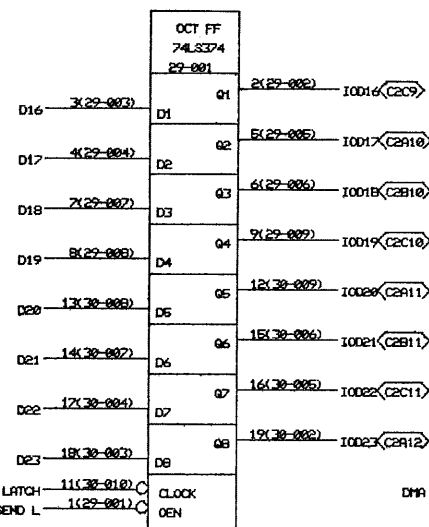
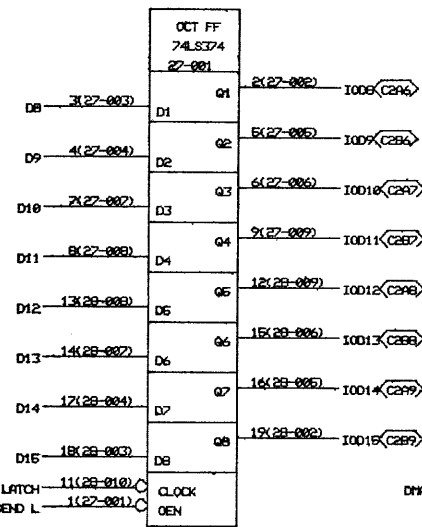
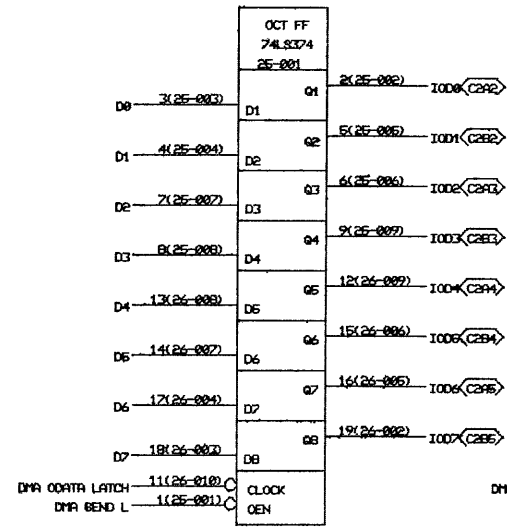
OCT BUFF 74LS244 19-025	
D32	2(19-026) 1A1 1Y1 18(20-027) I032(C291)
D33	4(19-028) 1A2 1Y2 16(20-029) I033(C291)
D34	6(19-030) 1A3 1Y3 14(20-031) I034(C291)
D35	8(19-032) 1A4 1Y4 12(20-033) I035(C291)
MI32	11(20-034) 2A1 2Y1 9(19-033) I032(C291)
MI33	13(20-032) 2A2 2Y2 7(19-031) I033(C291)
MI34	15(20-030) 2A3 2Y3 5(19-029) I034(C291)
MI35	17(20-028) 2A4 2Y4 3(19-027) I035(C291)
D READ L	1(19-025) 1G
MI READ L	19(20-026) 2G

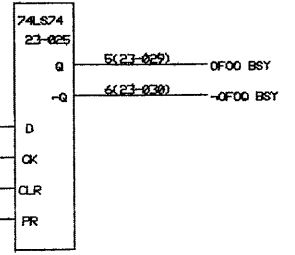
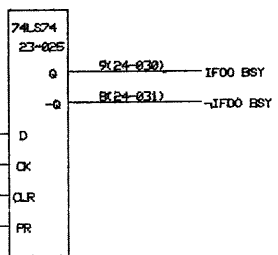
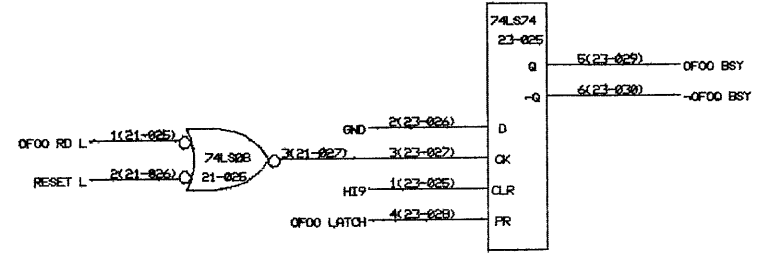
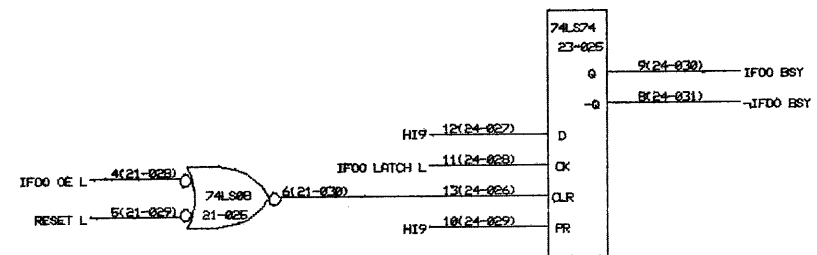
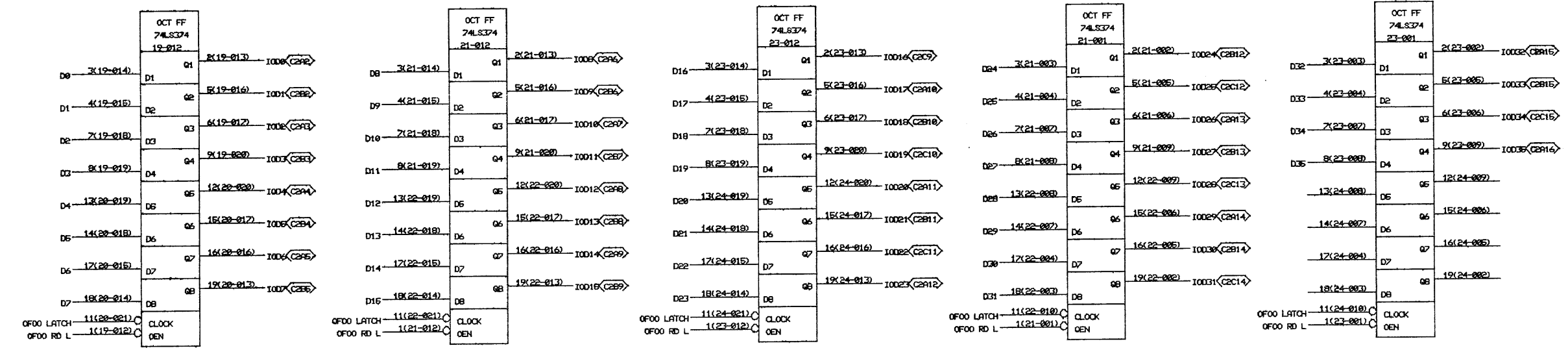
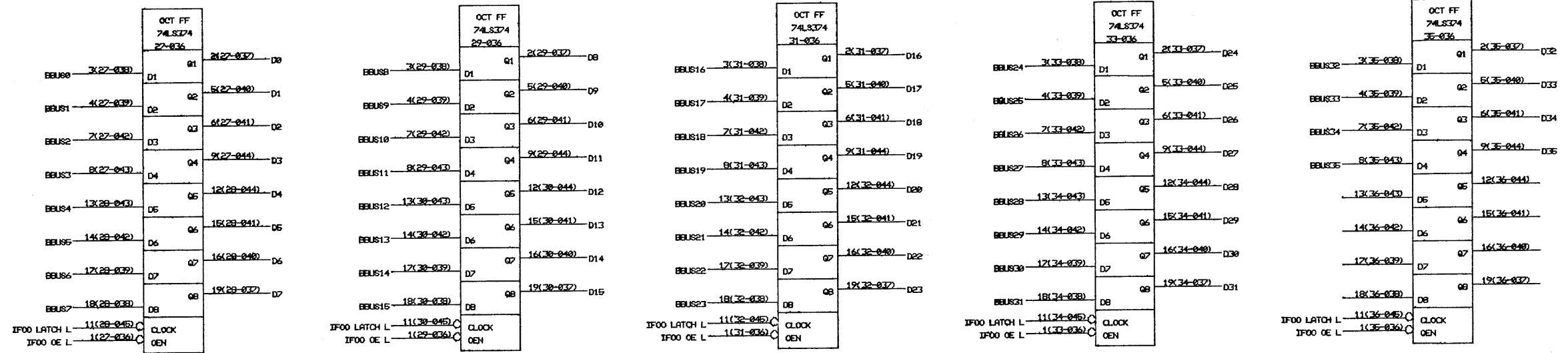
CCRMA Stanford University		POLY Interface DBUS & MI Readback		06-MAR-82 19:05	
DRAWN BY: John Gordon		PAGE 7 OF 31		DMIRD.DRW (605754, 524155)	
APPROVED BY: <i>Zippy</i>		PROJECT:		NUMBER	
				REV.	

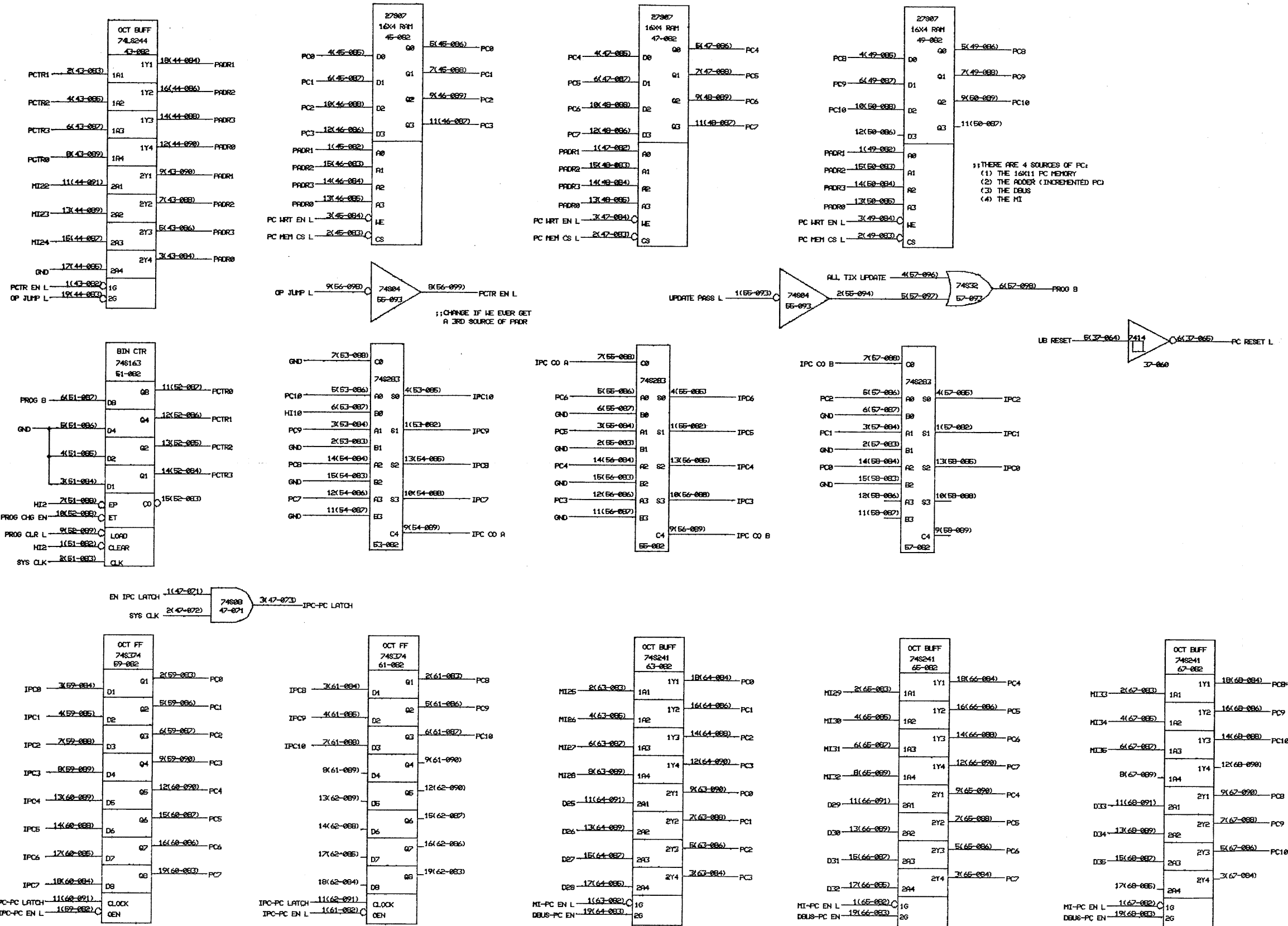


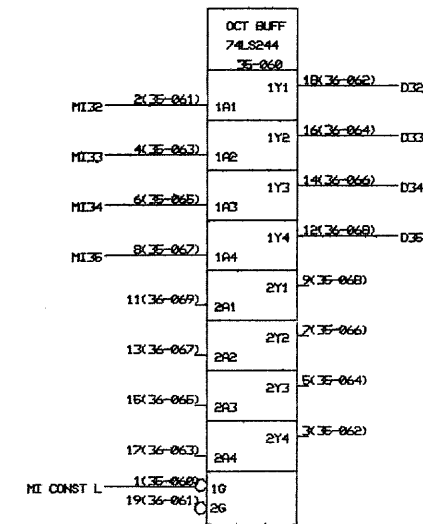
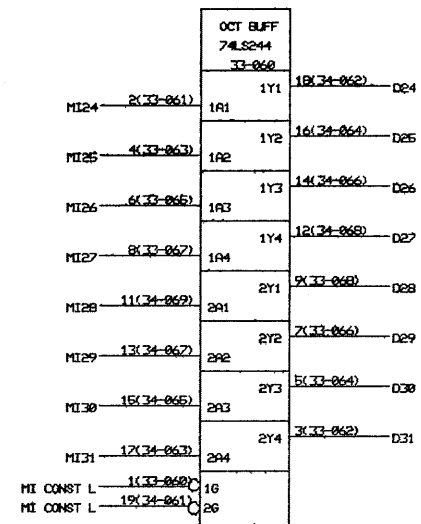
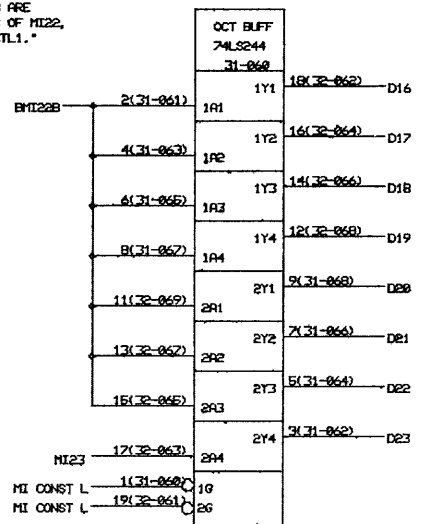
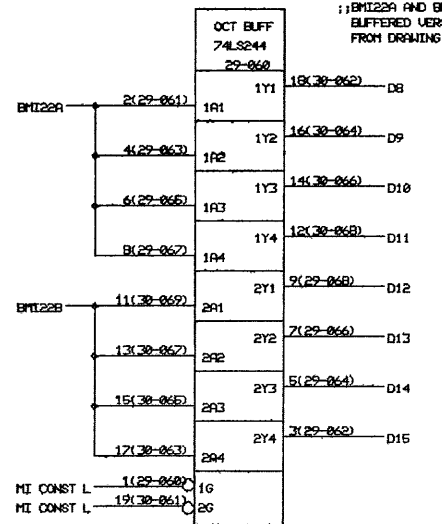
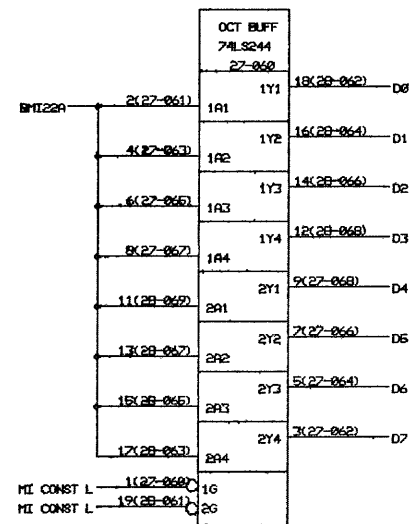
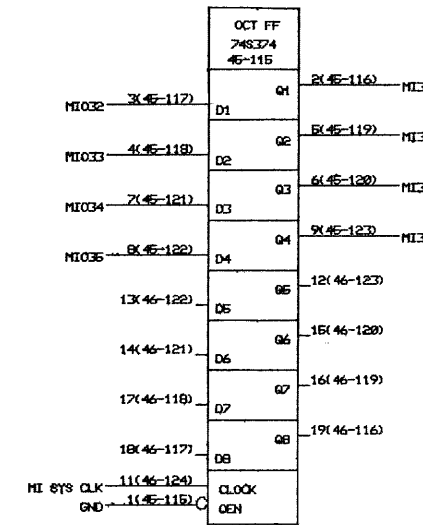
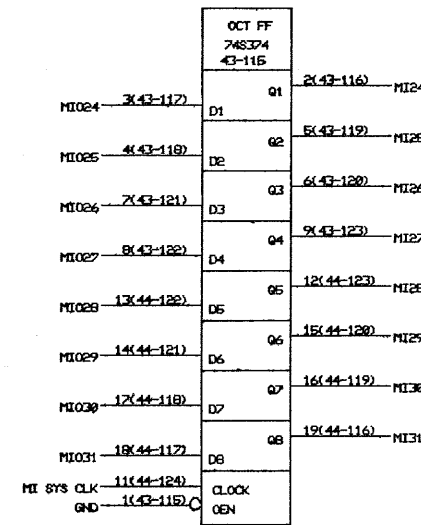
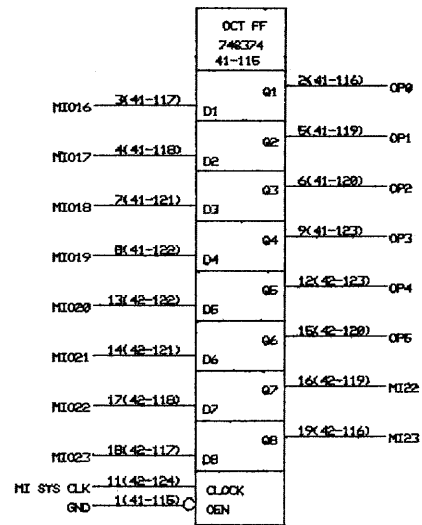
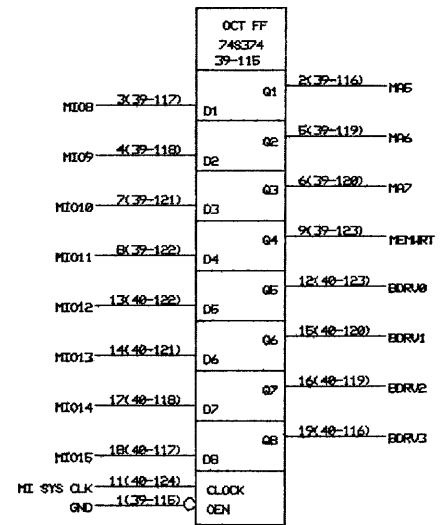
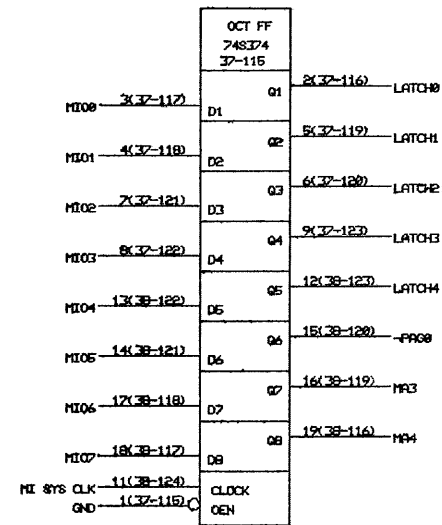
;; STATUS2 REGISTER HAS EXPANDED  
AT A LATER DATE

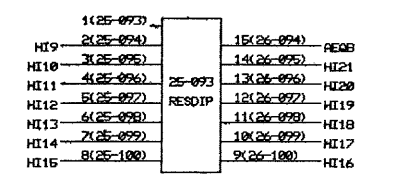
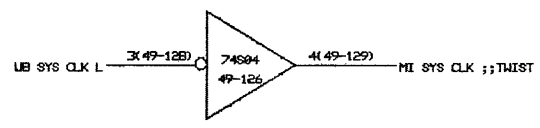
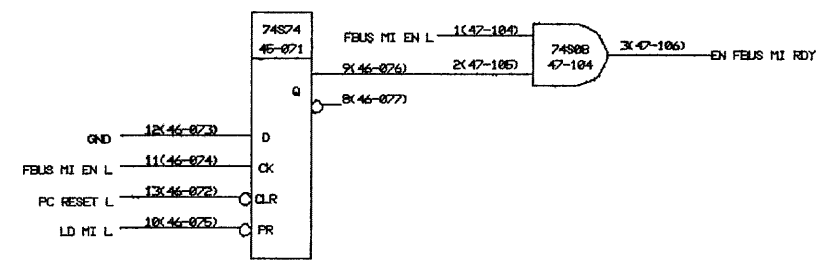
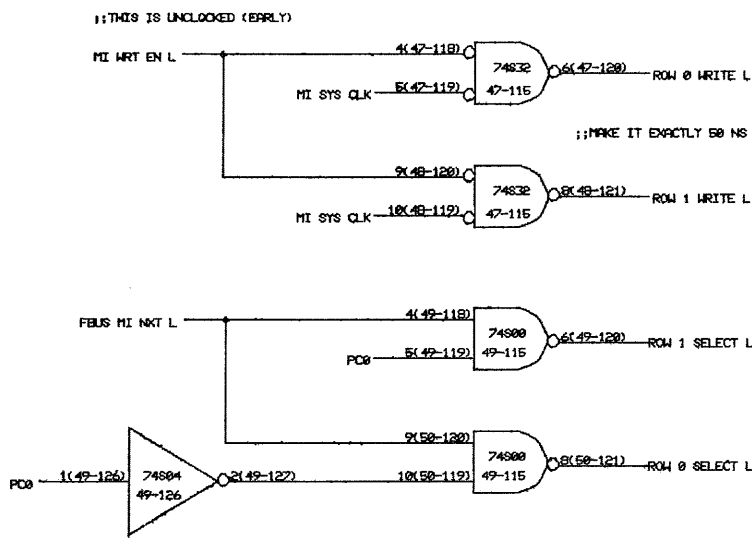
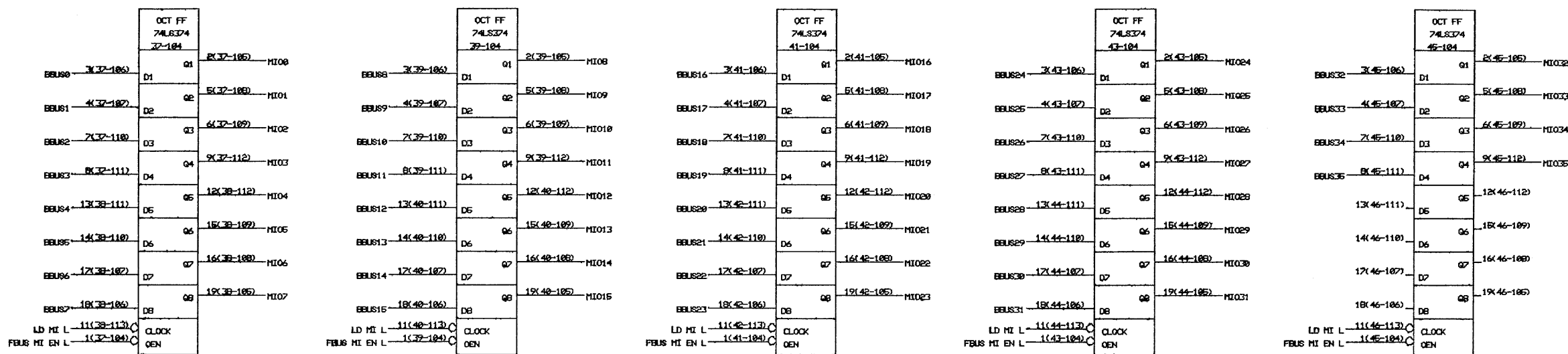






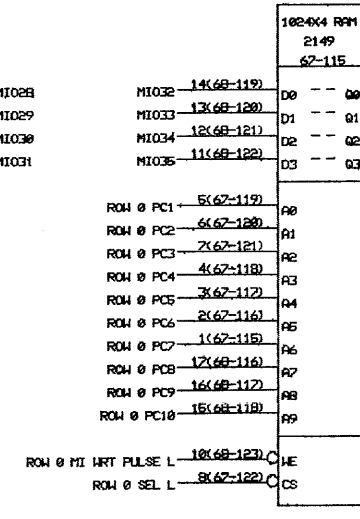
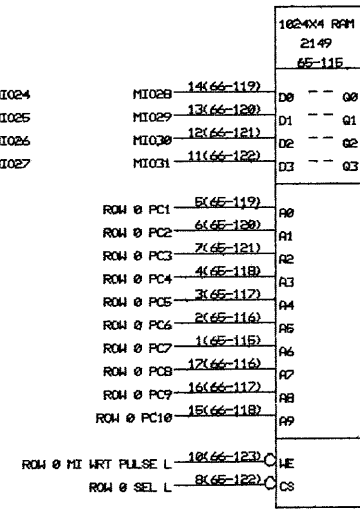
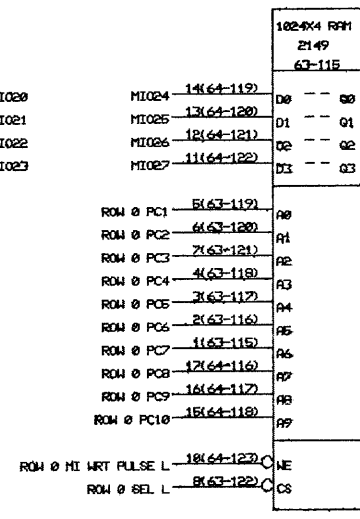
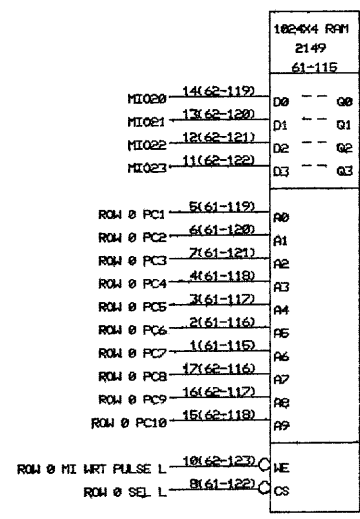
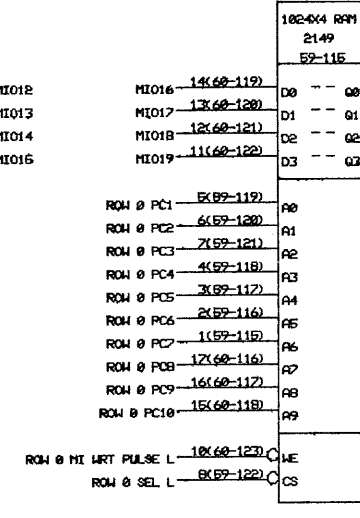
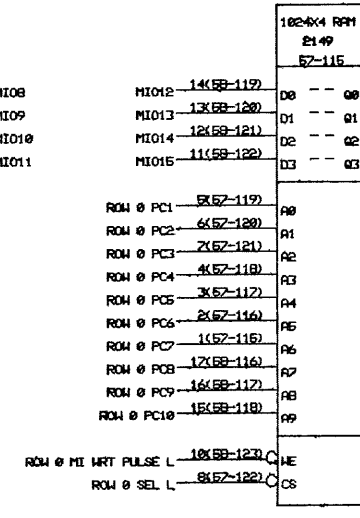
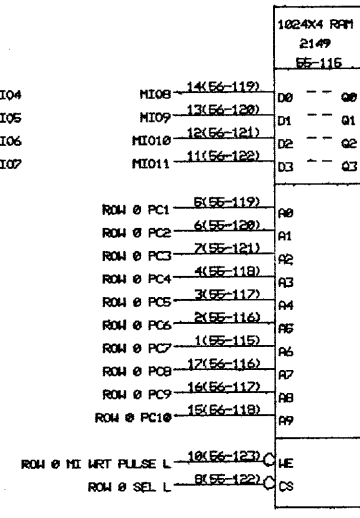
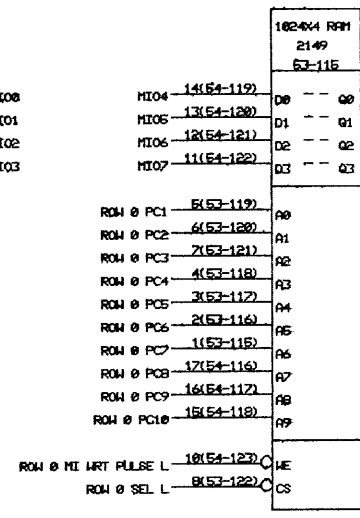
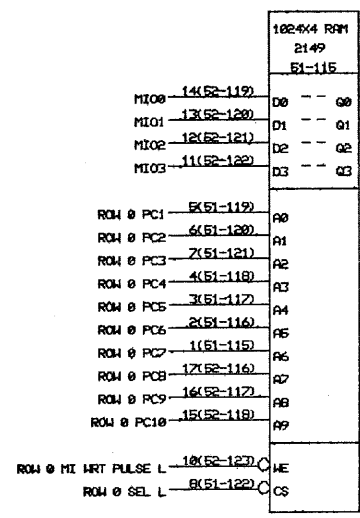
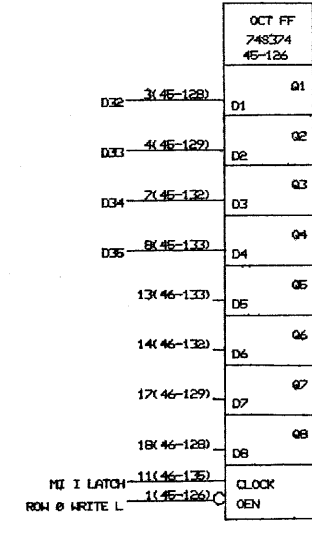
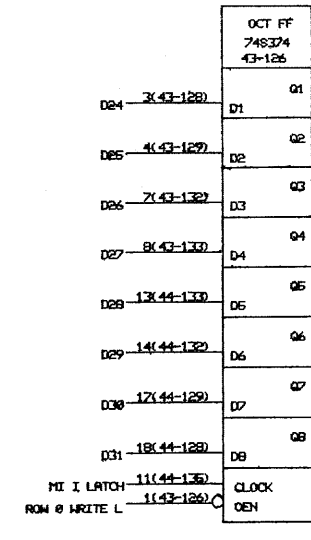
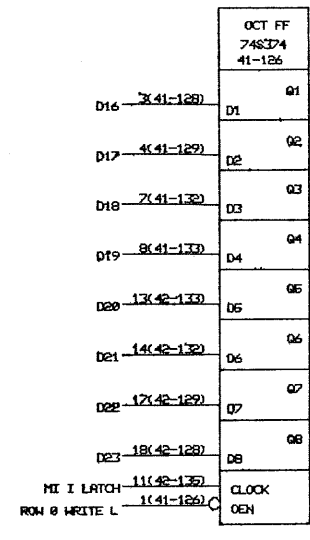
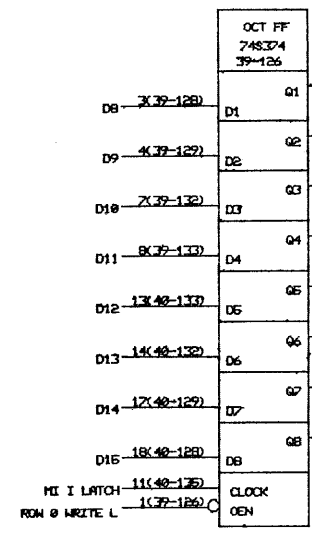
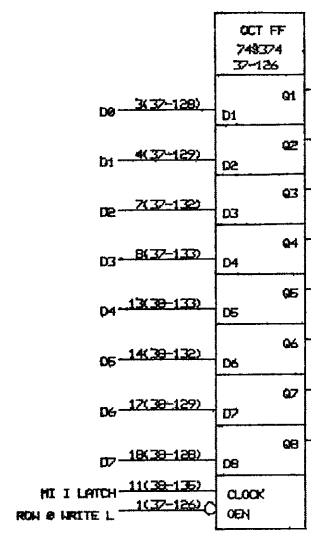


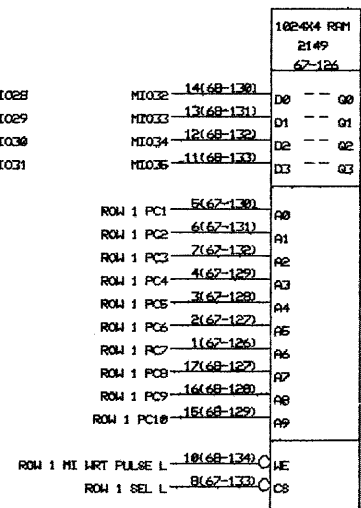
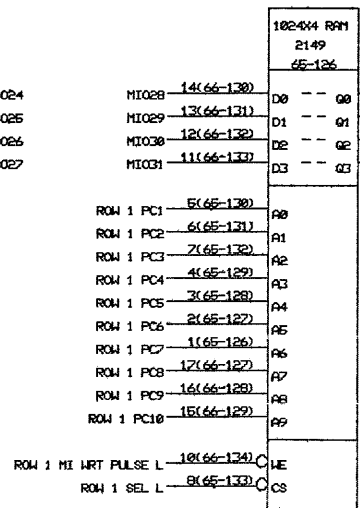
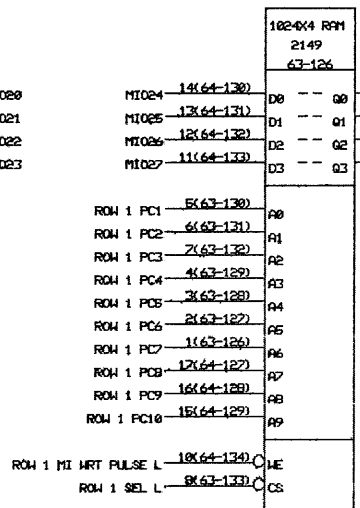
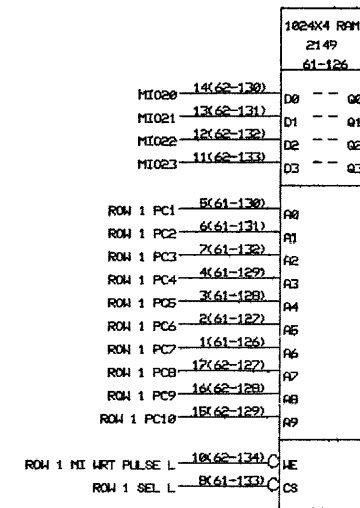
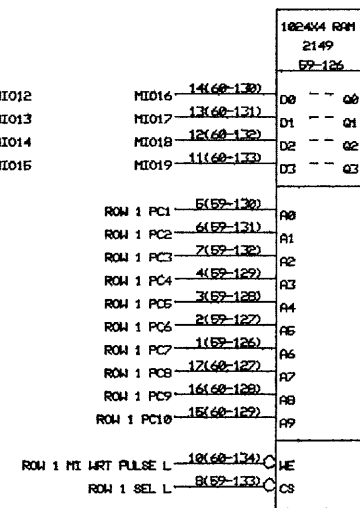
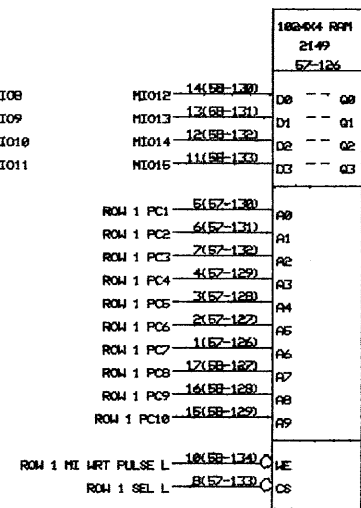
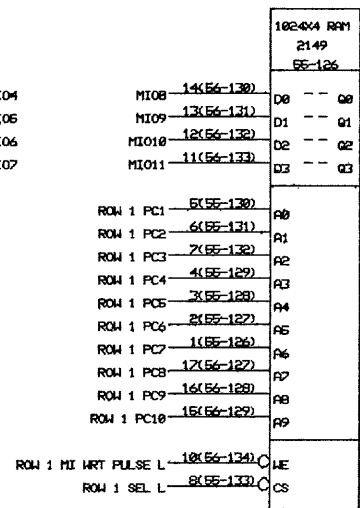
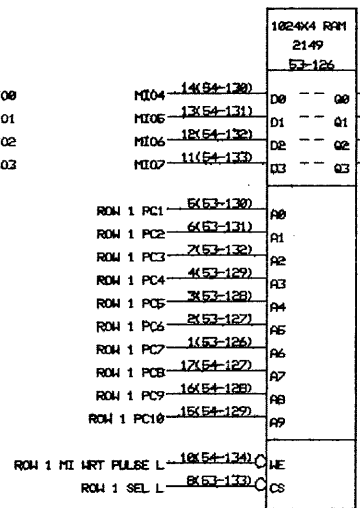
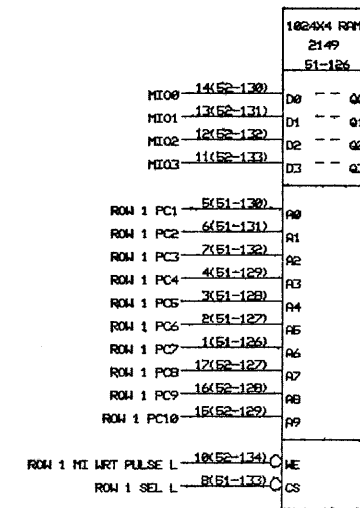
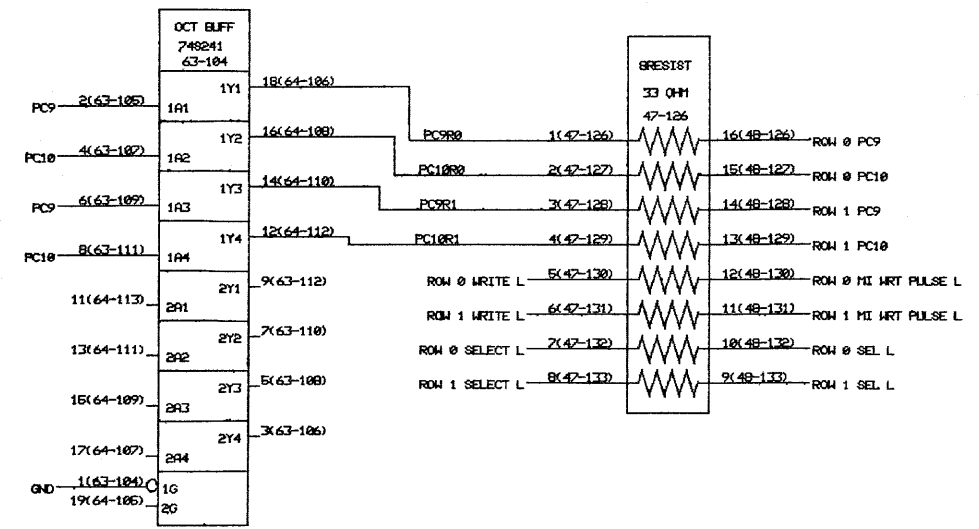
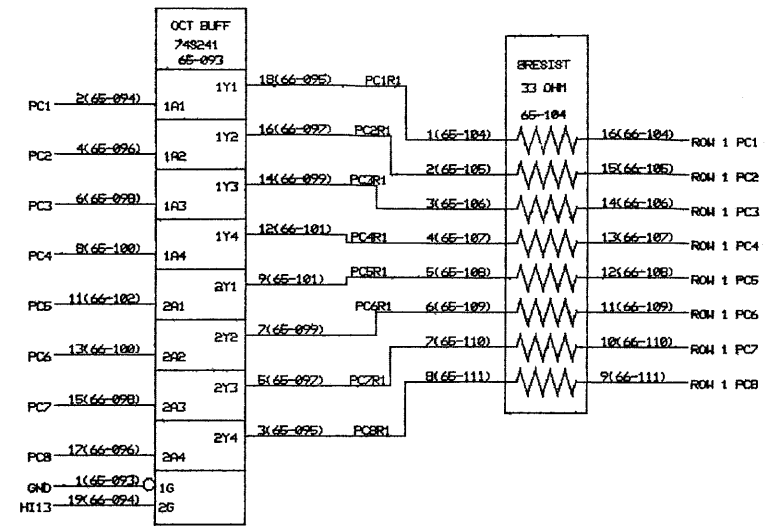
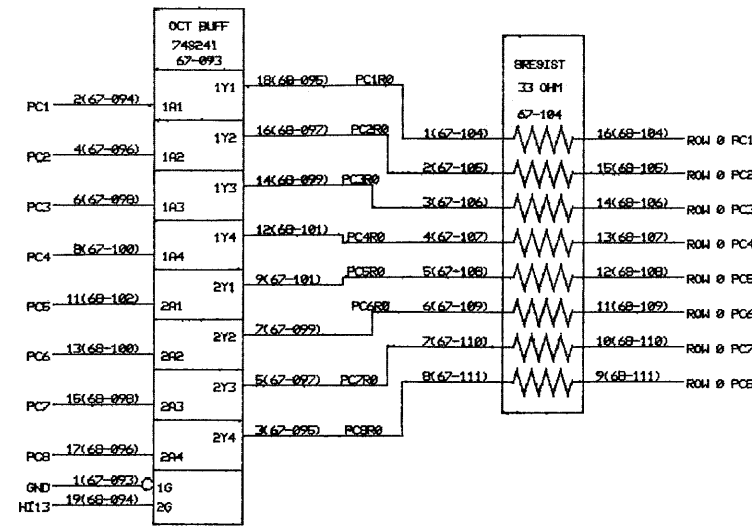


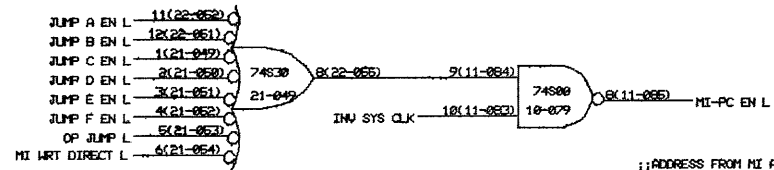
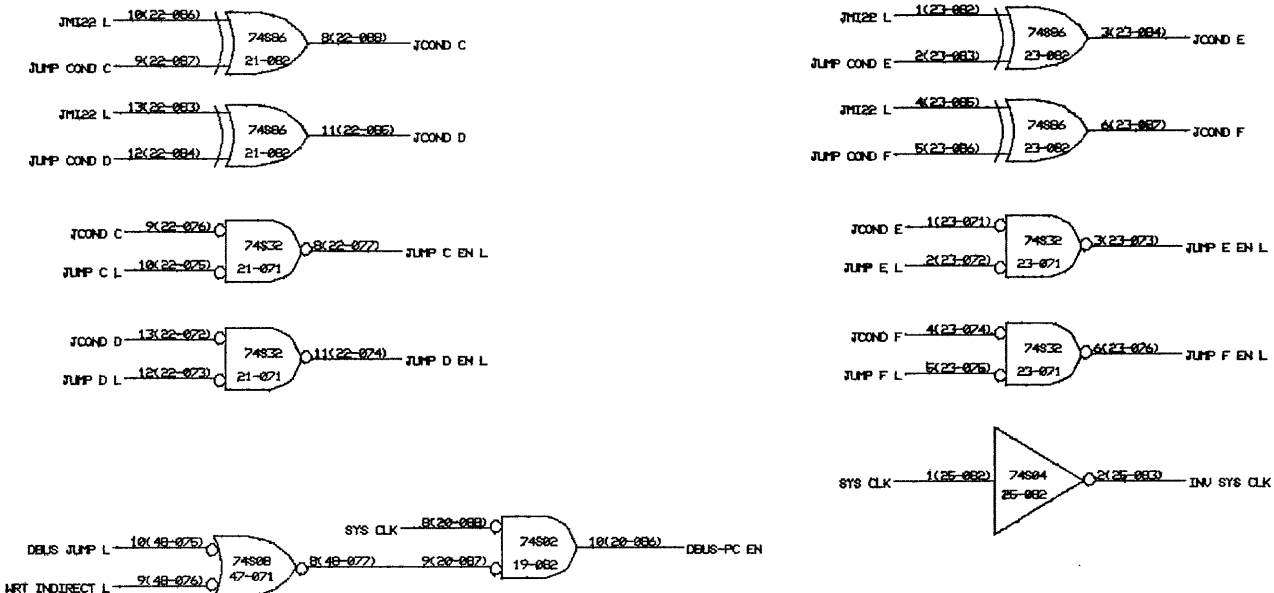
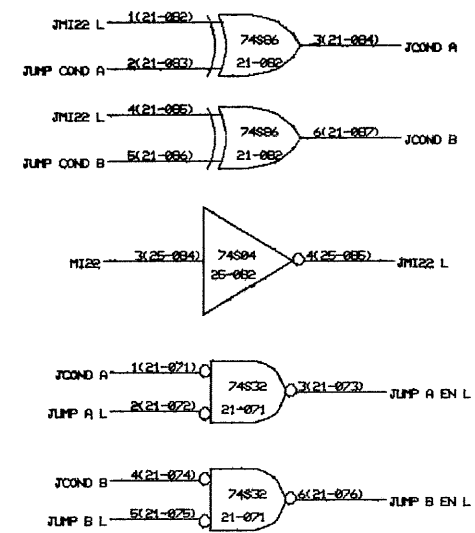
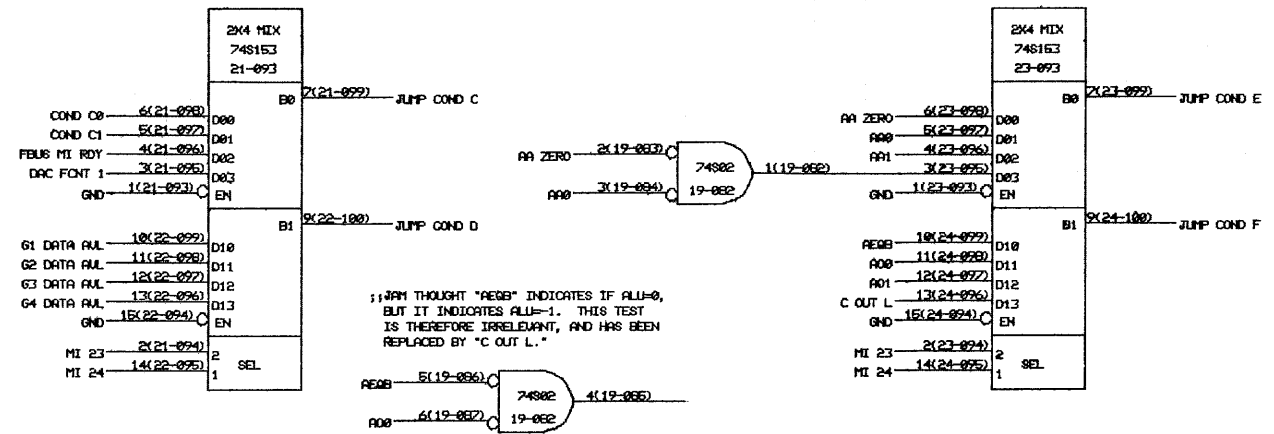
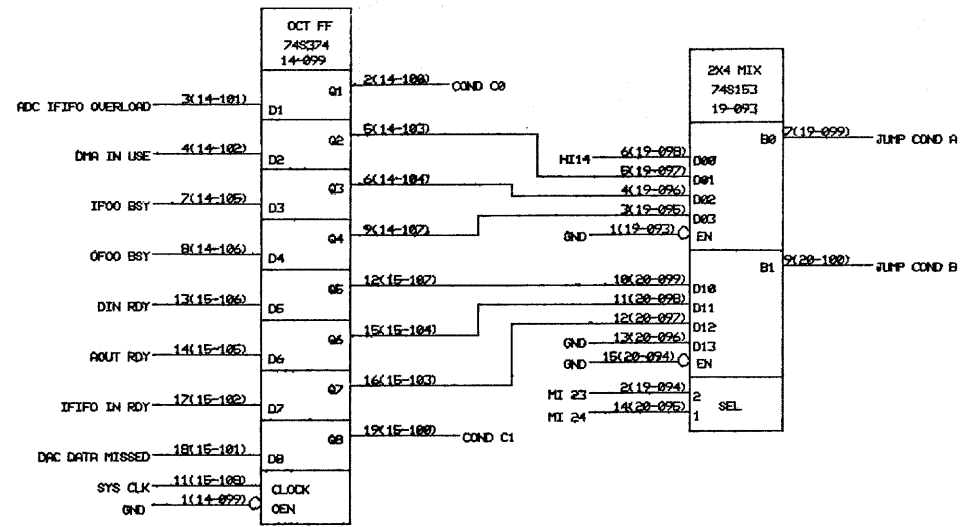


CCRMA Stanford University		POLY Interface FBUS-MI & MIMEM Control		06-MAR-82 19:12	
DRAWN BY: John Gordon		PAGE 13 OF 31		NUMBER	
APPROVED BY: <i>Zippy</i>		PROJECT:		REV.	

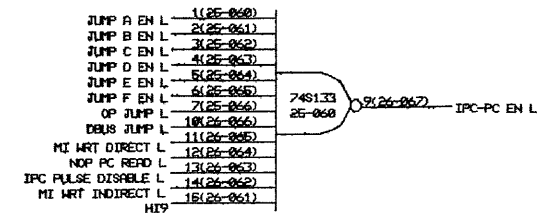
FBUSMI.DRW [605754,524155]







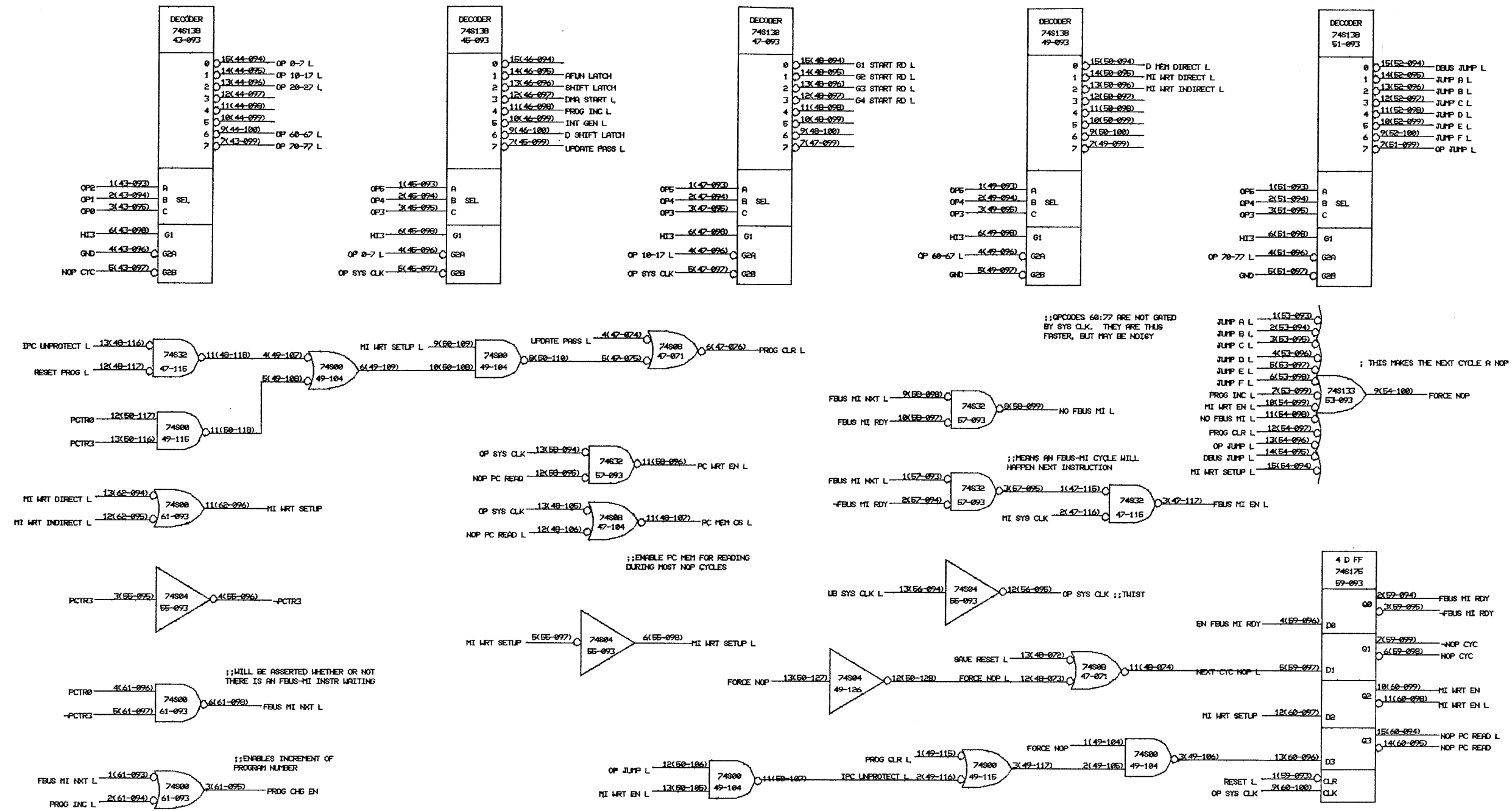
;; ADDRESS FROM MI ALWAYS GETS WRITTEN INTO PC MEMORY.



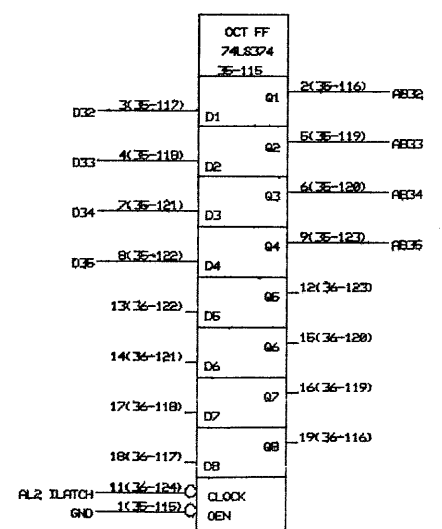
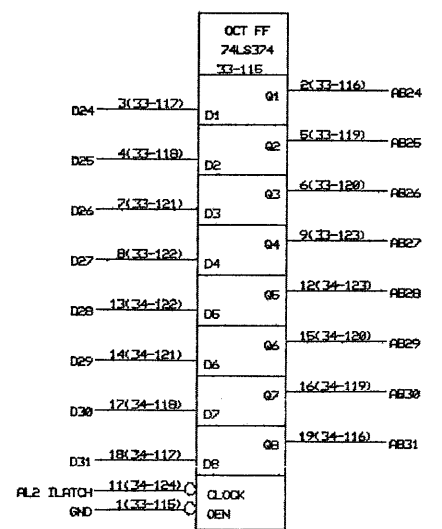
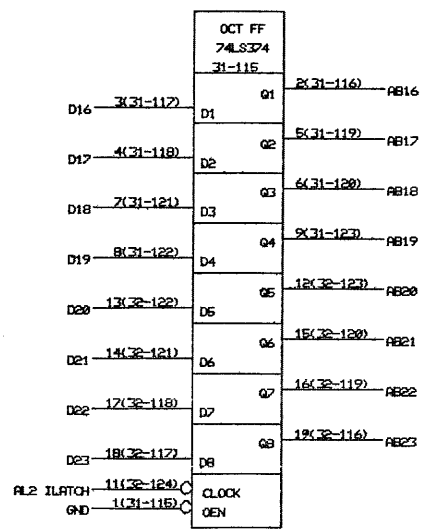
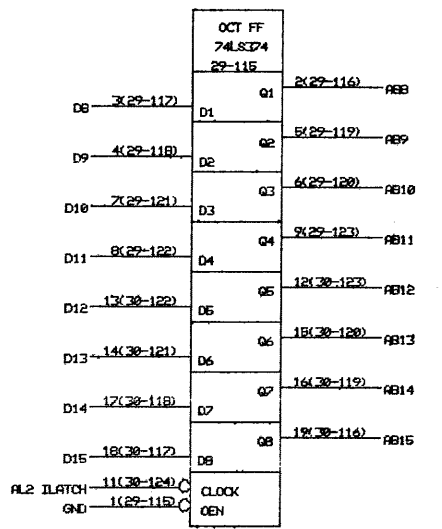
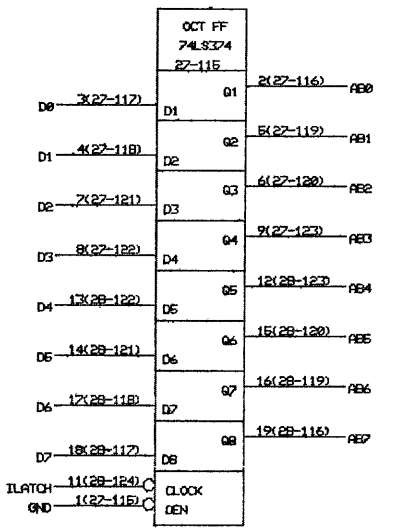
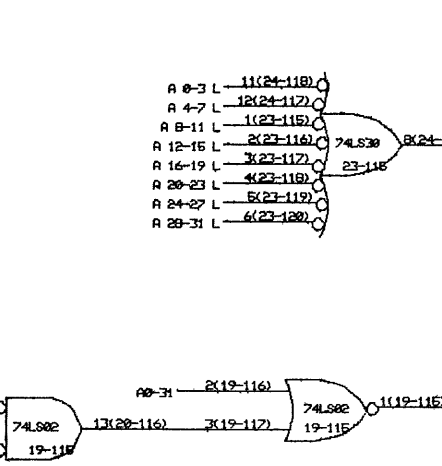
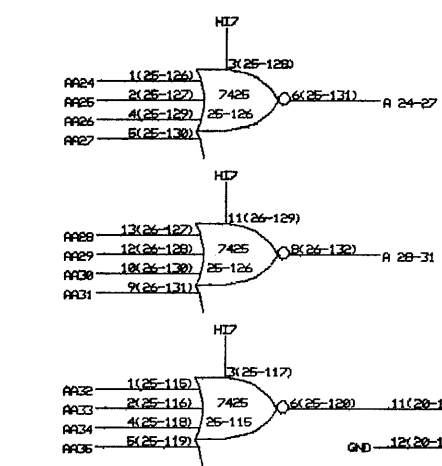
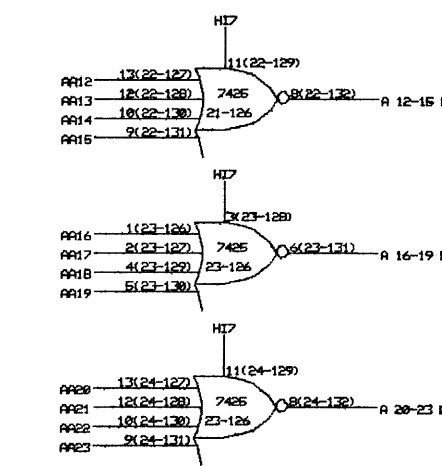
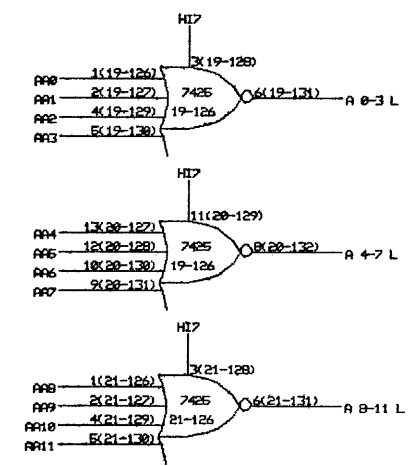
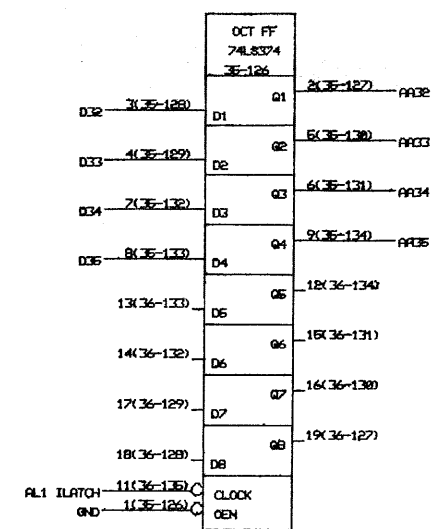
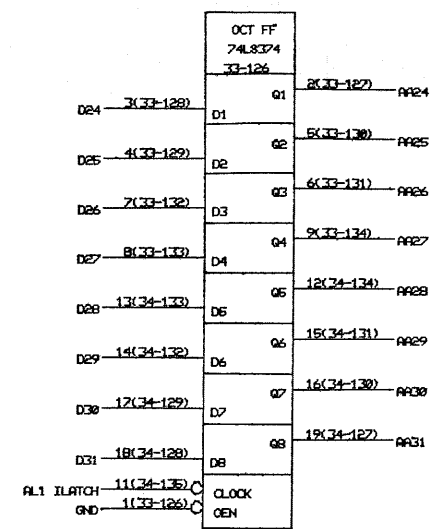
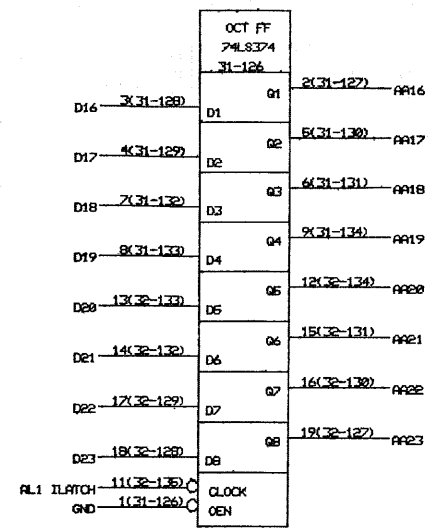
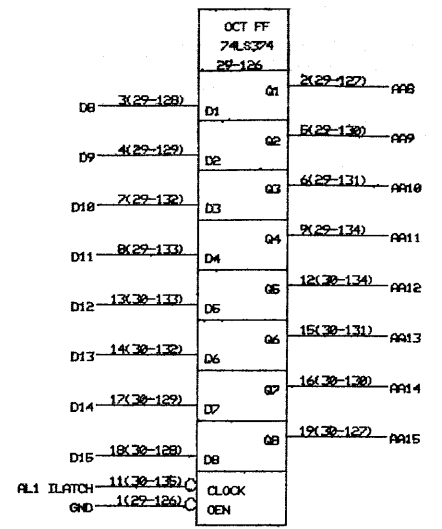
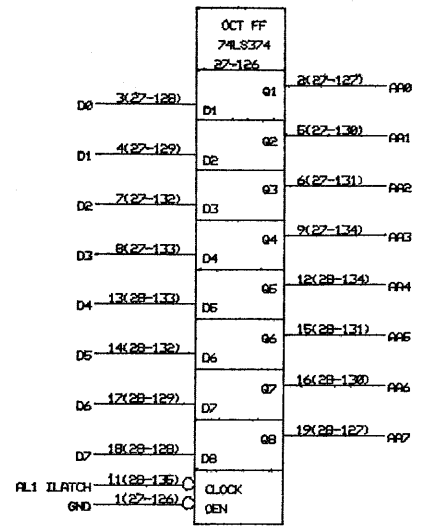
CCRMA Stanford University		POLY Interface Jumps & PC Enables		28-APR-82 13:28	
DRAWN BY: John Gordon		PAGE 16 OF 31		NUMBER	
APPROVED BY: <i>Zippy</i>		PROJECT:		REV.	

JUMP.DRW [605754,524155]

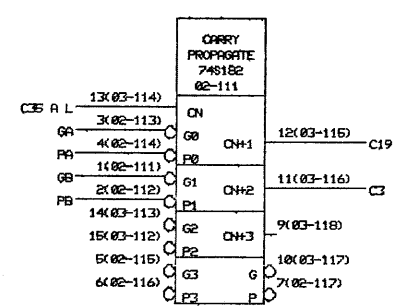
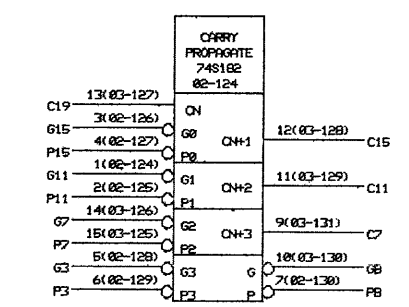
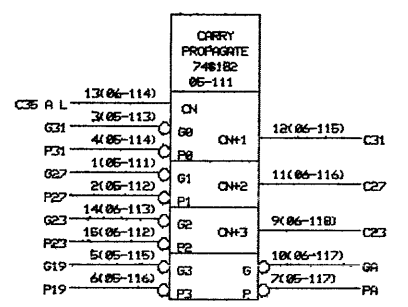
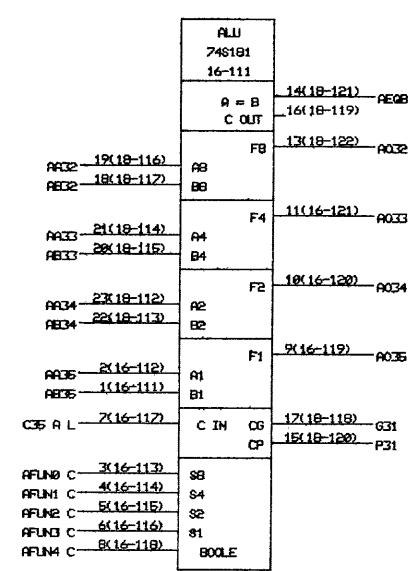
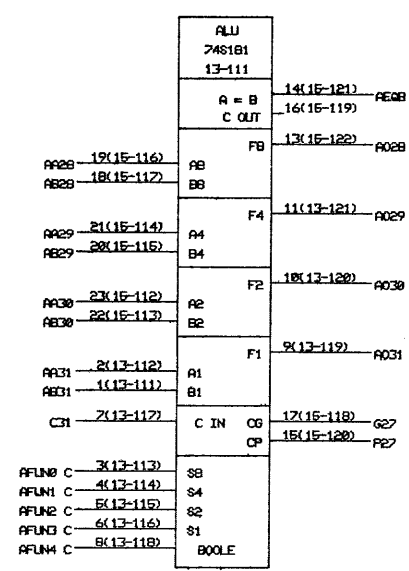
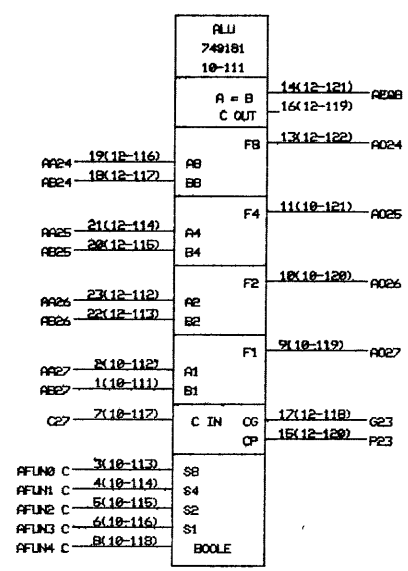
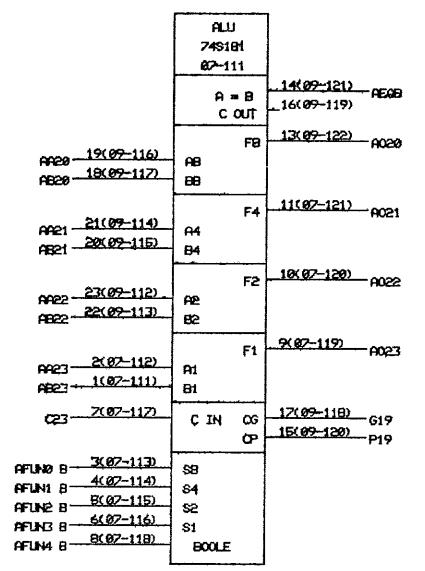
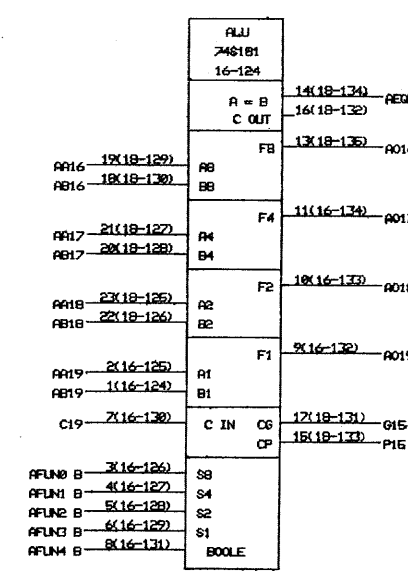
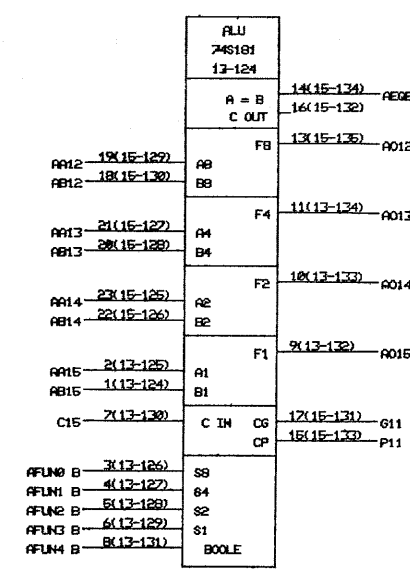
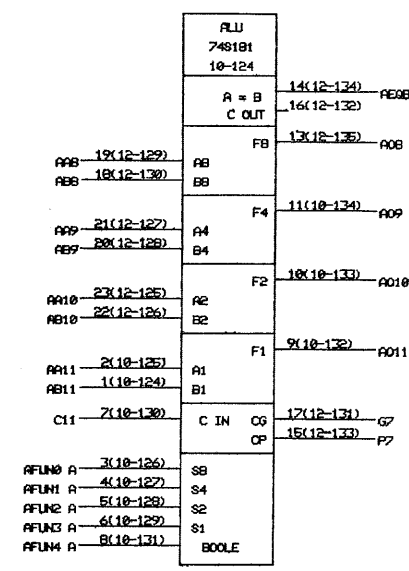
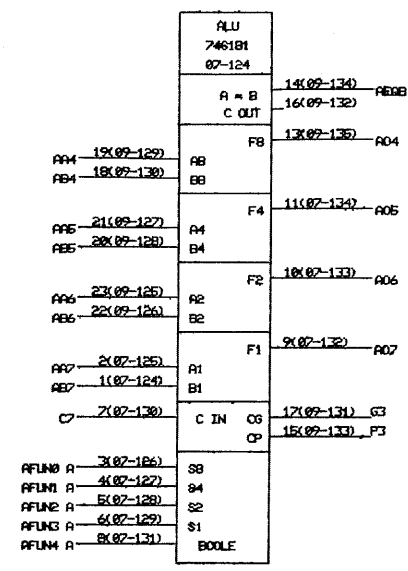
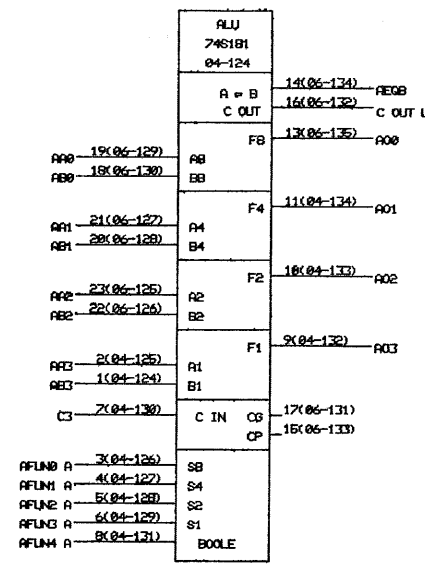


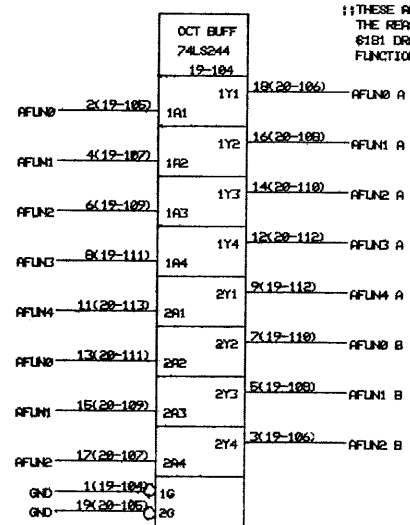
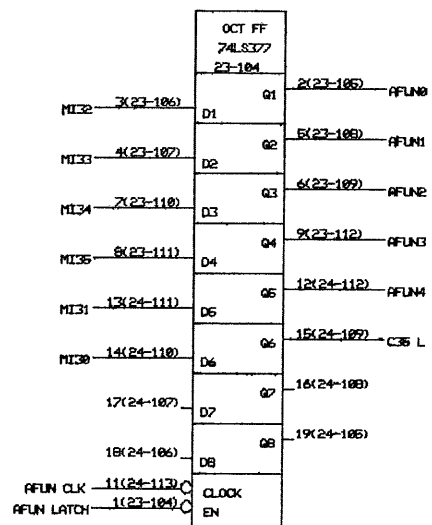
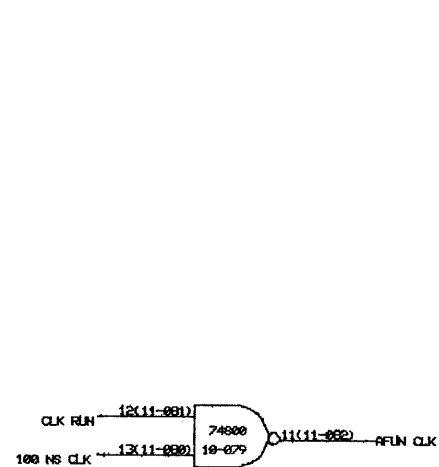


CCRMA Stanford University		POLY Interface OpCodes, NOP & FBUS-MI Logic		02-MAY-83 15:38
DRAWN BY: John Gordon		PAGE 17 OF 31		OP.DRW (605754, 524155)
APPROVED BY: <i>Zippy</i>		PROJECT:		NUMBER
				REV.

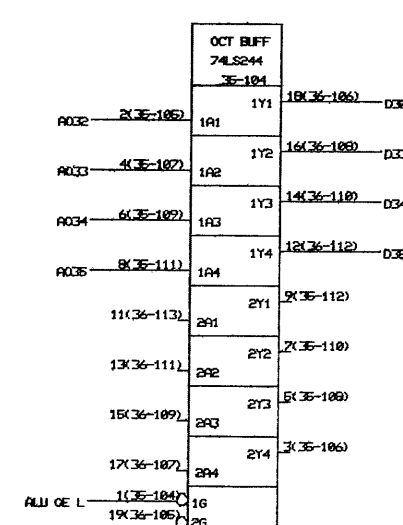
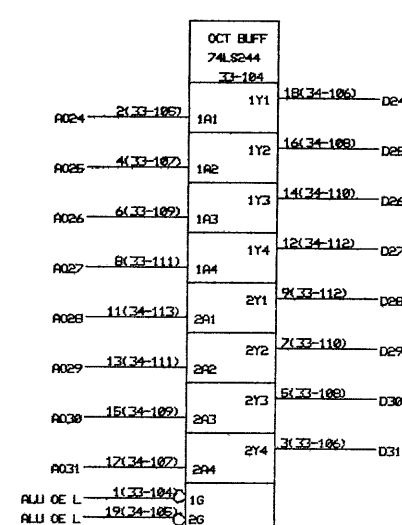
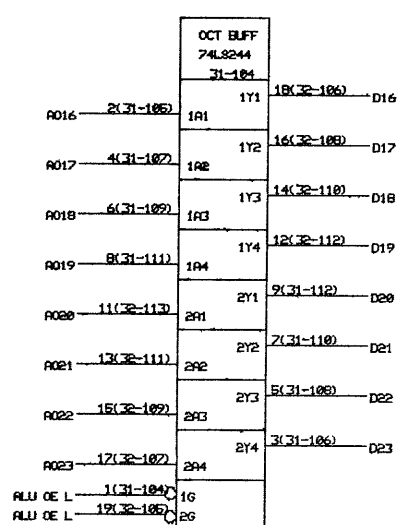
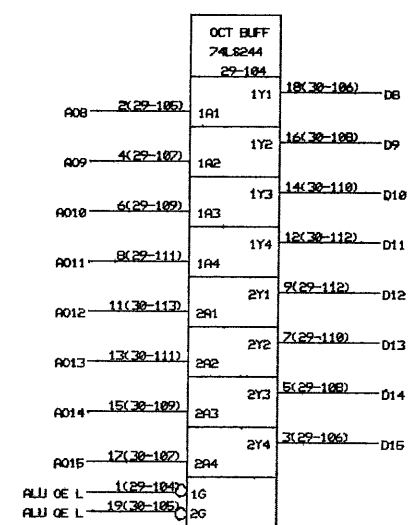
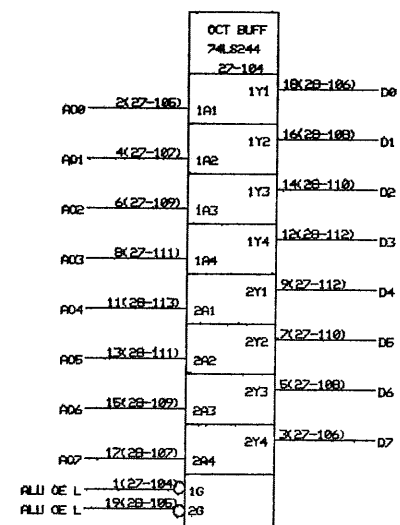
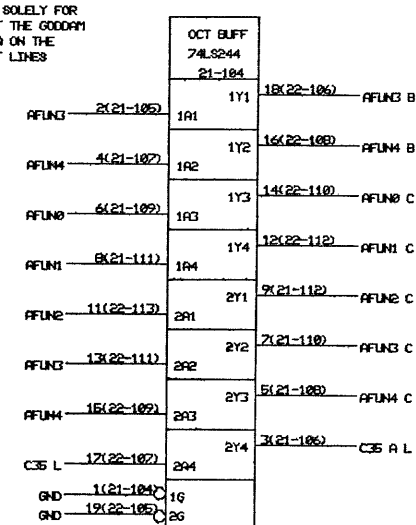


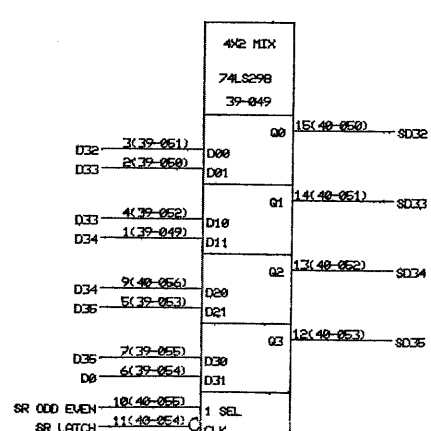
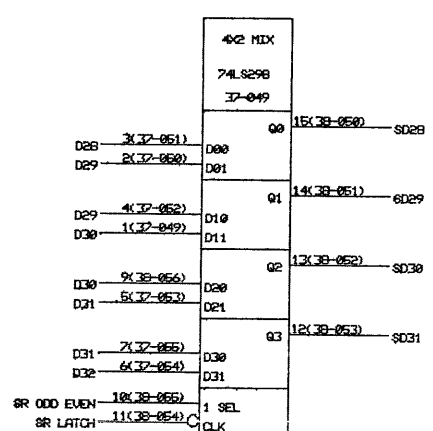
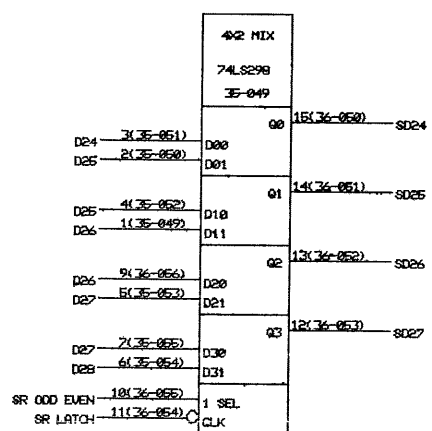
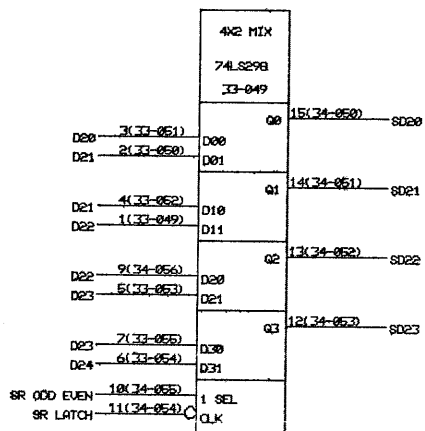
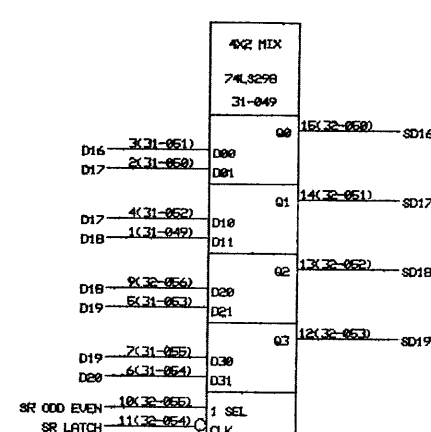
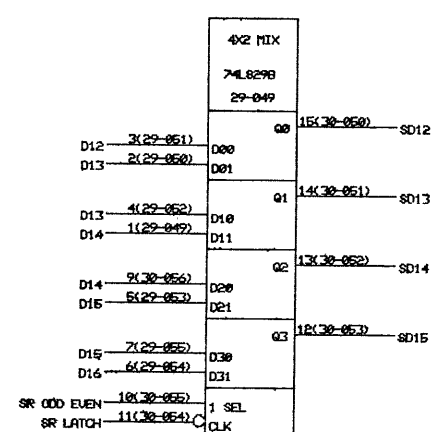
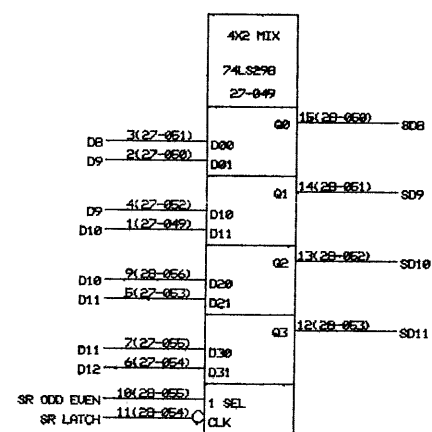
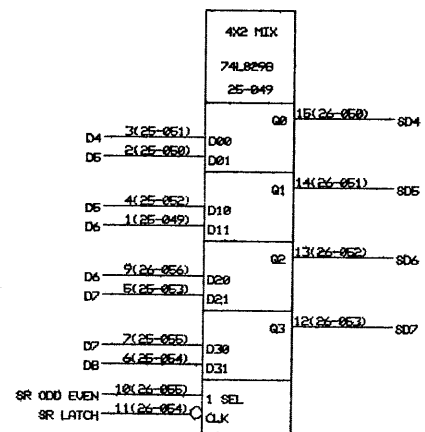
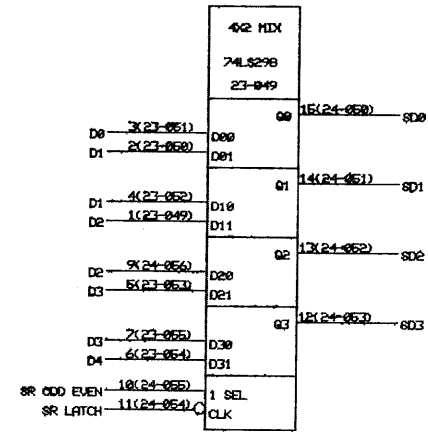
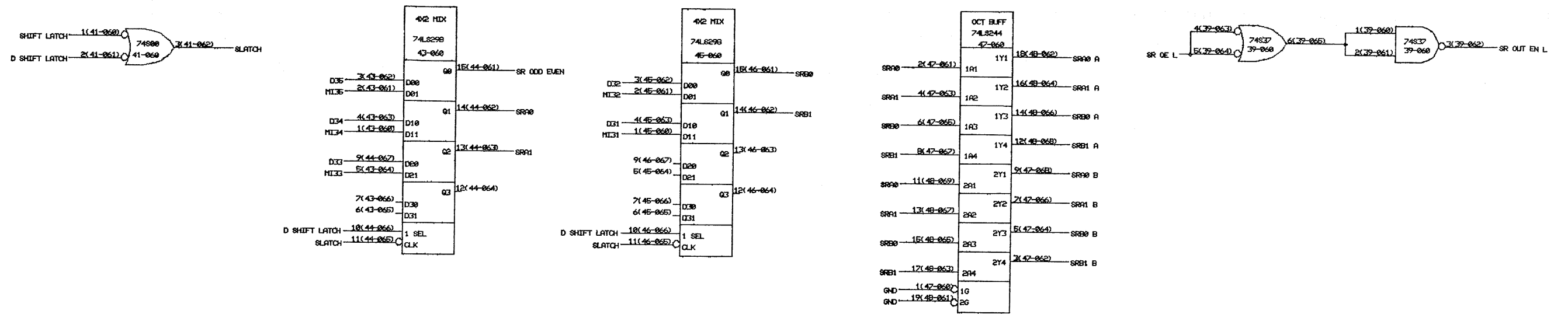
CCRMA Stanford University		POLY Interface A & B Registers, A=0 Logic		06-MAR-82 19:17	
DRAWN BY: John Gordon		PAGE 18 OF 31		NUMBER	
APPROVED BY: <i>Zippy</i>		PROJECT:		REV.	
ALUAB.DRW (605754, 524155)					

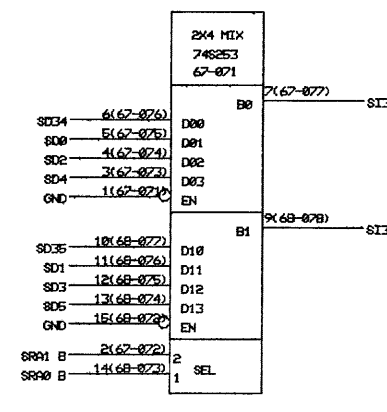
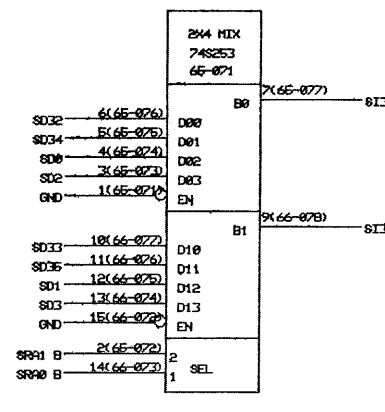
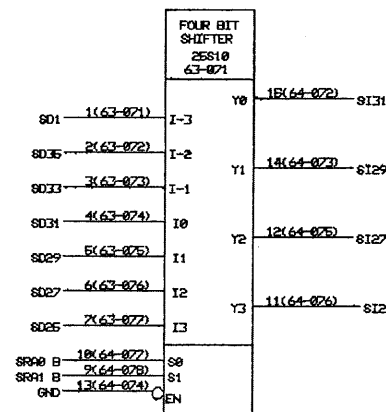
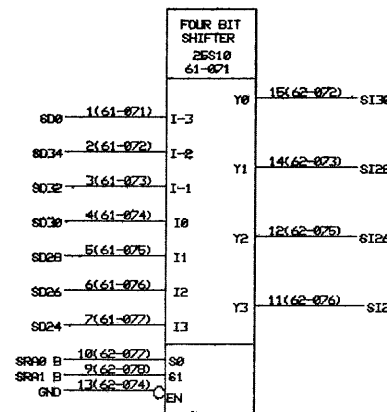
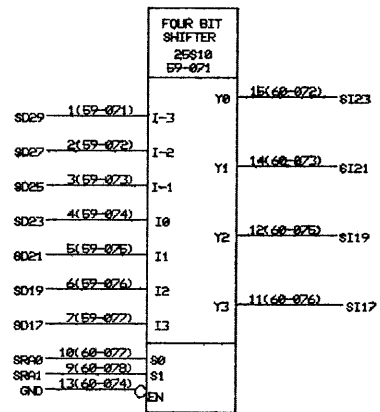
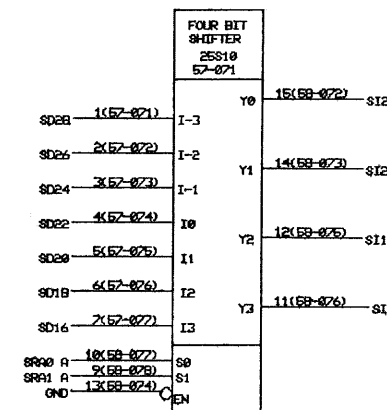
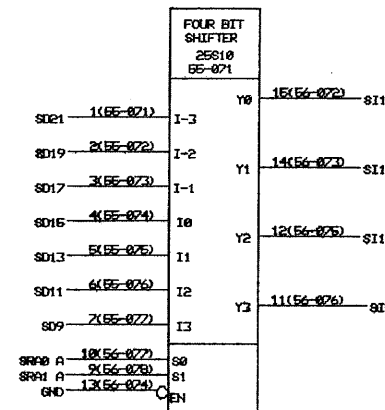
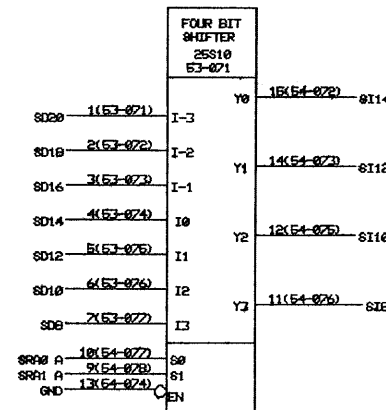
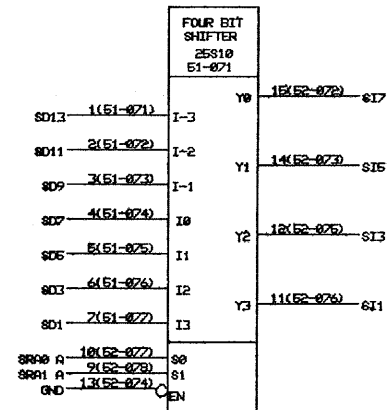
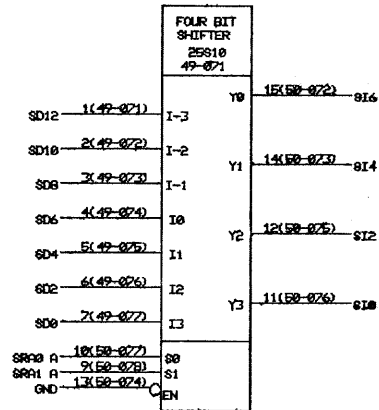




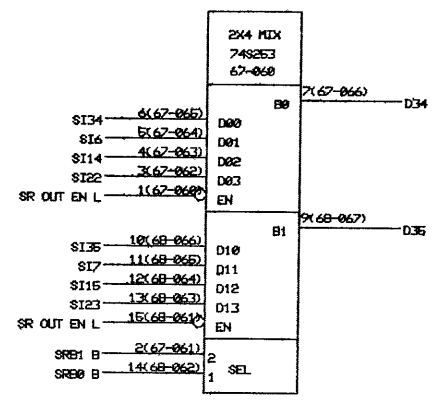
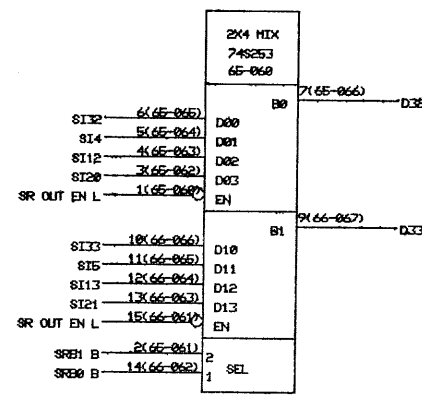
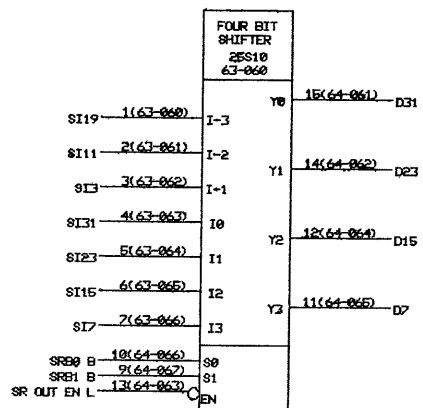
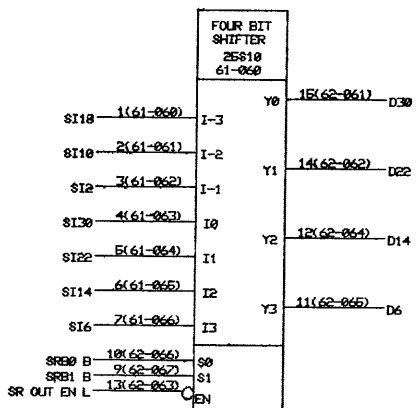
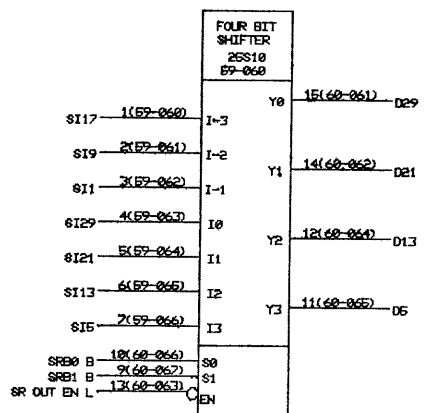
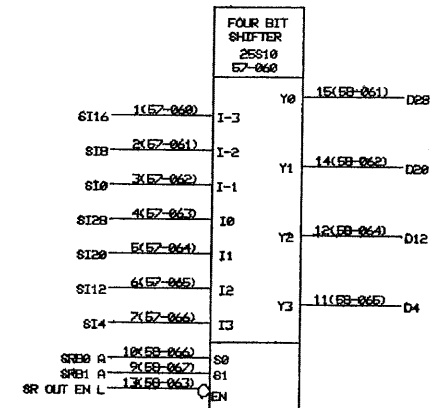
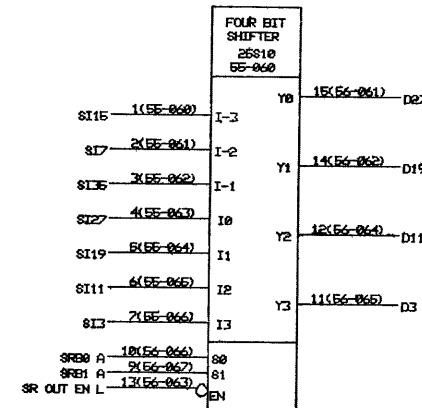
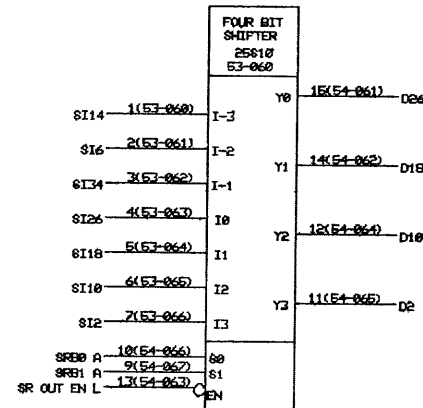
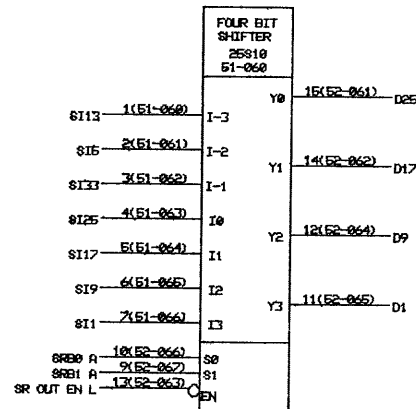
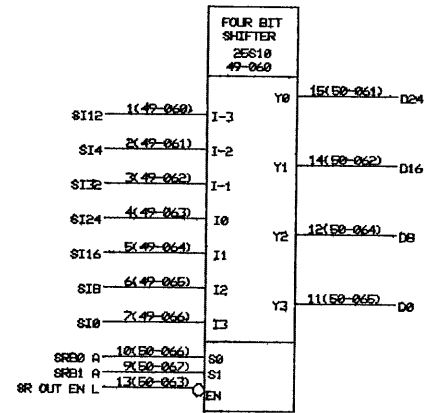
!! THESE ARE HERE SOLELY FOR THE REASON THAT THE GOODRAM 6181 DRAWS 8 MA ON THE FUNCTION SELECT LINES

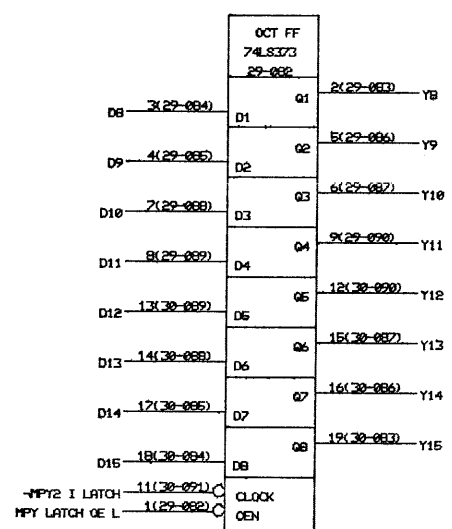
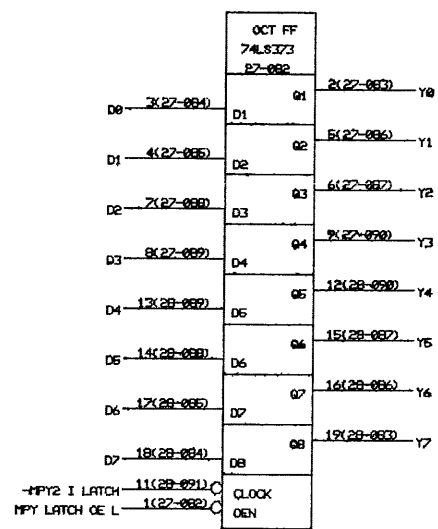
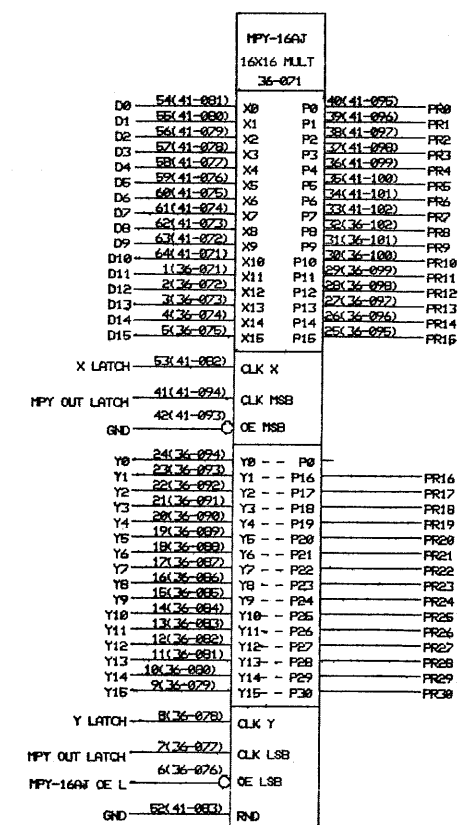
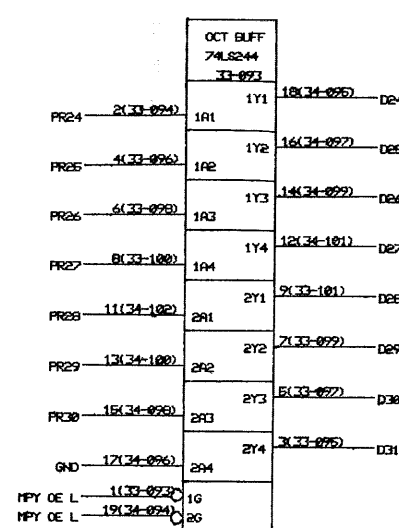
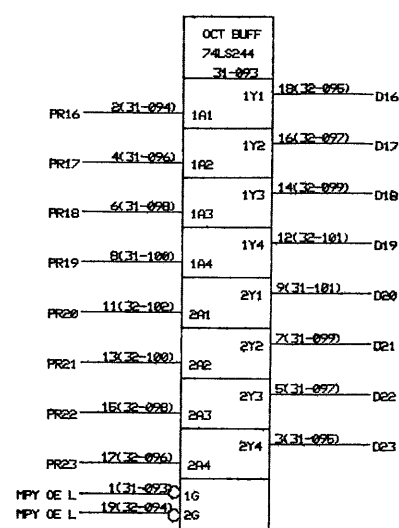
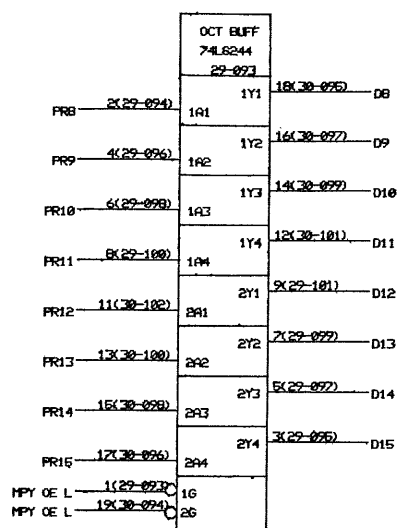
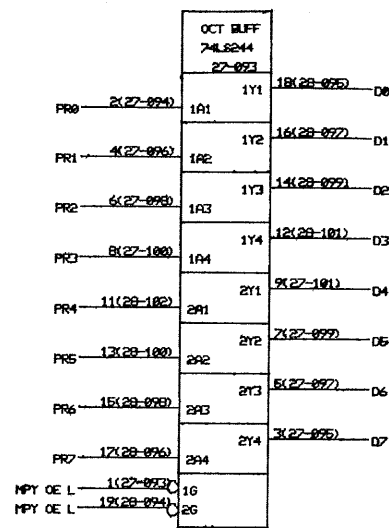




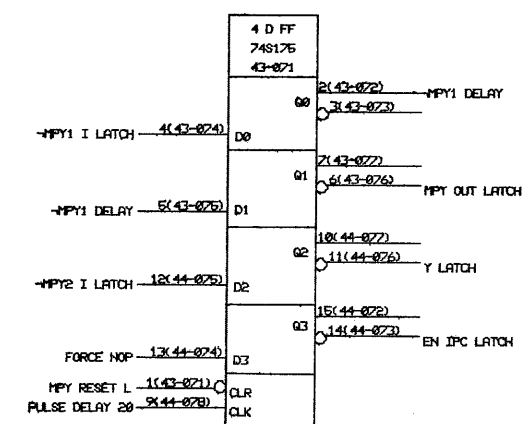
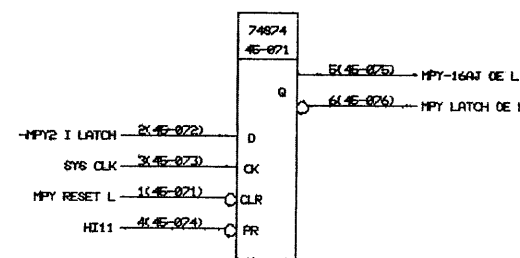


CCRMA Stanford University		POLY Interface Rotator, 0121416-bits shift		06-MAR-82 19:21
DRAWN BY: John Gordon		PAGE 22 OF 31		SHIFT2.DRW [605754,524155]
APPROVED BY: <i>Zippy</i>		PROJECT:		NUMBER
				REV.

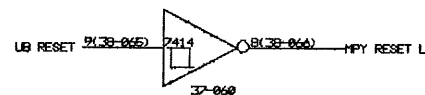
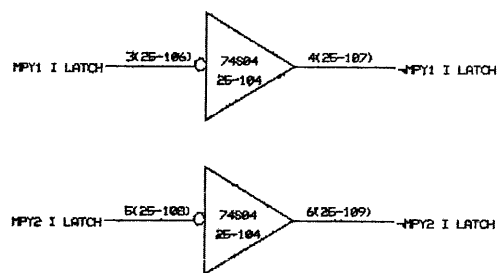




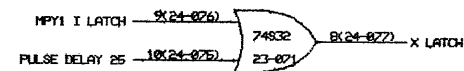
;;THE HPY OUTPUT ENABLE IS SLOW AND HAS VERY LITTLE POOP (4N4 OR SO). HENCE THE NEED FOR THE PR REGISTER.



;;THE LATCHING OF Y IS DELAYED BY 80 NS. HENCE THE NEED FOR ANOTHER Y REGISTER.



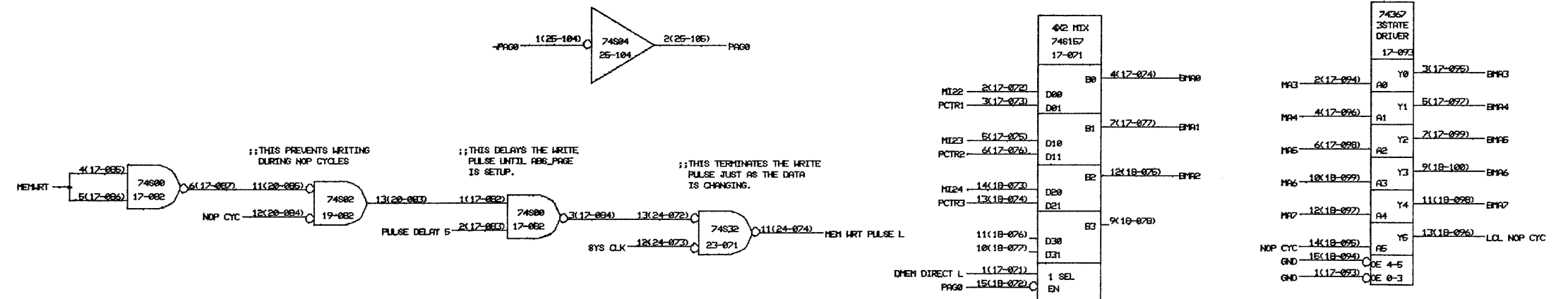
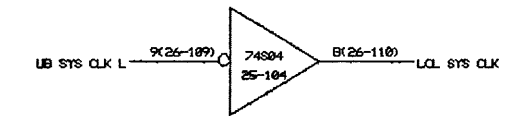
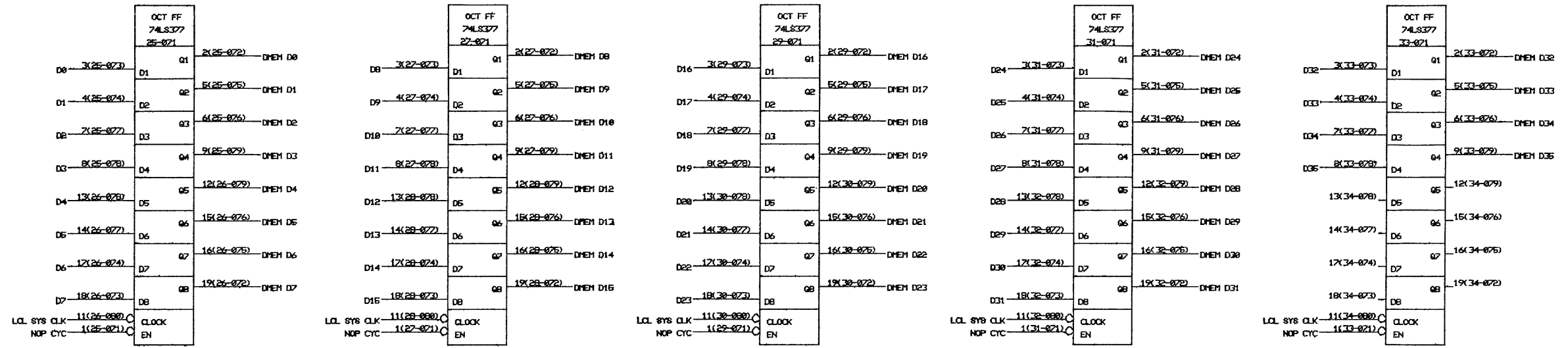
;;X GETS LATCHED DIRECTLY, BUT IT HAS TO GET LATCHED EARLY BECAUSE THE HPY NEEDS 20 NS HOLD TIME.



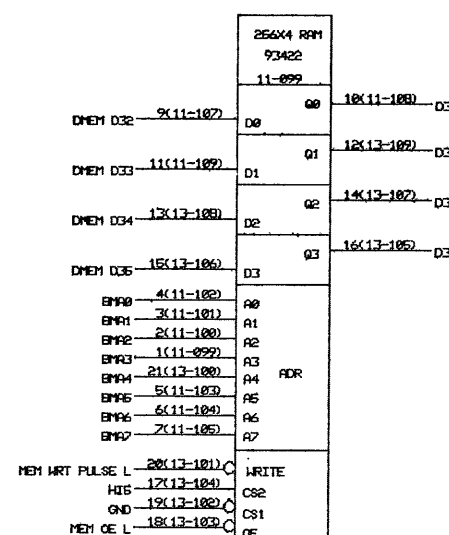
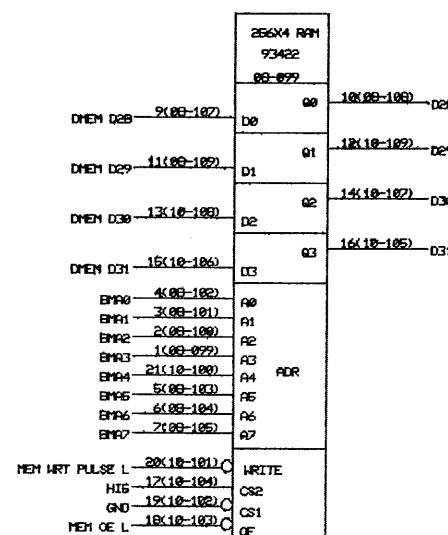
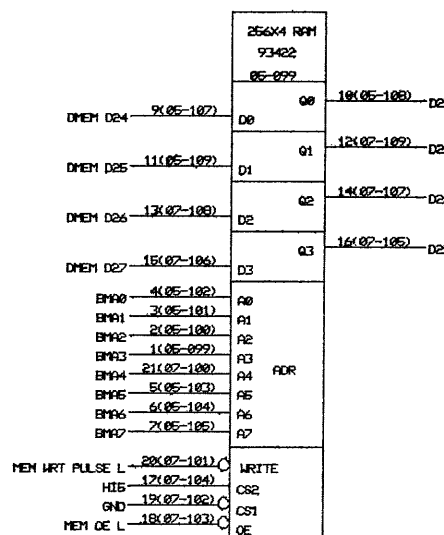
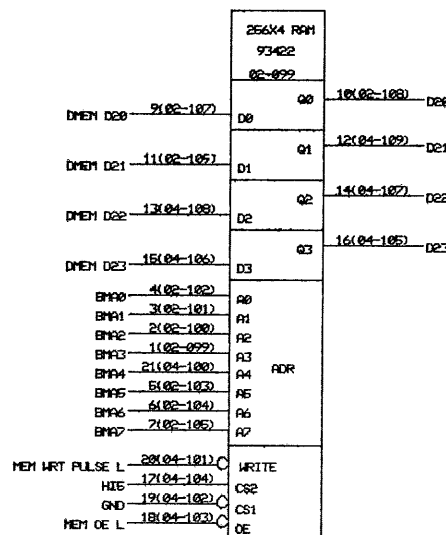
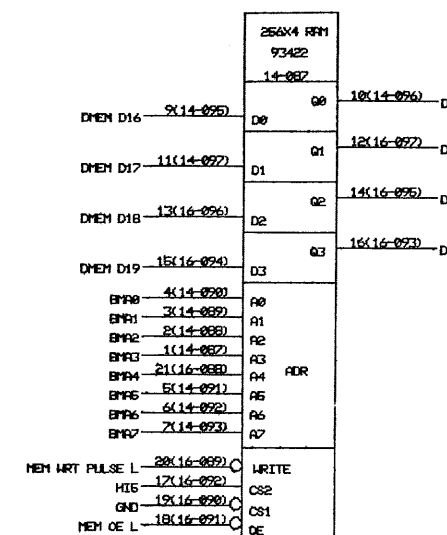
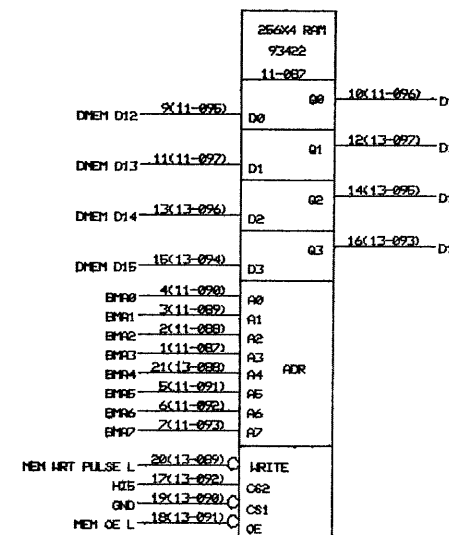
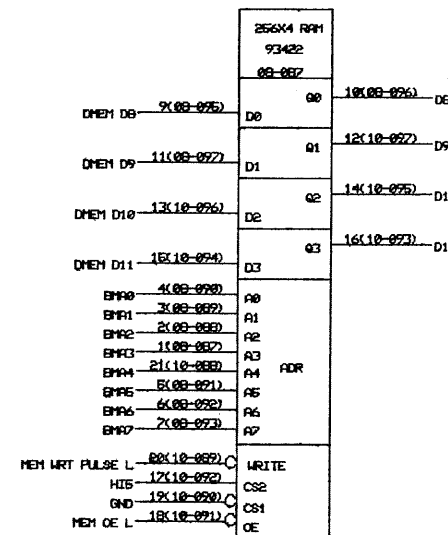
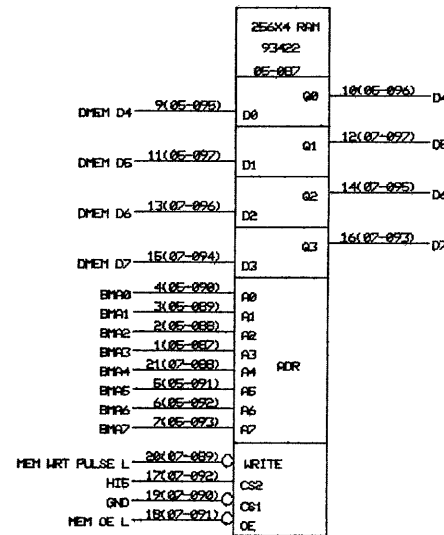
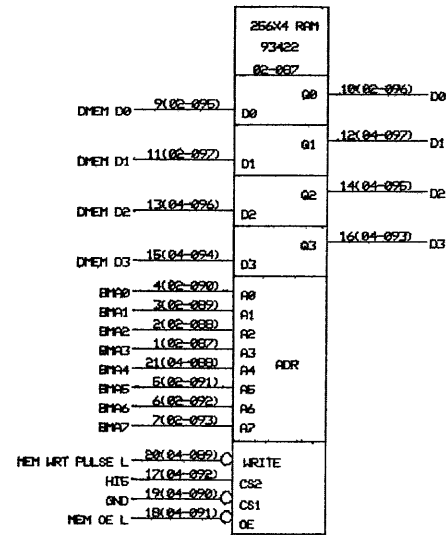
CCRMA Stanford University		POLY Interface Multiplier		02-FEB-83 13:16	
DRAWN BY: John Gordon		PAGE 24 OF 31		NUMBER	
APPROVED BY: <i>Zippy</i>		PROJECT:		REV.	

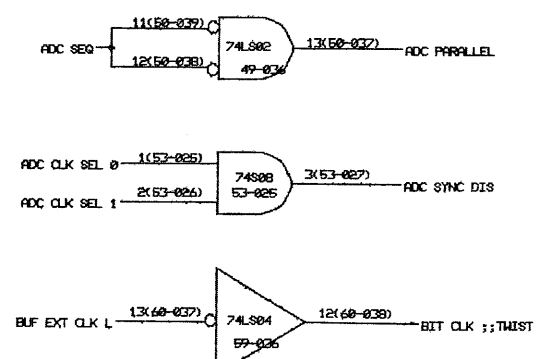
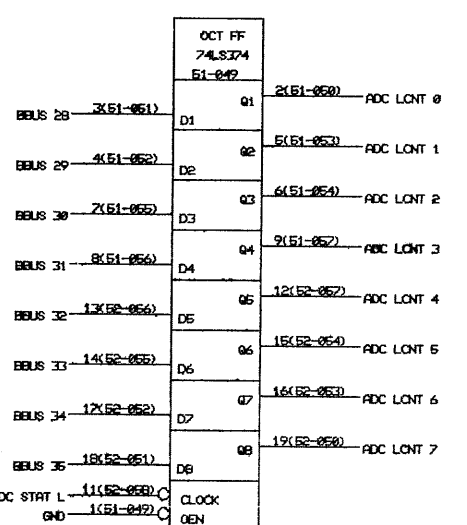
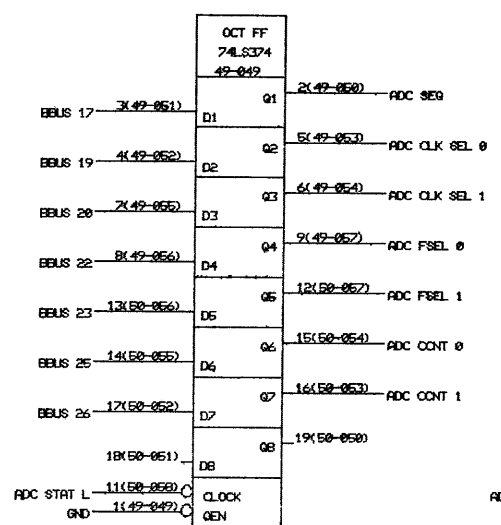
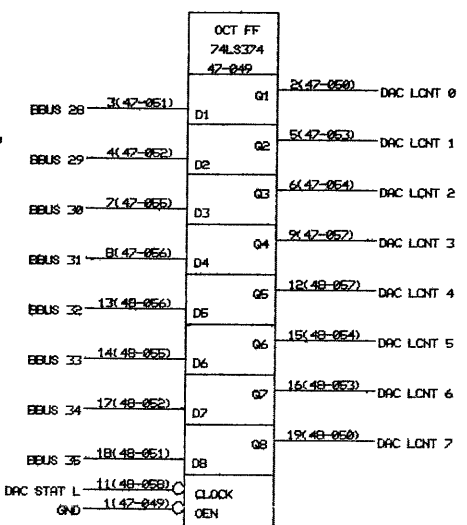
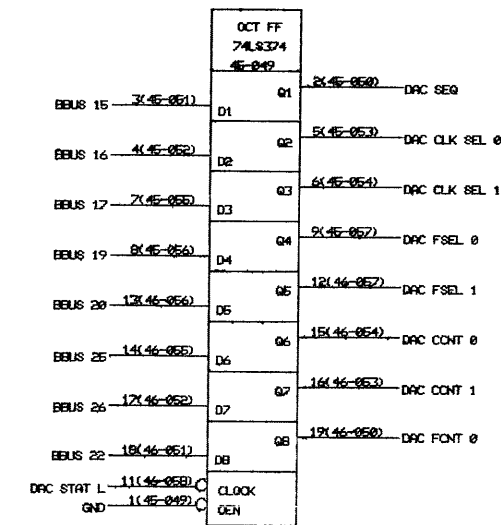
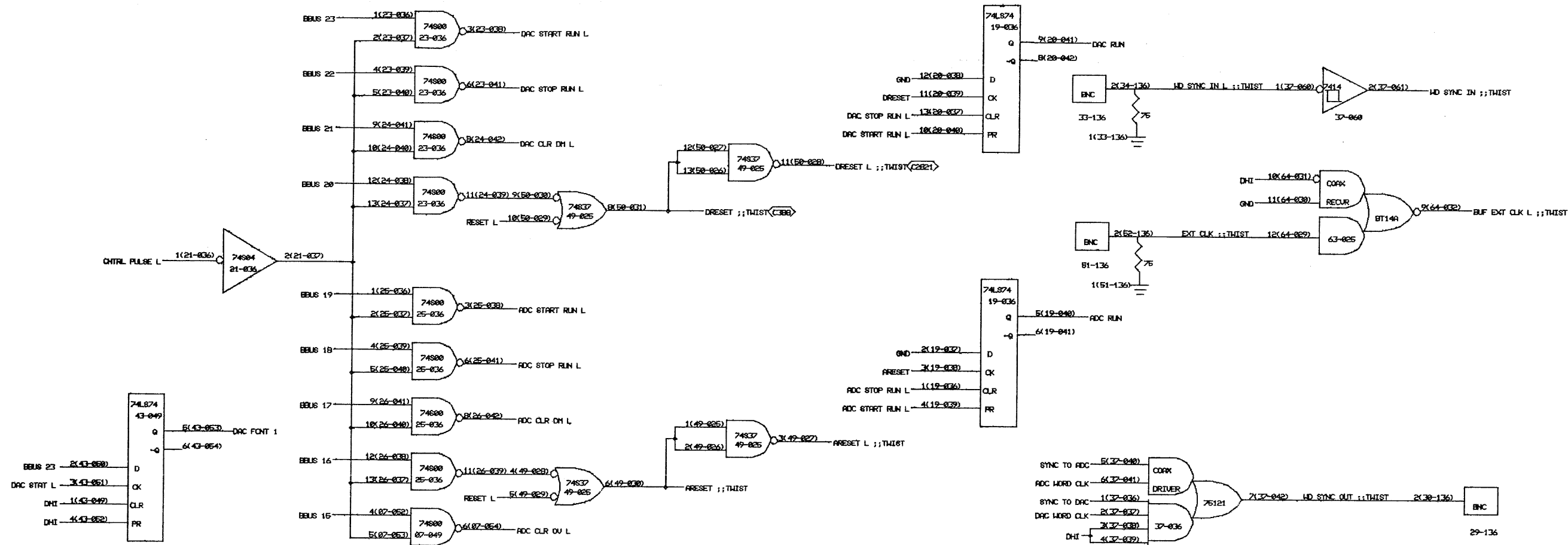
MPY.DRW (605754, 524155)

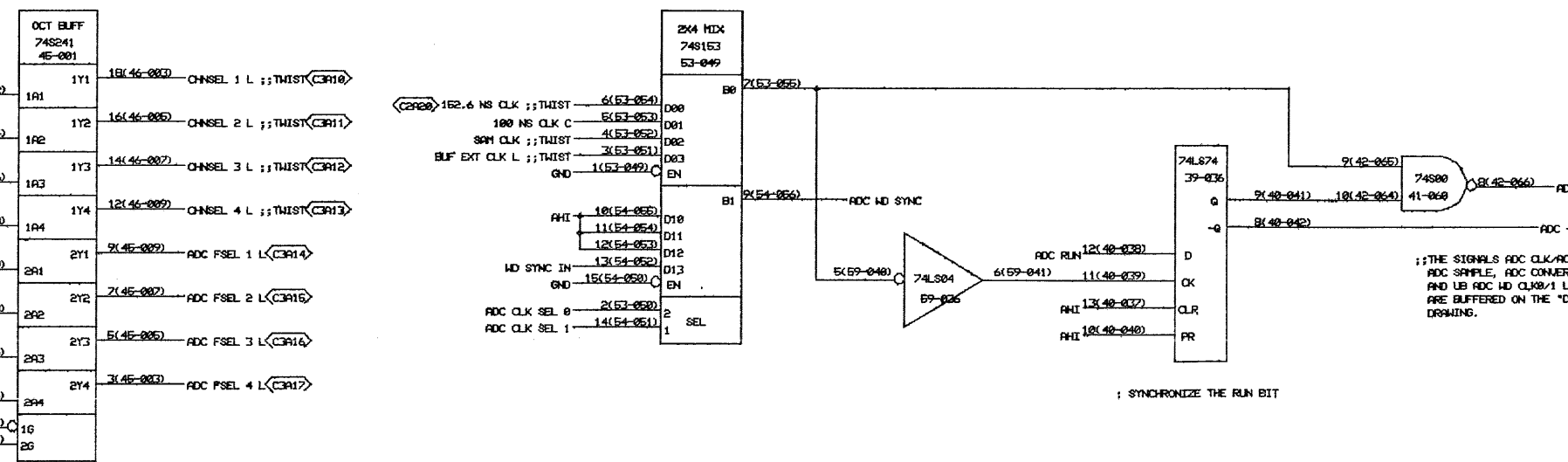
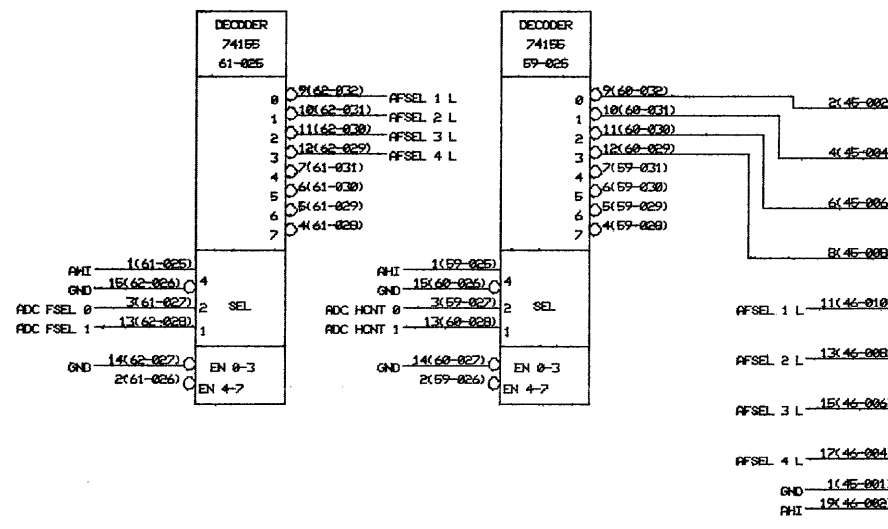
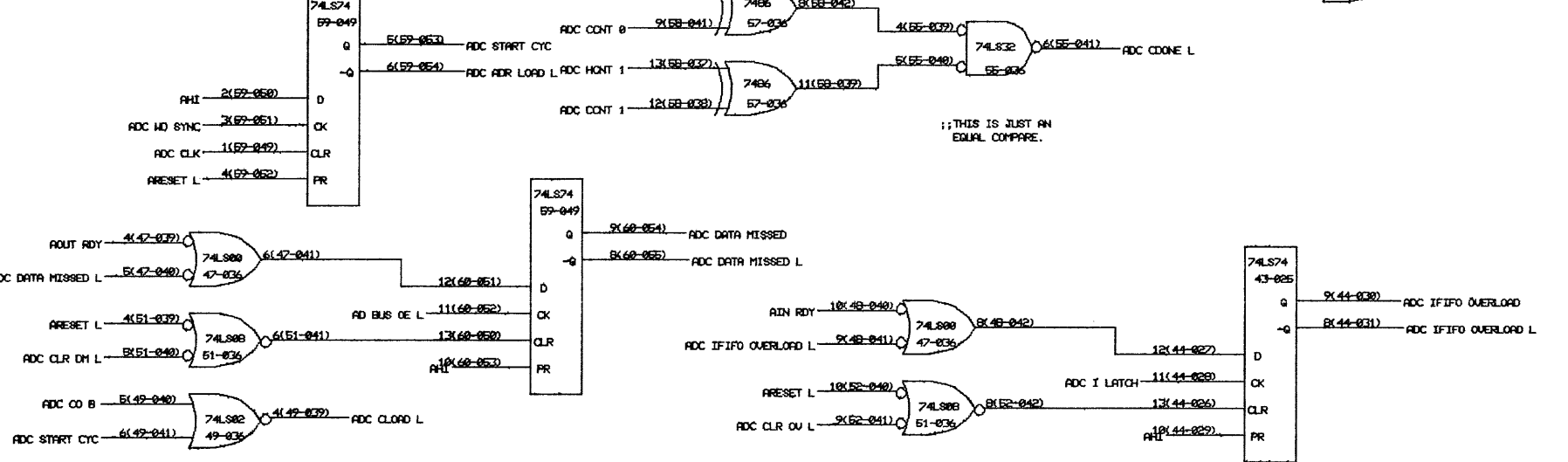
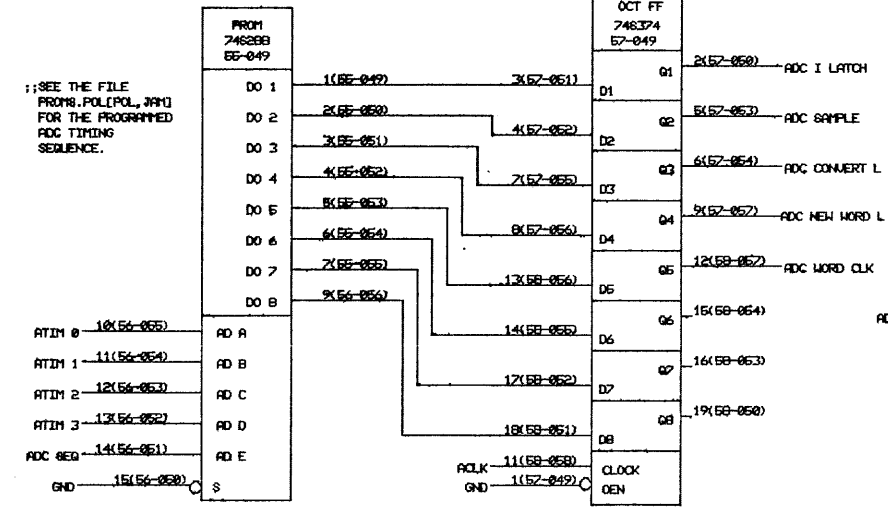
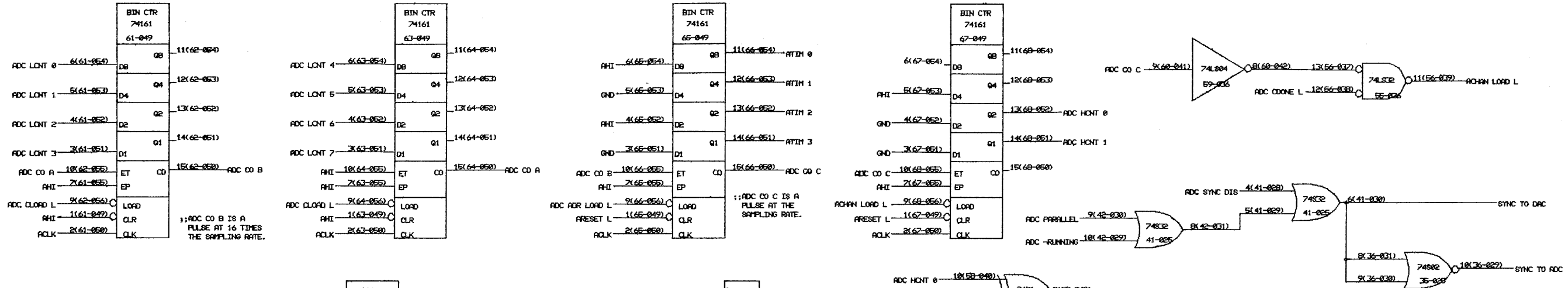


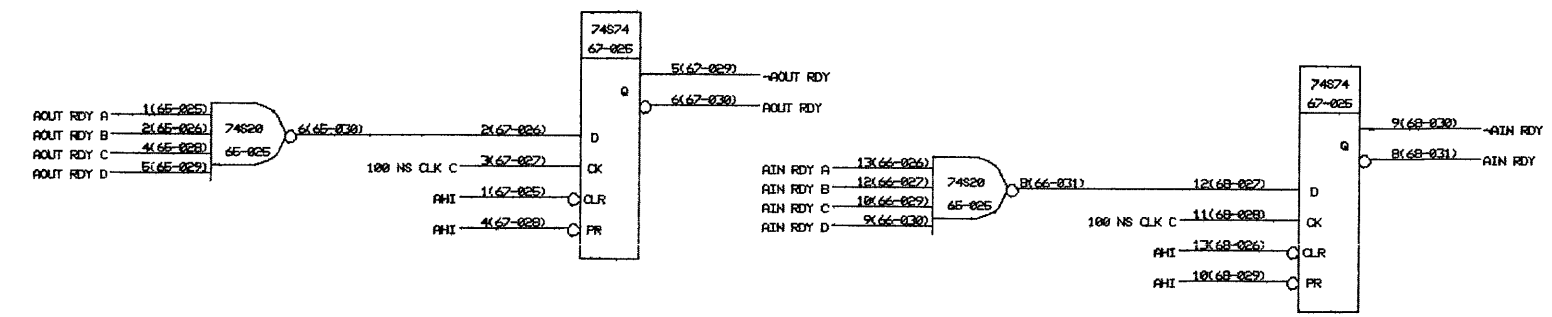
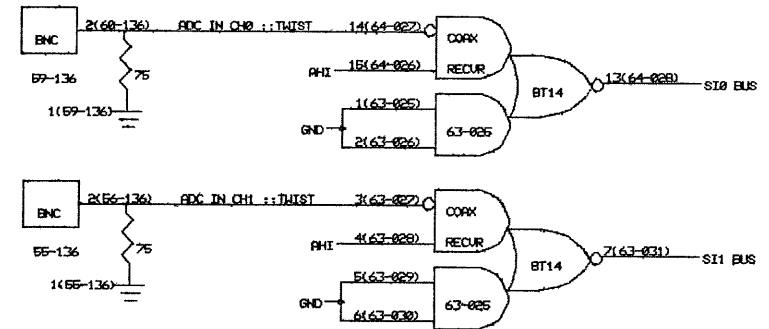
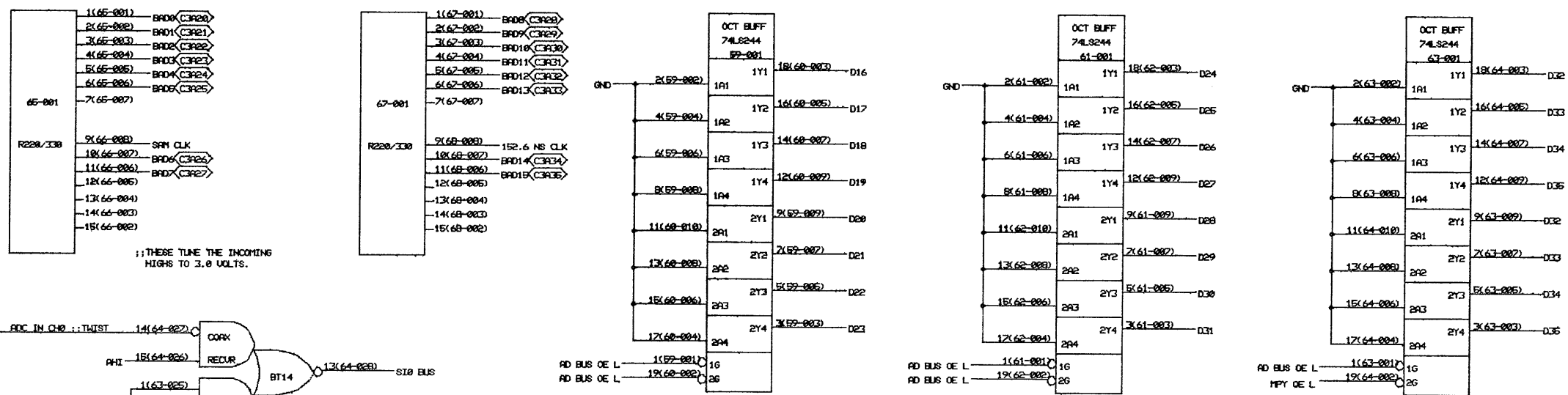
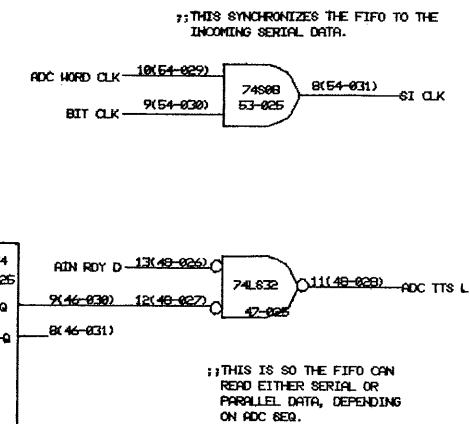
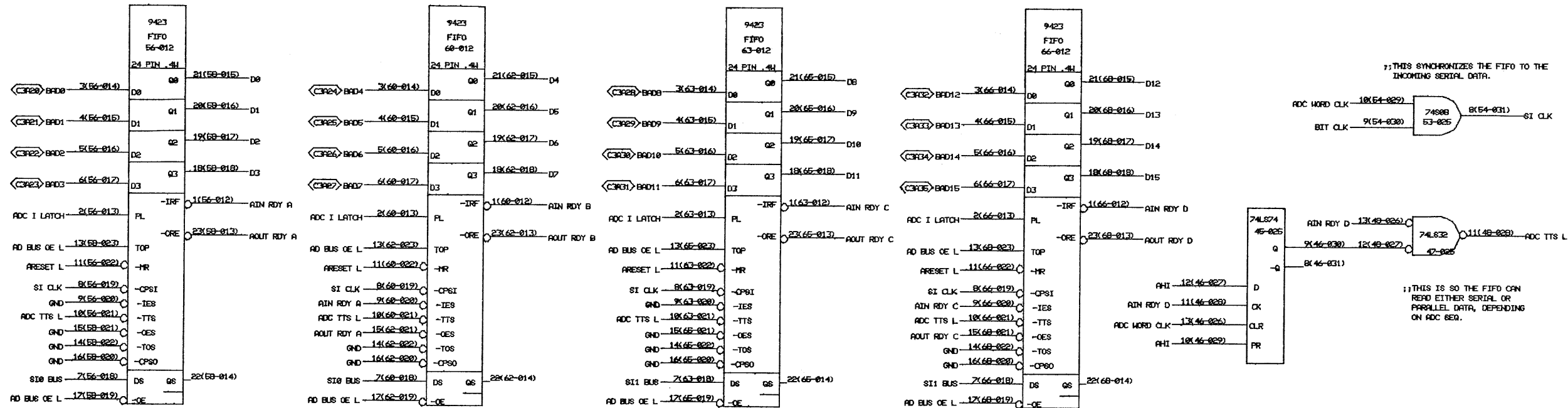


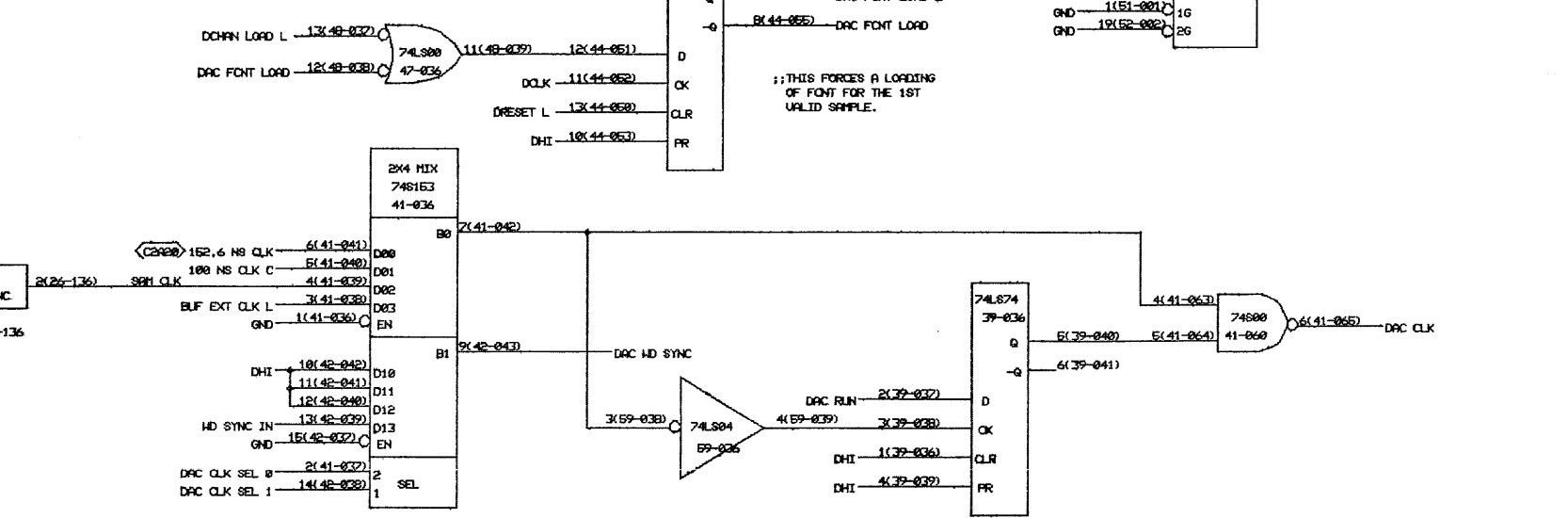
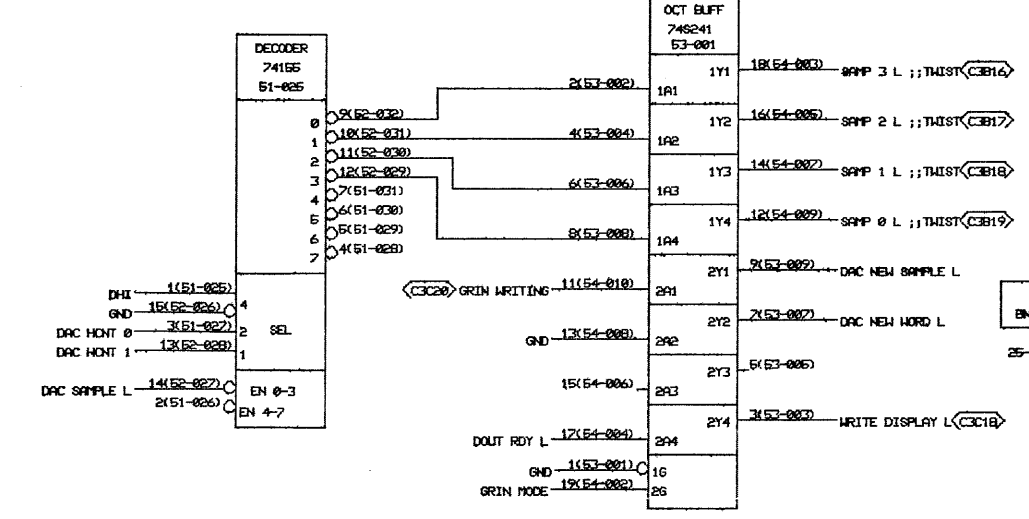
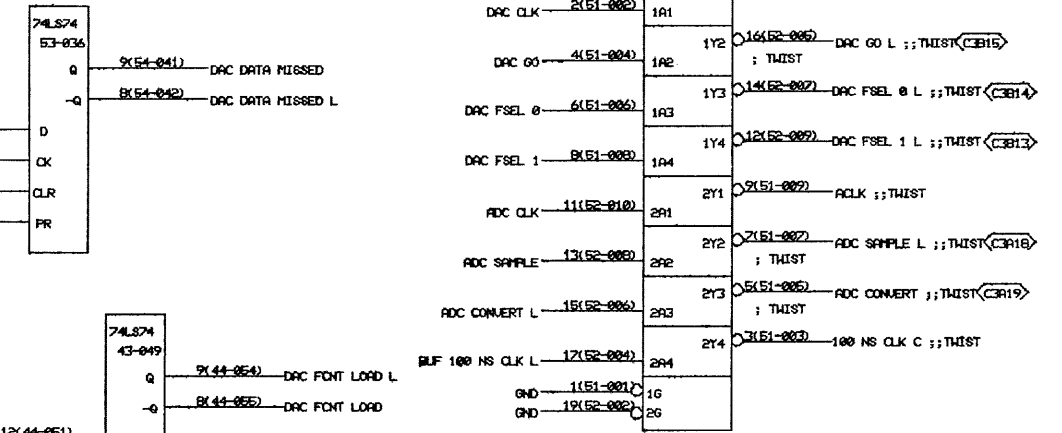
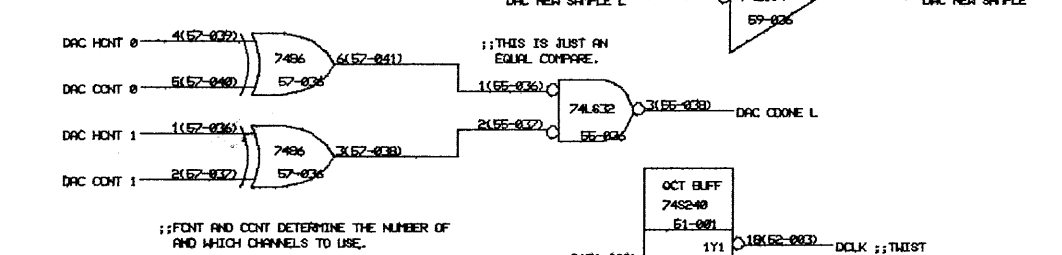
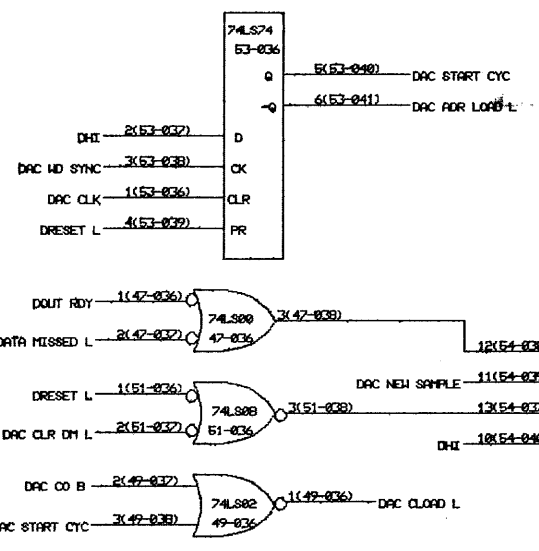
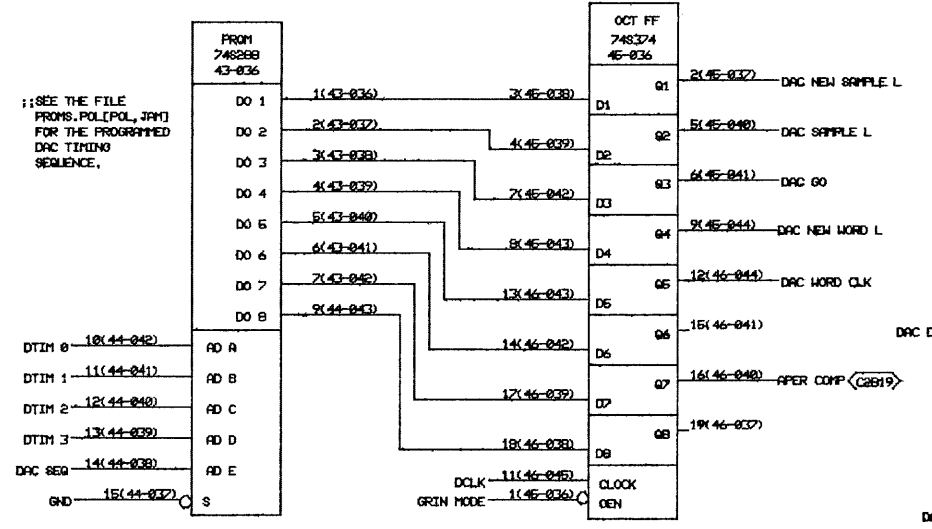
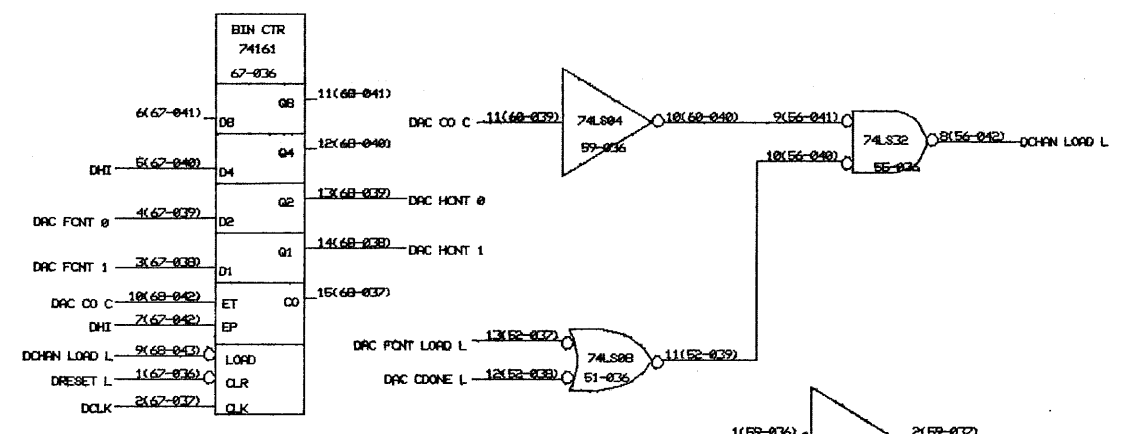
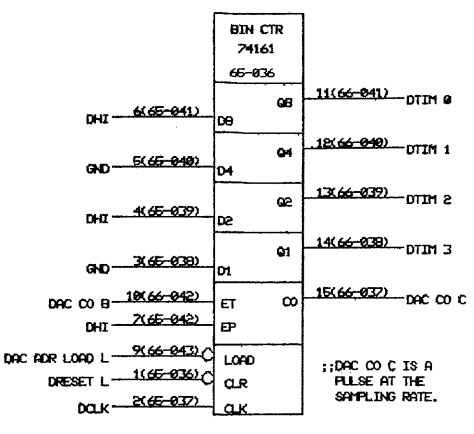
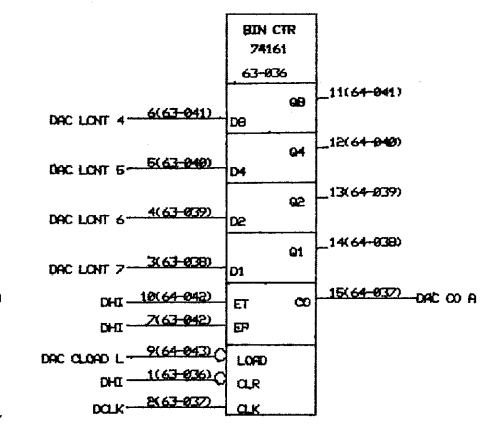
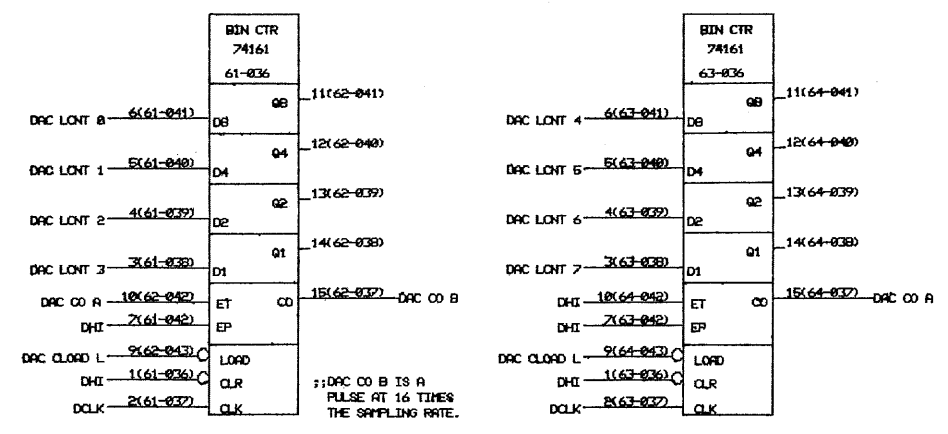
CCRMA Stanford University		POLY Interface DMEM Input & Control		25-MAY-82 15:33
				DMEM1.DRW [605754,524155]
DRAWN BY:	John Gordon	PAGE	25 OF 31	NUMBER
APPROVED BY:	<i>Zippy</i>	PROJECT:		REV.

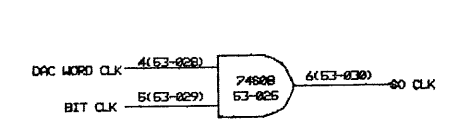
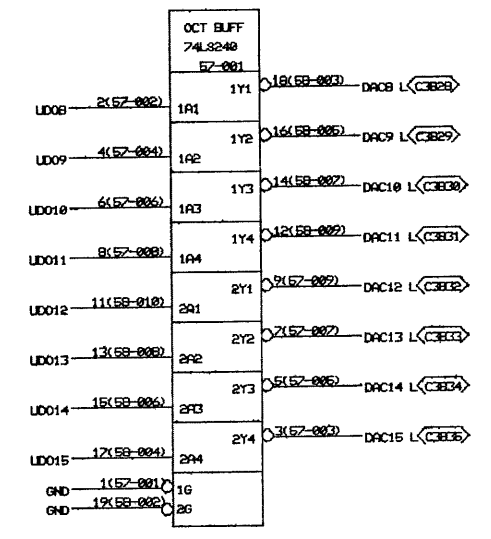
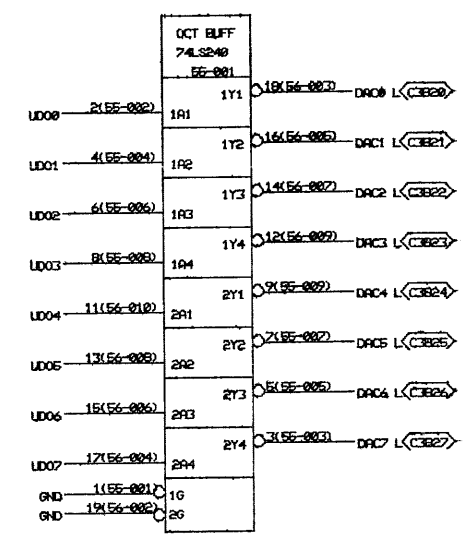
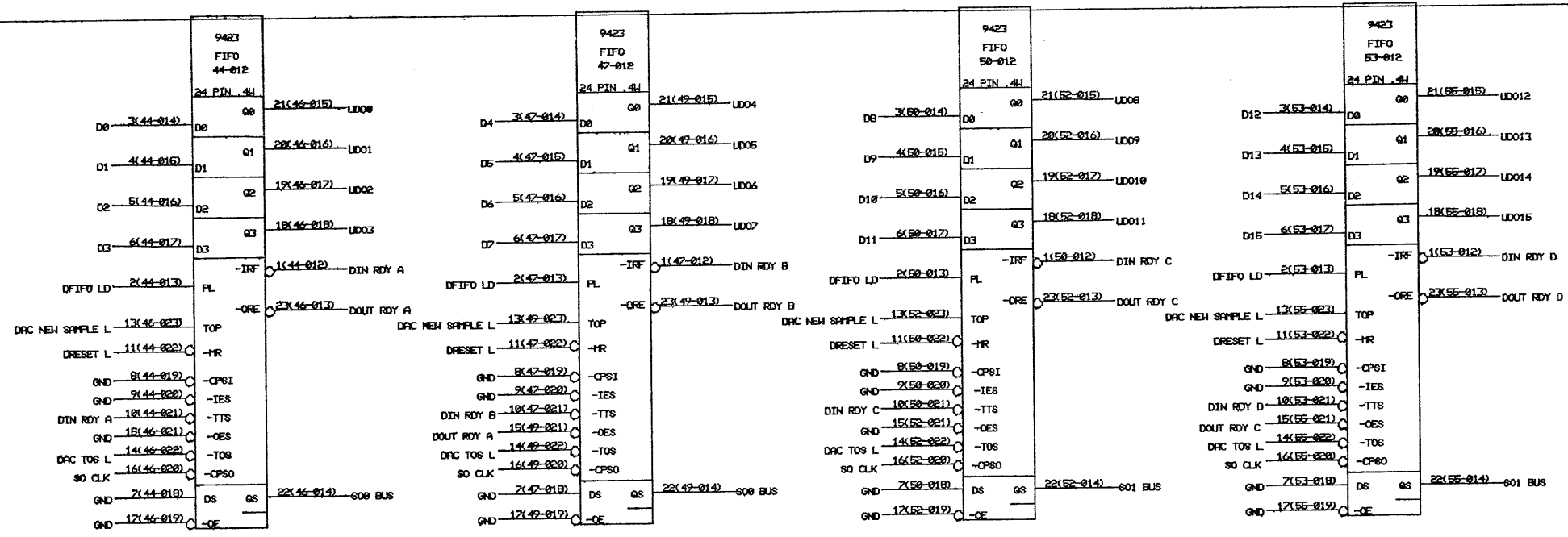




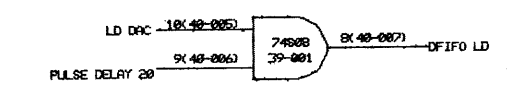
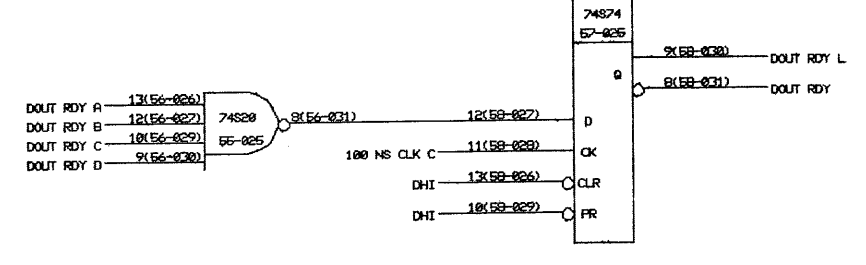
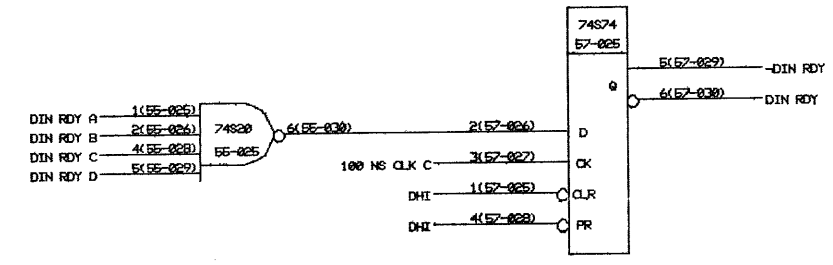
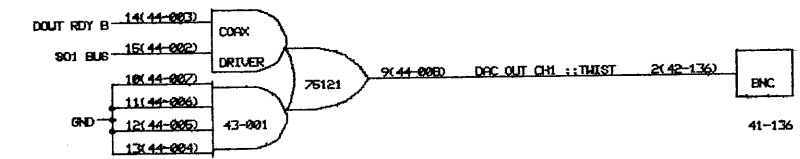
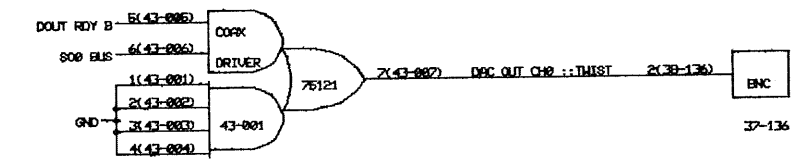
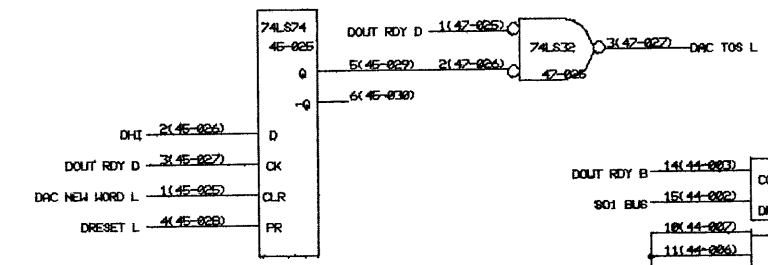






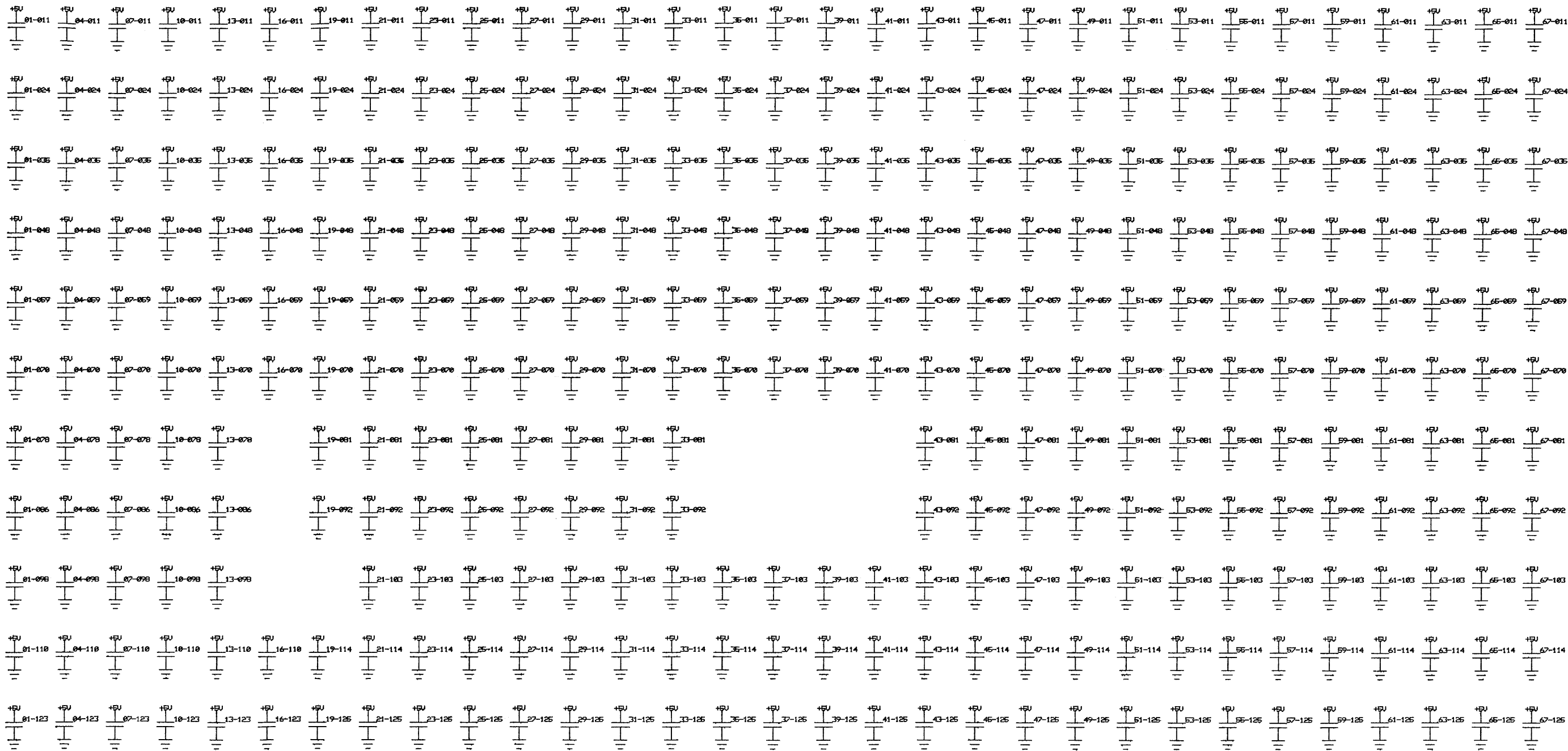


;; THIS IS SO THE FIFO CAN WRITE EITHER SERIAL OR PARALLEL DATA, DEPENDING ON DAC SEQ.



!! HAVE TO MAKE SURE DELAY IS VALID BEFORE LOADING, BECAUSE THE FIFO IS ONES CATCHING.

CCRMA Stanford University		POLY Interface DAC FIFO and Buffering		04-MAY-83 10:00	
				DFIFO.DRW [605754, 524155]	
DRAWN BY: John Gordon		PAGE 31 OF 31	NUMBER		REV.
APPROVED BY: <i>Zippy</i>		PROJECT:			



FOONLY, INC.		02-AUG-81 14:21	
DRAWN BY:		PAGE	OF
APPROVED BY: <i>Zippy</i>		NUMBER	REV.
PROJECT:			

CAPS. DRW [605754, 524155]